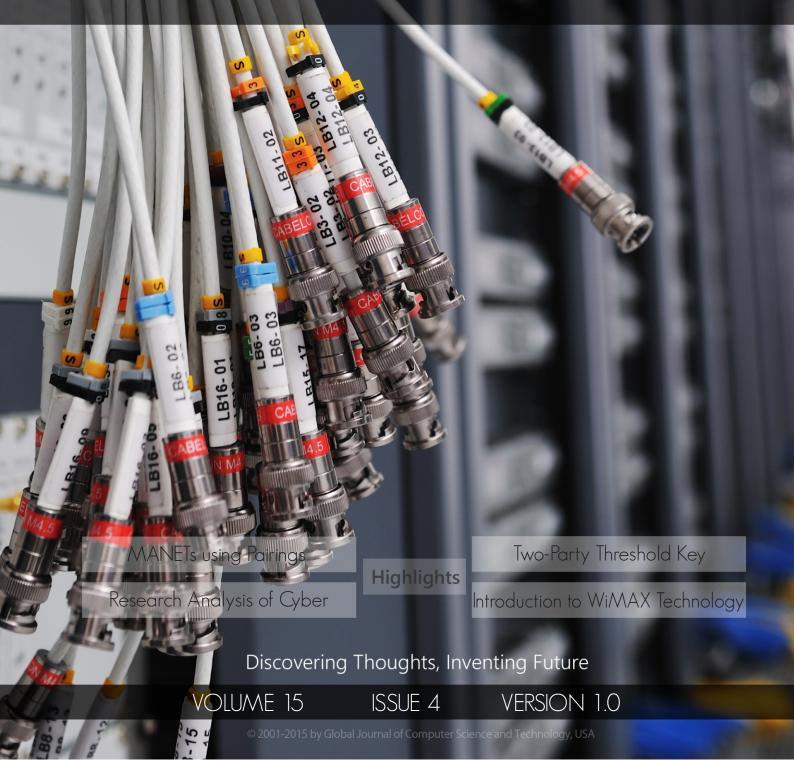
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# Network, Web & Security





#### GLOBAL JOURNAL OF COMPUTER SCIENCE AND TECHNOLOGY: E Network, Web & Security

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#### Contents of the Issue

- i. Copyright Notice
- ii. Editorial Board Members
- iii. Chief Author and Dean
- iv. Contents of the Issue
- 1. Two-Party Threshold Key Agreement Protocol for Manets using Pairings. 1-6
- 2. Research Analysis of Cyber Security. 7-10
- 3. Implementation of AES with Time Complexity Measurement for Various Input. *11-20*
- 4. A Cross Layer Model to Support Qos for Multimedia Applications on Wireless Networks. *21-31*
- 5. The Encryption Algorithm AES-RFWKIDEA32-1 based on Network RFWKIDEA32-1. *33-41*
- v. Fellows and Auxiliary Memberships
- vi. Process of Submission of Research Paper
- vii. Preferred Author Guidelines
- viii. Index



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# Two-Party Threshold Key Agreement Protocol for Manets using Pairings

By Ch. Asha Jyothi, G. Narsimha, J. Prathap & Gorti Vnkv Subba Rao

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Abstract- In MANET environment, the nodes are mobile i.e., nodes move in and out dynamically. This causes difficulty in maintaining a central trusted authority say Certification Authority CA or Key Generation Centre KCG. In addition most of cryptographic techniques need a key to be shared between the two communicating entities. So to introduce security in MANET environment, there is a basic need of sharing a key between the two communicating entities without the use of central trusted authority. So we present a decentralized two-party key agreement protocol using pairings and threshold cryptography ideas. Our model is based on Joux's three-party key agreement protocol which does not authenticate the users and hence is vulnerable to man-in-the-middle attack. This model protects from man-in-the-middle attack using threshold cryptography.

Keywords: pairing-based cryptography, threshold cryptography, bilinear maps, mobile ad hoc networks, key agreement protocol.

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## Two-Party Threshold Key Agreement Protocol for Manets using Pairings

Ch. Asha Jyothi<sup>a</sup>, G. Narsimha<sup>o</sup>, J. Prathap<sup>o</sup> & Gorti Vnkv Subba Rao<sup> $\omega$ </sup>

Abstract- In MANET environment, the nodes are mobile i.e., nodes move in and out dynamically. This causes difficulty in maintaining a central trusted authority say Certification Authority CA or Key Generation Centre KCG. In addition most of cryptographic techniques need a key to be shared between the two communicating entities. So to introduce security in MANET environment, there is a basic need of sharing a key between the two communicating entities without the use of central trusted authority. So we present a decentralized twoparty key agreement protocol using pairings and threshold cryptography ideas. Our model is based on Joux's three-party key agreement protocol which does not authenticate the users and hence is vulnerable to man-in-the-middle attack. This model protects from man-in-the-middle attack using threshold cryptography.

*Keywords:* pairing-based cryptography, threshold cryptography, bilinear maps, mobile ad hoc networks, key agreement protocol.

#### I. INTRODUCTION

'ireless technology [22] is suitable of communicating virtually every location on the plane of the earth. Most of the people exchange information every day using pagers, cellular telephones, laptops, several types of personal digital assistants (PDAs) and other wireless communication products. A Mobile Ad hoc NETwork (MANET) is one that comes into practice as needed, without the support of existing infrastructure or any other kind of fixed stations. MANET is an independent system of mobile hosts (also serving as routers), connected by wireless links. In a MANET, no infrastructure exists and the network topology may dynamically change in an unpredictable manner since nodes are free to move. The important natural characteristics of MANETs [22] include frequently changing Topology, Lack of Central Administration, Battery Power supply or Restricted Energy, Restricted bandwidth, Physical Security fear.

Ad hoc networks are particularly prone to malicious behavior. Lack of any centralized network management or certification authority makes these dynamically changing wireless structures extremely vulnerable to penetration, eavesdropping, interference,

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Author  $\omega$ : Vice Principal,Sree Dattha Institutions,Hyderabad. e-mail: gvnkvsubbarao@yahoo.com and so on. Security [22] is considered to be the major "barrier" in the commercial use of this technology. Security is indeed one of the most difficult problems to be solved in these networks due to lack of centralized network management. Most of the security mechanisms essentially require a secret key or session key or master key to be shared between the two communicating entities. So there is a need to share a key between the sender and receiver without the use of centralized network management or certification authority.

Kev agreement is one of the basic cryptographic essentials. This is needed in cases where two or more users want to communicate securely among themselves. The first two-party key sharing protocol was introduced by Diffie-Hellman. Since its detection in 1976, the Diffie-Hellman protocol [1] has become one of the most well-known and mostly used cryptographic primitive. In its basic version, it is an efficient solution to the problem of creating a common secret between two participants. Since this protocol is also used as a building block in many complex cryptographic protocols, finding a generalization of Diffie-Hellman would give a new tool and might lead to new and more efficient protocols. But this is an unauthenticated protocol in the sense that an adversary who has control over the communication channel can use the man-in-the-middle attack to share two separate keys with the two users, without the users being aware of this. In this paper, we present a secure two-party key agreement protocol that protects from man-in-themiddle attack. Our protocol is based on Joux's protocol [1] which in turn is the generalization of Diffie-Hellman protocol.

One round tripartite key agreement Joux's protocol [1] uses Weil and Tate Pairings and the idea of Diffie-Hellman. These pairings were first used in cryptology as cryptanalysis tools to decrease the complexity of the discrete logarithm problem on some "weak" elliptic curves, but they are also used today to build cryptographic systems.

In this paper, we present a secure two-party key agreement protocol for MANET environment. This model extends the popular known Joux's tripartite key agreement protocol [1] to two-partite with minor modifications. Similar to Joux model [1], this model uses pairings or bilinear maps, unlike Joux this model uses threshold cryptography. Recently Pairing-based

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cryptography in the form of Identity-based cryptography has become a highly working research issue.

The paper is organized as: Section II discusses on the background fundamentals needed to understand the proposed model. Section III discusses on the previous work done to share a key between two entities using pairings. Section IV talk about the detailed description of the proposed model. Section V gives the software implementation of the proposed model and Section VI confers the conclusion and future enhancements that can be done to improve the model.

#### II. Preliminaries

#### a) Bilinear Maps

The bilinear map was proposed originally as a tool for attacking elliptical curve encryption by reducing the problem of discrete algebra on an elliptical curve to the problem of discrete algebra in a finite field, thereby reducing its complexity. However, this method has been used recently as an encryption tool for information protection, instead of an attacking tool. Bilinear pairing is equivalent to a bilinear map.

Consider two additively written abelian groups A1 and A2; the identity element being 0. Also consider a multiplicatively written cyclic group C; the identity element being 1. A pairing [2][17] on  $A_1$ ,  $A_2$  and C is a non-degenerate, bilinear map

 $e:A_1\times A_2\to C.$ 

A bilinear pairing e is a function which maps a pair of points on an elliptic curve E, defined over fields A1 and A2, to an element of the multiplicative group of a finite extension field C. This mapping is said to be pairing as it maps a pair of elliptic curve points. The pairing e has the following characteristics:

*Non-degenerate:* Given a point  $\mathcal{O} \neq X \in A_1$  there exists a point  $Y \in A_2$  such that  $e Y \in A_2$ ; Where  $\mathcal{O}$  is the point at infinity on the elliptic curve over the finite field A1.

 $\begin{array}{l} \textit{Bilinear: for all points } X, X_1, X_2 \in A_1 \textit{, and } Y, Y_1, Y_2 \\ \in A_2 \textit{ and } u, v \in Z \textit{ we have} \end{array}$ 

$$\begin{aligned} & e(X_1 + X_2, Y_1) = e(X_1, Y_1) e(X_2, Y_1), \\ & e(X_1, Y_1 + Y_2) = e(X_1, Y_1) e(X_1, Y_2). \end{aligned}$$

This can be redefined in the following way:

 $e([u]X, [v]Y) = e(X, Y)^{uv} = e([v]X, [u]Y;.$ where [u]X = X + X + ..+ X (u times)

Computable: There exists a computationally efficient algorithm to find e(X, Y) for all  $X \in A_1$  and  $Y \in A_2$ .

Laws of Bilinear Pairings: The following equations holds good for the bilinear pairing e. Consider  $X \in A_1$ , and  $Y \in A_2$  and  $u, v \in Z$  and  $\mathcal{O}$  is the point at Infinity.

$$\begin{split} & e(X, \, {\mathcal O}) = e({\mathcal O}, \, Y) = 1 \\ & e(\text{-}X, \, Y) = e(X, \, Y)^{\text{-}1} = e(X, \, \text{-}Y) \\ & e([u]X, \, Y) = e(X, \, Y)^u = e(X, \, [u]Y) \\ & e([u]X, \, [v]Y) = e(X, \, Y)^{uv} \end{split}$$

Some of the examples of cryptographic bilinear maps are Weil Pairing [11] and Tate Pairing [5]. Pairings in elliptic curve cryptography are functions which map a pair of elliptic curve points to an element of the multiplicative group of a finite field.

There are two types of pairings commonly used in the cryptography literature. The first type of pairing called Symmetric Pairings are of the form

 $e: A_1 \times A_1 \rightarrow C$ , where  $A_1$  and C are cyclic groups of prime order p written additively and multiplicatively respectively.

The second type of pairing called Asymmetric Pairings are of the form

 $e: A_1 \times A_2 \rightarrow C$ , where A1, A2 are additively written cyclic groups of prime order p and C is a multiplicatively written cyclic group of prime order p.

The first form is just the special case with A2= A1. Asymmetric Pairings are further divided into two types and hence leading to totally three types of Pairings [19]

*Type 1:*  $A_1 = A_2$  Symmetric Pairing;

Type 2 :  $A_1 \neq A_2$  Asymmetric Pairing but there is an efficiently computable homomorphism function  $\Psi$ :  $A_2 \rightarrow A_1$ ;

*Type 3* :  $A_1 \neq A_2$  Asymmetric Pairing and there are no efficiently computable homomorphism functions between  $A_1$  and  $A_2$ .

#### b) Threshold Cryptography

Let t and n be positive integers,  $t \le n$ . A (t, n)threshold scheme [25] is a method of sharing a secret K among a set of n participants in such a way that any t participants can compute the value of the secret, but no group of t-1 or fewer can do so.

Let the set of participants be denoted by E. The value of the secret K is chosen by the dealer, denoted D, who is a special participant not in E. When D wants to share the secret K among the participants in E, D gives each participant some partial information, called a share. The shares are distributed secretly, so no participant knows any other participant's share.

At a later time, when some qualified subset of participants  $F \subseteq E$  want to compute the secret K, they will then pool their shares together. The most famous construction of a (t, n)-threshold scheme, called the Shamir Threshold Scheme [18][21], is invented in 1979. Therefore, a (t, n) threshold secret sharing scheme can protect the secret against an adversary who can intercept at most t – 1 paths. In the proposed model D don't want to share the secret K among several participants in E, but D wants to share the key with the other end of communication say G, with whom he wants a secure communication. So D sends the shares of the secret key K through n independent paths [24] to G. When G receives at least t shares, he can recover the secret and there by a key is shared between D and E.

The opponent is facing the challenge of getting at least t shares by intercepting t paths at the same time, unless until he cannot recover the secret key.

#### III. Related Work

There are many key agreement protocols based on bilinear maps, and later most of them have been broken. One of the first applications of pairing based cryptography was a tripartite key agreement protocol given by Joux [1]. This key agreement protocol does not authenticate the users, and thus is subject to the attack namely man-in-the-middle. Of course, it was an important step in the advancement of pairing based cryptography. This protocol only uses pairings especially Tate pairing but does not use identity-based cryptography.

Many key agreements from bilinear maps and identity based cryptography have been since proposed. Scott [7], Smart [8], and Chen and Kudla [6] have proposed two-party key agreement protocols, none of which have been broken. All of these schemes require that all parties involved in the key agreement are clients of the same Key Generation Centre (KGC). Nalla recommends a tripartite identity-based key agreement in [9], and Nalla and Reddy recommends a authenticated tripartite identity-based key agreement scheme in [10], but both have been broken down [12, 13]. Shim presents two key agreement protocols [14, 15], but both of these schemes have been broken by Sun and Hsieh [16]. Another authenticated tripartite key agreement protocol recommended by Al-Riyami and Patterson [3] was broken by Shim [4]. Cullagh and Barreto recommend a two-party identity based authenticated key agreement. Most of the above protocols are based on identity-based cryptography.

Our proposed model is based on Joux's Protocol [1]. It uses bilinear maps (Pairings) and Threshold cryptography concepts. It does not uses Identity based cryptography(IDC) because IDC needs the use of Key Generation Centre (KCG), a centralized controller and which is infeasible in MANETs environment.

#### a) Joux's Protocol

Joux Protocol [1] considers the three communicating parties A, B and C want to share a secret key KABC among them. Let A, B and C chooses random integers u, v, and  $w \in \mathbb{Z}_q^{\bullet}$  respectively. Consider the Symmetric Pairing e:  $A_1 \times A_1 \rightarrow C$  and P is the generator of the cyclic group A1 publicly known. The Protocol continues as follows and shown in Fig. 1:

1.	$A \rightarrow B, C$	:	[u]P
2.	$B \rightarrow A, C$	:	[v]P
3.	$C \rightarrow A, B$	:	[w]P

4. A computes  $K_A = e([v]P, [w]P)^u$ 

- 5. B computes  $K_B = e([u]P, [w]P)^v$
- 6. C computes  $K_C = e([u]P, [v]P)^w$

From the laws of bilinear pairings, K<sub>A</sub>, K<sub>B</sub>, K<sub>C</sub> result in the same value, say K<sub>ABC</sub>. So common agreed key of A, B, C  $K_{ABC} = K_A = K_B = K_C = .e(P, P)^{uvw}$ .

- Assumption : Bilinear Diffie-Hellman (BDH) [2] [Sec. 3.2.] problem is hard to compute.
- Security : Secure against passive opponent under the assumption that BDH problem is hard.
- Efficiency :
- Communication : Number of Rounds required is 1; number of group elements sent are 3.

*Computation :* 3 scalar multiplications; 3 pairing computations; 3 exponentiations.

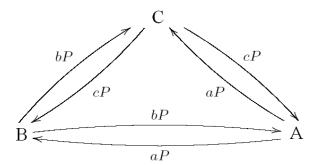


Figure 1: Joux's Tripartite Key Agreement

#### b) Diffie-Hellman Assumption

In this subsection we specify the version of the Diffie-Hellman problem which we will require. Consider the triple  $\langle A_1, C, e \rangle$  where  $A_1, C$  are two cyclic subgroups of a large prime order q and e : A1 x A<sub>1</sub>  $\rightarrow$  C is a cryptographic bilinear map. We take A<sub>1</sub> as an additive group and C as a multiplicative group.

#### Bilinear Diffie-Hellman BDH Problem

The strength of Joux's protocol is based on the Bilinear Diffie-Helman (BDH) [2] assumption. Let P be the generator of A1 and a, b, c are positive integers

. The BDH assumption considers the computation of e(P, P)^{abc}given <P, aP , bP, cP> to be hard.

#### c) Man-in-the-middle Attack

Let three parties A, B, C respectively have chosen secrets at random  $\in \mathbb{Z}_q^{\bullet}$  and let D be the attacker chooses three random secrets u', v', w' and let D be the Consider the Symmetric Pairing e: A<sub>1</sub> x A<sub>1</sub>  $\rightarrow$  C and P is the generator of the cyclic group A1 publicly known. The attack functions as follows:

1. 
$$A \rightarrow B, C: [u]P.$$

D intercepts [u]P and instead sends [u']P to B, C.

#### 2. $B \rightarrow A, C: [v]P.$

D intercepts [v]P and instead sends [v']P to A, C.

3.  $C \rightarrow A, B: [w]P.$ 

D intercepts [w]P and instead sends [w']P to A, B.

4. A computes  $K_1 = e([w']P, [v']P)^u = e(P, P)^{uv'w'}$ .

D computes  $K_1 = e([u]P, [v']P)^{w'} = e(P, P)^{uv'w'}$ .

5. B computes  $K_2 = e([u']P, [w']P)^v = e(P, P)^{u'vw'}$ .

D computes  $K_2 = e([v]P, [w']P)^{u'} = e(P, P)^{u'vw'}$ .

6. C computes  $K_3 = e([u']P, [v']P)^w = e(P, P)^{u'v'w}$ .

D computes  $K_3 = e([u']P, [w]P)^{v'} = e(P, P)^{u'v'w}$ .

Finally instead of a key shared between three users A, B and C, three keys are shared among four users A, B, C and D where one key K1 between A and D, another K2 between B and D and another K3 between C and D.

#### IV. PROPOSED MODEL

One of the applications of Joux's protocol is to share a master key between two communicating parties and one central authority say certification Authority CA or Public Key Generator PKG. MANET environment lacks central management and hence there is need for two-party key agreement protocol. Our proposed two party key agreement algorithm is based on Joux's Protocol. It makes use of Pairings (or Bilinear Maps) and Threshold cryptography concepts. Let A and B be the two communicating parties want to share a secret or session key. Let A, B respectively select integers at random  $\mathbf{v} \in \mathbf{Z}_{\mathbf{u}}$ .

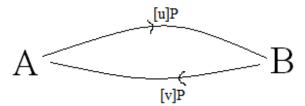
1.  $A \rightarrow B : [u]P$ 

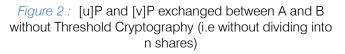
- 2.  $B \rightarrow A : [v]P$
- 3. A computes  $e(P, [v]P)^{u} = e(P,P)^{uv}$ .
- 4. B computes  $e([u]P, P)^v = e(P,P)^{uv}$ .

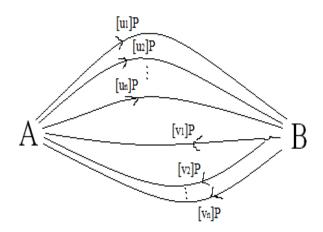
If R=[u]P and S=[v]P are transmitted as is without applying threshold cryptography as shown in Fig 2., adversary can easily compute the key as e([u]P,[v]P)= e(P,P)uv by just intercepting [u]P and [v]P during steps 1 and 2.

To counter this we apply the concept of threshold cryptography for steps 1 and 2; steps 3 and 4 remain the same. The secrets 'u' and 'v' are split into n shares each using Shamir's secret sharing mechanism [21] to get  $u_i$  and  $v_i \forall 1 \le i \le n$ , where n is the number of multiple independent paths that exist between sender and receiver. The shares of the products [u] P and [v]P are then calculated as  $R_i = [u_i]P$  and  $S_i = [v_i]P$ . These shares are then exchanged through n independent paths with the other party as shown in Fig 3. The n independent paths used to transmit  $[u_i]P$  and  $[v_i]P$  are

the same, but shown differently in Fig 3 for easy understanding.







*Figure 3 :* The n shares of [u]P and [v]P exchanged between A and B over n independent paths

When A and B receives at least t shares of Si and Ri respectively, they can reconstruct S and R as

$$R = \sum_{l=1}^{t} R_l \prod_{1 \le m \le t, m \ne l} \frac{m}{l-m}$$
$$S = \sum_{l=1}^{t} S_l \prod_{1 \le m \le t, m \ne l} \frac{m}{m}$$

Hence unless the adversary intercepts at least t shares of Ri and Si, he cannot reconstruct R and S and therefore the key. Also the key is the session key that has small life time i.e., over a single session; hence the time scope for adversary to reconstruct the key is small, thereby protecting the protocol from man-in-the-middle attack.

#### V. Implementation

The proposed key agreement protocol is implemented in software using the Pairing-Based Cryptography Library (PBC) [20]. The results are as follows:

The Elliptic curve is chosen as:  $y^2 = x^3 + x$ , with x, y elements of a Field Fq; q is a prime number. A1 is a subgroup of E(Fq). C is a subgroup of Fq2. There are q+1 points on the ECC curve, i.e. #ECC(Fq) = q+1. We consider symmetric bilinear map A1 x A1  $\square$  C.

#### $q = 3 \mod 4.$

r = order of A1 = prime factor of q+1.

h = cofactor = #ECC(Fq) / r.

The values for the parameters of the elliptic curve are chosen as:

#### **q**=8780710799663312522437781984754049815806 8831994142082110286533992664756308802229570 7862517942266222142315585876958231745927771 3367317481324925129998224791

#### **r** =

#### 7307508186654516213611192455715049014059765 59617

**h**=1201601226489114607938882136674053420480 2954401251311822919615131047207289359704531 102844802183906537786776

The below figure shows the output of the proposed model using the above elliptic curve parameters and pairing based cryptography library:

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*Figure 4 :* Snapshot showing the execution of the proposed model

From the above execution, the key K shared between the two communicating parties A and B takes the value as (for certain integer values of u and v):

K =

[363491729790068469392561995270411097829316104 156594577714042448502171634089271920327615921 920004770048290991588623854829412201391189267 9184970413844060998,

858461927735908169968899784862922131639479632 862313889300069721130367348791328889908613437 669417596253709218854900684187378935137609173 589847246783309559]

#### VI. CONCLUSION AND FUTURE SCOPE

In this article, we described a generalization of the Diffie-Hellman protocol and Joux Protocol to twoparties. Our two-party key agreement protocol uses the pairings and threshold cryptography concepts. Our model also does not assume a centralized trusted authority, which is difficult to establish in MANET environment. Therefore, this new protocol seems quite promising as a new building block to construct new and efficient complex cryptographic protocols. On the other hand, there is a scope to ensure the integrity of the secret shares. Additionally, there is scope to use this shared secret key in pairing based cryptography for encryption and decryption of messages, there by secret transmission of messages between the two communicating parties.

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### Research Analysis of Cyber Security

#### By Rabea Masood, Qaria Zainab & Mehreen Sarshar

Fatima Jinnah Women University, Pakistan

Abstract- In an age of cyber technology with it fast pacing and ever evolving, securing data in cyber space is a major enigmawhich needs to be resolved. With vulnerabilities everywhere, data security and privacy is always at risk. This specially comes in play when services of third party are used knowingly or unknowingly. Government and business organizations are testing and implementing security and monitoring techniques to stand a better chance in raging war against cyber-crimes. Moreover, the formulation of new methods also poses new limitations of the systems as well as the users like lack of efficiency or complexity which need to be resolved in order to get better results. In this research paper some of those limitations and their solutions are discussed.

Keywords: cybercrime, security, complexity, usage, efficiency.

GJCST-E Classification : C.2.0



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## Research Analysis of Cyber Security

Rabea Masood<sup>°</sup>, Qaria Zainab<sup>°</sup> & Mehreen Sarshar<sup>P</sup>

Abstract- In an ageof cyber technology with it fast pacing and ever evolving, securing data in cyber space is a major enigmawhich needs to be resolved.With vulnerabilities everywhere, data security and privacy is always at risk. This specially comes in play when services of third party are used knowingly or unknowingly. Government and business organizations are testing and implementing security and monitoring techniques to stand a better chance in raging war against cyber-crimes. Moreover, the formulation of new methods also poses new limitations of the systems as well as the users like lack of efficiency or complexity which need to be resolved in order to get better results. In this research paper some of those limitations and their solutions are discussed. *Keywords: cybercrime, security, complexity, usage, efficiency.* 

#### I. INTRODUCTION

ne of the major issues of today's ever updating technology dependent world is the safety of their private data. Whether it is data of the major organizations launching a new product or secret military operation details, the safety and protection of that data is the most important enigma.

In present time, the ratio of cybercrimes is increasing by each day. In a recent list presented by FBI, it is very clear that cybercrimes now are not only limited to small data theft or simple hacks through malware, but their scope is expanding way behind that horizon. Some of the recent cases of FBI (Cyber Crime branch) areRansom-ware, more than 2000 ATM hits at once, Phishing attacks and more crimes of same nature.

Even though research is being done in cyber security field and practices are also being updated but the problem of cyber-crimes is far from being solved. According to recent researches, the main limitation seems to be the approach used. The methods used are not evolving fast enough to combat the problem.

While many approaches have been implemented, there are limitations that arise with their use. Major limitations are complexity for local user, if more than one different security infrastructures used. Some of other known limitations are decrease in usage, etc. In order for these limitations to be

Author o : Department of Computer Sciences, Fatima Jinnah Women University Rawalpindi, Pakistan. e-mail: qariazainab@gmail.com Author p : Department of Computer Sciences, Fatima Jinnah Women University Rawalpindi, Pakistan. e-mail: msarshar@gmail.com efficiency, data collection, need for monitoring of resolved, more work needs to be done especially in field of research. Research needs to be done starting at institution level. For this purpose, usage is also needed to be monitored to study the user habits and patterns.

Another issue that needs attention is validation of software used and methods and standards used to test or validate them. This is the issue that calls out for attention desperately. As with the ever growing trend of third-party applications and new launch of software every day, there is no telling which one is safe and which is not. So to check their validity and to declare them safe or non-safe, old methods are not enough.

New methods should be built based on International Society of Automation (ISA) standards. The importance of organizational level security is also discussed.

Through this work the importance of cyber security in the modern world has been conveyed. It has also been discussed as to which limitations need to be resolved for it to be effective.

#### II. Related Work

Even though research is being done in cyber security field and practices are also being updated but the problem of cyber-crimes is far from being solved. According to recent researches, the main limitation seems to be the approach used. The methods used are not evolving fast enough to combat the problem.

While many approaches have been implemented, there are limitations that arise with their use. Major limitations are complexity for local user, if more than one different security infrastructures used. Some of other known limitations are decrease in efficiency, data collection, need for monitoring of usage, etc.

In order for these limitations to be resolved, more work needs to be done especially in field of research. Research needs to be done starting at institution level. For this purpose, usage is also needed to be monitored to study the user habits and patterns.

Another issue that needs attention is validation of software used and methods and standards used to test or validate them. This is the issue that calls out for attention desperately. As with the ever growing trend of third-party applications and new launch of software every day, there is no telling which one is safe and which is not. So to check their validity and to declare them safe or non-safe, old methods are not enough.

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New methods should be built based on International Society of Automation (ISA) standards.

#### III. Conclusion

From the above work, the importance of cyber security is emphasized. It is also concluded that closely monitoring systems and users provide and insight on the attacks and user reaction to them. Also monitored systems are less vulnerable to threats, data theft, phishing, frauds and other cyber-crimes.

Since the validation of software is necessary, so ISA standardized systems should be developed to validate them.

Also one of the major roles should be played by Government. It should take hold of every bit of events that occur in cyber space including formulation of new algorithms and techniques to prevent unauthorized access to any intruder.

In future, work would be done on monitoring techniques, their shortcomings and role play. Also, further research will include methods of secure authorizations.

#### a) Analysis

While analyzing the data, the first keen thing observed was the possibility of System being noncomplex as well as vulnerability free very narrow. If a system is to be secure to the highest level, userfriendliness or ease of access especially to users with basic knowledge cannot be provided. Also the fault tolerance of currently existing systems is very low, even in the high-end computers. It could only be increased by closely monitoring the capabilities of existing systems in their ability to treat vulnerabilities. The systems with higher level of robustness have more reliability rate.

Some other components related to cyber security are as follows:

#### b) Security

The most important and most basic requirement of any system is security. In order for any system to qualify as reliable, at least basic level of security need to be provided. With passing time, the need better cyber security seems to be the basic one.

#### c) Efficiency

Efficiency is to use least possible resources to achieve most functionality. Encryption, antispyware and secure routes etc. are used to achieve this purpose.

#### d) Ease of use

The user being able to operate even with basic skill is important. With increase in level of security comes the implementation of complex infrastructures, which makes it difficult to keep the system difficulty free for a basic skilled user. Open source development and other such methodologies are being used to achieve this. To achieve this at a standard level, iterative techniques and human brain inspired infrastructures are being developed.

#### f) Case study

Analyses not only at organization level but at much larger level are being conducted. To make comparisons using these studies, surveys and volunteer research are being conducted.

#### g) Testability

Testing plays extremely important role to check functionality of the systems. The security techniques before massive or global level implementation are tested several times on smaller networks.

#### h) System availability

The system availability to perform the necessary immunization steps before connecting to networks are to be done.

#### i) Fault tolerance

User participation in detecting vulnerabilities, phishing attacks and other such threats play an important role in increase of fault tolerance.

#### j) Monitoring

By closely monitoring the habits of users and keeping a close watch at young user habits can reduce the number of vulnerabilities at immense level.

#### IV. Acknowledgements

We wish to acknowledge the guidelines and effort provided by designated professors.

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Evaluation parameters	Meanings	Possible value Yes, No	
Security	The proposed technique is able to detect and correct errors		
Efficiency	System is efficient in terms of software	Yes, No	
Case study	Examples can use to support the methodology	Yes, No	
Ease of use	Software is easy to use or learn for the user	Yes, No	
Robustness	System is able to correct errors that are not specified	Yes, No	
Testability	Proposed design tested or not	Yes, No	
Reliability	System is working or not till the time line given	Yes, No	
System availability	The time when the application must be available for use	Yes, No	
Fault tolerance	The ability to remain partially operational during a failure	Yes, No	
Monitoring	To keep under systematic review	Yes, No	

S #	Technique	Security	Efficiency	Case study	Ease of use	Robustness	Testability	System availability	Fault tolerance	Monitorin g
1	J. Malgeri et al, 2009	Yes	Yes	No	No	No	No	No	No	Yes
2	R. Pal et al, 2014	Yes	No	Yes	No	No	No	No	No	No
3	S. Kowtha et al, 2012	Yes	Yes	No	No	Yes	No	Yes	No	No
4	L. yang et al, 2010	Yes	Yes	Yes	Yes	Yes	Yes	No	No	No

5	T. Feglar	Yes	No	No	Yes	No	No	Yes	No	No
6	H.Sandberget al,2010	Yes	Yes	No	Yes	Yes	No	Yes	No	Yes
7	H. Peng et al 2010	Yes	Yes	Yes	No	Yes	Yes	Yes	No	Yes
8	M. Alex et al,2013	Yes	Yes	No	Yes	Yes	Yes	Yes	Yes	Yes
9	R. Sandhu et al, 2010	Yes	Yes	No	No	No	Yes	Yes	No	Yes
10	D. Zama et al,2011	Yes	Yes	N.A	Yes	No	No	Yes	No	Yes
11	T. Peter et al,2010	Yes	Yes	Yes	No	Yes	Yes	N.A	Yes	No
12	R.Rayne et al, 2014	Yes	Yes	No	Yes	Yes	Yes	Yes	Yes	Yes
13	K .Jan et al, 2012	Yes	N.A	Yes	No	N.A	Yes	N.A	Yes	Yes
14	A. Robert et al, 2011	Yes	No	No	Yes	Yes	Yes	N.A	Yes	Yes
15	E. lan, 2014	Yes	Yes	Yes	Yes	Yes	Yes	No	Yes	N.A
16	S. Robert et al, 2009	Yes	No	Yes	N.A	Yes	Yes	Yes	Yes	No
17	H. Dennis et al ,2010	Yes	N.A	No	No	Yes	Yes	Yes	Yes	Yes
18	D. Dipankaret al, 2010	No	No	Yes	No	No	Yes	No	No	Yes
19	F. Rebecca, 2012	Yes	No	Yes	Yes	No	No	Yes	N.A	Yes
20	L. James et al, 2013	Yes	Yes	Yes	No	Yes	Yes	Yes	Yes	Yes



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# Implementation of AES with Time Complexity Measurement for Various Input

#### By Shraddha More & Rajesh Bansode

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Abstract- Network Security has a major role in the development of data communication system, where more randomization in the secret keys increases the security as well as the complexity of the cryptography algorithms. In the recent years network security has become an important issue. Cryptography has come up as a solution which plays a vital role in the information security system against various attacks. This security mechanism uses the AES algorithm to scramble data into unreadable text which can only be decrypted with the associated key. The AES algorithm is limited only for text as an input. It also has, the more time complexity. So it suffers from vulnerabilities associated with another type of input and time constraints. So its challenge to implement the AES algorithm for various types of input and require less decryption time. The propose work demonstrate implementation of a 128-bit Advanced Encryption Standard (AES), which consists of both symmetric key encryption and decryption algorithms for input as a text, image and audio. It also gives less time complexity as compared to existing one. At the last stage comparing the time complexity for encryption and decryption process for all three types of input. This paper also demonstrates a side channel attack on the standard software implementation of the AES cryptographic algorithm.de

Keywords: side channel attack, aes, des,rsa, encryption, decryption, cryptography, network security..

GJCST-E Classification : F.1.3 C.2.1



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# 2015 Year Global Journal of Computer Science and Technology (E) Volume XV Issue IV Version I

## Implementation of AES with Time Complexity Measurement for Various Input

Shraddha More<sup> a</sup> & Rajesh Bansode<sup> p</sup>

Abstract- Network Security has a major role in the development of data communication system, where more randomization in the secret keys increases the security as well as the complexity of the cryptography algorithms. In the recent years network security has become an important issue. Cryptography has come up as a solution which plays a vital role in the information security system against various attacks. This security mechanism uses the AES algorithm to scramble data into unreadable text which can only be decrypted with the associated key. The AES algorithm is limited only for text as an input. It also has, the more time complexity. So it suffers from vulnerabilities associated with another type of input and time constraints. So its challenge to implement the AES algorithm for various types of input and require less decryption time. The propose work demonstrate implementation of a 128bit Advanced Encryption Standard (AES), which consists of both symmetric key encryption and decryption algorithms for input as a text, image and audio. It also gives less time complexity as compared to existing one. At the last stage comparing the time complexity for encryption and decryption process for all three types of input. This paper also demonstrates a side channel attack on the standard software implementation of the AES cryptographic algorithm.de

Keywords: side channel attack, aes, des,rsa, encryption, decryption, cryptography, network security.

#### I. INTRODUCTION

ryptography plays an important role in the security of data transmission. Data Security is a challenging concern of data communications that focuses on many areas including secure communication channel and strong data encryption technique. The secure transmission of confidential data enclosed gets a great deal of attention because of the rapid development in information technology. The predictable methods of encryption can only maintain the data security. The development of computing technology imposes stronger requirements on the cryptography schemes. The rapidly growing number of wireless communication users has led to the increasing demand for security measures and devices to protect user data transmitted over wireless channels[1].

Two types of cryptographic systems have been developed for that purpose symmetric (secret key) and asymmetric (public key) cryptosystems. Symmetric

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cryptography, such as in the Data Encryption Standard (DES), 3DES, and Advanced Encryption Standard (AES) uses an identical key of the sender to encrypt the message text and receiver to decrypt the encrypted text. Asymmetric cryptography, such as the Rivest-Shamir-Adleman (RSA) uses different public keys for encryption and decryption, eliminating the key exchange problem.[2] Symmetric cryptography is more suitable for the encryption of a large amount of data. The Data Encryption Standard (DES) has been used by the U.S. government standard since 1977. However, now, it can be cracked quickly and inexpensively. The AES algorithm defined by the National Institute of Standards and Technology (NIST) of the United States has widely accepted to replace DES as the new symmetric encryption algorithm [3]. This above cryptographic algorithms are not more secure. To overcome the vulnerabilities in network security in 2000, the Advanced Encryption Standard (AES) replaced the DES to meet the ever-increasing requirements for security. In cryptography, the AES, also called as Rijndael, is a block cipher adopted as an encryption standard by the US government, which specifies an encryption algorithm capable of protecting sensitive information[4]. The Rijndael algorithm is a symmetric block cipher that can encrypt and decrypt information. Encryption converts data into an unintelligible form called cipher-text. Decryption of the cipher-text converts the data back into its unique form which is called plaintext. The AES algorithm supports 128, 192 and 256 bit key length to encrypt and decrypt data in blocks of 128 bits, hence AES-128. AES-192 and the name AES-256 respectively[5]. The hardware implementation of the AES algorithm can provide high performance, low cost for specific applications and trustworthiness compared to its software counterparts[6].

The organization of the paper is as follows, Section II describes the design overview of AES algorithm for both encryption and decryption. Section III presents implementation Details, Section IV is discussed on Experimental Results. Section V projects on future scope and conclusion.

#### II. DESIGN OVERVIEW OF AES

AES is a symmetric block cipher with block length of 128 bits. It allows three different key lengths 128,192 and 256 bits. In encryption process processing of 128 bit keys required for 10 rounds, 192 bit keys

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required for 12 rounds and 256 bit keys required for 14 rounds which is shown in table1. AES is a round based algorithm. For encryption and decryption each round has four functions excepting last round. Last round required three functions. The encryption algorithm has four round functions SubByte(), ShiftRows(), MixColumn() and AddRoundKey(). The decryption, also has the same number of rounds with reverse transformation, order of round function is different i.e. InvShiftRow(), InvSubByte(), AddRoundKey() and InvMixColumn() [2]-[3].

Table 1 : AES parameters for the various AES versions

AES PARAMETERS	AES-128	AES-192	AES-256
Key Size (Bits)	128	192	256
Number of rounds	10	12	14
Plaintext box size (Bits)	128	128	128

#### a) AES Encryption Algorithm

The Encryption process consists of a number of different transformations applied consecutively over the data block bits in a fixed number of iterations which is called as rounds. The number of rounds depends on the length of the key used for the encryption process. 10 iterations are required for key length of 128 bits.

#### i. High-level description of the algorithm

KeyExpansions -round keys are derived from the cipher key using Rijndael's key schedule. AES requires a separate 128-bit round key block for each round plus one more.

- ii. InitialRound
- AddRoundKey()- Each byte of the state is combined with a block of the round key using bitwise xor. Rounds
- 2. SubBytes()- A non-linear substitution step where each byte is replaced with another according to a lookup table.
- ShiftRows()-A transposition step where the last three rows of the state are shifted cyclically a certain number of steps.
- 4. MixColumns()-A mixing operation which operates on the columns of the state, combining the four bytes in each column.
  - iii. Final Round (No MixColumns)

SubBytes()

ShiftRows()

#### AddRoundKey().

Steps : These steps are used to encrypt128-bit block.

- 1. The set of round keys from the cipher key.
- 2. Initialize state array and add the initial round key to the starting state array.
- 3. Perform round = 1 to 9 : Execute Usual Round.

- 4. Execute Final Round.
- 5. Corresponding cipher text chunk output of Final Round Step

#### iv. Encryption process

#### Each round consists of the following four steps:

SubBytes Transformation: In this transformation, each of the byte in the state matrix is replaced with another byte as per the S-box (Substitution Box)[7]. The S-box is generated by firstly calculating the respective reciprocal of that byte in GF (2^8) and then affine transform is applied.

*ShiftRows Transformation:* In this transformation, the bytes in the first row of the State do not change. The second, third, fourth and fifth rows shift cyclically to the left by one byte, two bytes, three bytes and four bytes respectively [7].

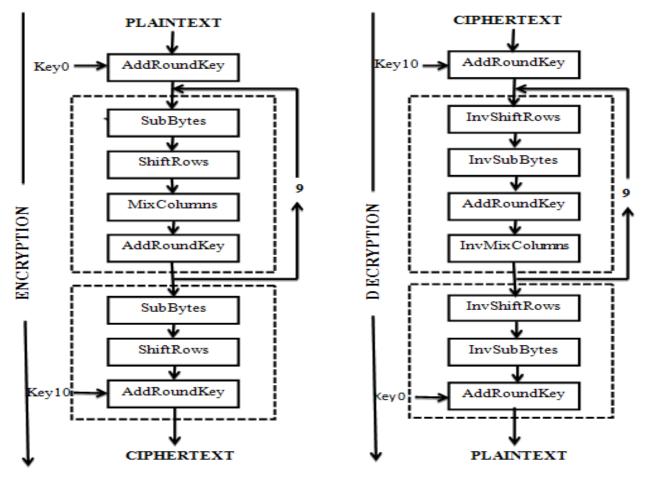
*MixColumns Transformation:* It is the operation that mixes the bytes in each column by the multiplication of the state with a fixed polynomial matrix [7]. It completely changes the scenario of the cipher even if all bytes look very similar. The Inverse Polynomial Matrix does exist in order to reverse the mix column transformation.

AddRoundKey Transformation: In AddRoundKey transformation, a roundkey is added to the State by bitwise Exclusive-OR (XOR) operation. AddRoundKey proceeds onecolumn at a time. AddRoundKey adds a roundkey word with each state column matrix.The operation performed in AddRoundKey is matrix addition.

#### b) AES Decryption Algorithm

Decryption is the process of extracting the plaintext from cipher text. For decryption the same process occurs simply in reverse order by taking the 128-bit block of cipher text and converting it to plaintext by the application of the inverse of the four operations. Decryption involves reversing all the steps taken in encryption using following inverse functions.

InvSubBytes Transformation: InvSubBytes is the inverse transformation of SubBytes, in which the inverse S-box is applied to individual bytes in the State. The inverse S-box is constructed by first applying the inverse of the affine transformation, then computing the multiplicative inverse in GF( $2^8$ ).



#### *Figure 1:* AES Encryption and Decryption

InvShiftRows Transformation: InvShiftRows is the inverse transformation of ShiftRows. In this transformation, the bytes in the first row of the State do not change. The second, third, and fourth and fifth rows are shifted cyclically by one byte, two bytes, three bytes and four bytes to the right respectively [2].

*InvMixColumns Transformation:* InvMixColumns is the inverse transformation of MixColumns. This is a complex procedure as it involves severely the byte multiplication under GF (2<sup>8</sup>)[2].

#### Key Expansion (Keyexpansion Operation)

Keyexpansion refers to the process in which the 128 bits of the original key are expanded into eleven 128-bit round keys.

To compute round key (n+1) from round key (n) these steps are performed:

 Compute the new first column of the next round key. First all the bytes of the old fourth column have to be substituted using the Subbytes operation. These four bytes are shifted vertically by one byte position and then

XORed to the old first column. The result of these operations is the new first column.

- 2. Columns 2 to 4 of the new round key are calculated as shown:
- [new second column] = [new first column] XOR
   [old second column]

- [new third column] = [new second column] XOR
   [old third column]
- [new fourth column] = [new third column] XOR [old fourth column]

The key expansion algorithm generates 128 bit key for each round and one more key for initial AddRoundKey function. The same expanded key is used for encryption and decryption except for decryption it reads in reverse order.

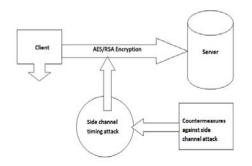
#### III. IMPLEMENTATION DETAILS

The system proposing aims to achieve network security by implementing appropriate countermeasures based on concept of constant time encryption against side channel timing attack to protect implementations of secret key cryptography. The contribution work includes implementing more suitable countermeasures against side channel attack.

#### a) System Overview

The propose system, is intended to provide secure transmission of data over the network by implementing the appropriate countermeasures against side channel attack on AES implementation which is shown in Fig.2. Here the work implementing AES 128bit algorithm using 10 rounds by taking input as text, image and audio. In AES encryption process, system

performs round functions like SubByte(), ShiftRows(), MixColumn() and AddRoundKey(). On the other side, the decryption processperforms round functions like InvShiftRow(), InvSubByte(), AddRoundKey() and InvMixColumn(). After that the work implementing side channel attack on the AES implementation in such a way that the receiver cannot decrypt the encrypted data. After successful implementation of side channel attack, work implementing some research appropriate countermeasures against side channel attack on AES implementation and finally evaluating their performance and soundness to prevent possible vulnerabilities and develop more secure systems.



#### Figure 2 : System architecture

#### b) AES Implementation

The work implemented AES 128-bit, 10 rounds algorithm by taking input as text, image and audio.

#### Encryption Process when input as an Text file

The work implemented 128 bit AES algorithm (10 round) encryption using text as an input by measuring performance parameter as time complexity which is shown in Fig.3.Time required for encryption process is 1.166557 milliseconds.

Encryption and Decryption					
Encryption Decryption					
Спетурани рестуран	•				
	Select a Plaintext File C:\U	sers\dell\Desktop	\SJCET III sem\docui	ment.txt Browse	
		t a little test of my	AES writer.		
	Let us try : File Contents new line	a couple			
	characters	i.			
Key			1234567890qwerty		
Click to e	encrypt ==>			Encrypt	
		01.000			
	SubBytes	ShiftRows	MIXC	Columns	AddRoundKey
Round 1					c5766c1ae141e693835c94e
					<b>▲</b>
Round 2					28452e29c82cd13ca6ef614a
				1.00	26d1700a1a75a6da3a81a4c
Round 3			330691339874 3510		4
					ba00f59e6e03030cef624807
Round 4					•
	ba00f59e6e03030cef62480	4 2e004cdb3e13	6f58e42f7cb8b 2e00	04cdb3e136f58e42f7cb8b	289ed74f466d193e4a77ed9
Round 5	•			III •	•
Round 6	289ed74f466d193e4a77ed	343c55b65af5	e884d6180eb2 343	c55b65af5e884d6180eb2	1062111dbd01377dce4688
Nouna o					
Round 7					d363f15dea610635443f30b1
Round 8		3 105ca2b7a240c		a2b7a240d3b1d1af65920	3dbff3fc741b1c8a060058425
					94132dae5cada6787ed820
Round 9	dbff3fc741b1c8aU6UU5842			3c2c5dat6a1d9263U1b06t	94132dae5cada6787ed820
	227dd8e44a9524bcf361b7				112f325251b880138c95210
Round 10		3 1227 uuse44as:	J24DCI30TD773	•	
Time Taken			1.166557 ms		

Figure 3 : AES Encryption: Input as Text

Decryption Process when input as an Text file

The work implemented 128 bit AES algorithm (10 round) decryption using text as an input by measuring performance parameter as time complexity which is shown in Fig.4.Time required for decryption process is 2.128282 milliseconds.

#### Encryption Process when input as an audio file

The work implemented 128 bit AES algorithm (10 round) encryption using audio as an input by measuring performance parameter as time complexity which is shown in Fig.5.Time required for encryption process is 13.899532 milliseconds.

#### Decryption Process when input as an audio file

The work implemented 128 bit AES algorithm (10 round) decryption using audio as an input by measuring performance parameter as time complexity which is shown in Fig.6.Time required for decryption process is 20.183485milliseconds.

#### Encryption Process when input as an Image file

The work implemented 128 bit AES algorithm (10 round) encryption using image as an input by

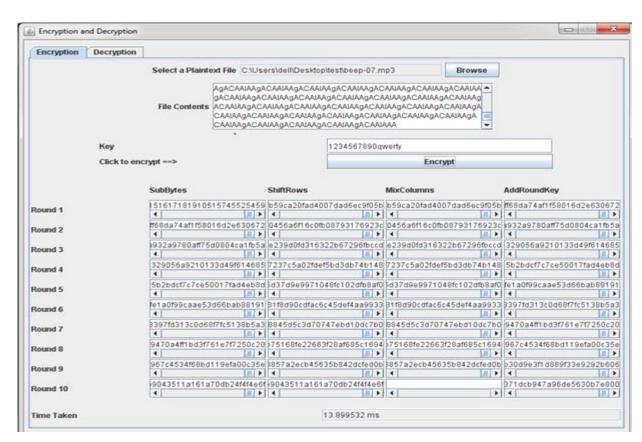
measuring performance parameter as time complexity which is shown in Fig.7. Time required for encryption process is 61.958627 milliseconds.

#### Decryption Process when input as an Image file

The work implemented 128 bit AES algorithm (10round) decryption us ing image as an input by measuring performance parameter as time complexity which is shown in Fig.8.Time required for decryption process is 31.509569milliseconds.

👙 Encryption and Decryption							
Encryption Decryption							
	Select a Ciphertext File stdell	\Desktop\SJCET	III sem\docume	ent.txt.encrypted	rowse		
	This is jus Let us try a new line characters		AES writer.				
Key			1234567890qv	verty			
Click to dec	:rypt ==>			Decrypt			
	InvShiftRows	InvSubBytes		InvMixColumns	Ir	nvAddRound	Кеу
Round 1	32295b77f4a61f1e4f351d8b		a6787ed8208f	94132dae5cada678	7ed8208f8	3089c2c5daf ∢	6a1d926301b06f
Round 2	8089c2c5daf6a1d926301b06	fBdbff3fc741b1c8	3a060058425e	8dbff3fc741b1c8a060	058425e I	D5ca2b7a24 ◀	0d3b1d1af65920
Round 3	05ca2b7a240d3b1d1af65920		635443f30b13	d363f15dea6106354		3ca7cc45b7a ∢	a5adba48b0b82ff
Round 4	9ca7cc45b7a5adba48b0b821		377dce4688f9	1062111dbd01377d	ce4688f9	343c55b65a	f5e884d6180eb2
Round 5	343c55b65af5e884d6180eb2	289ed74f466d	193e4a77ed9b	289ed74f466d193e4	a77ed9b 2	2e004cdb3e	136f58e42f7cb8b
Round 6	2e004cdb3e136f58e42f7cb8t	ba00f59e6e030		ba00f59e6e03030cet		if79d4988a2 ▲	
Round 7	6179d4988a20cf36780be5157					35fbdfaaa13	
Round 8	35fbdfaaa134233de9fa39874	28452e29c82cc		28452e29c82cd13ca		■ a6832210f84	
Round 9	a6832210f84a16a2ece050d			c5766c1ae141e6938	35c94e0 b	iafca20a0d4 ∢	
Round 10	bafca20a0d40083ad6e5bf05b	bafca20a0d400				<u></u>	20202020202020
Time Taken		11	2.128282 ms	1			

Figure 4 : AES Decryption :Input as Text





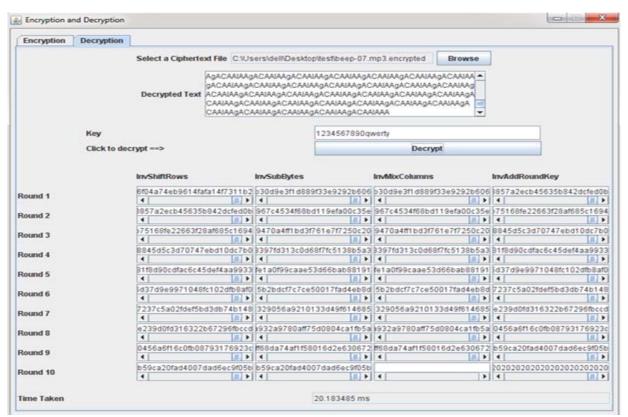
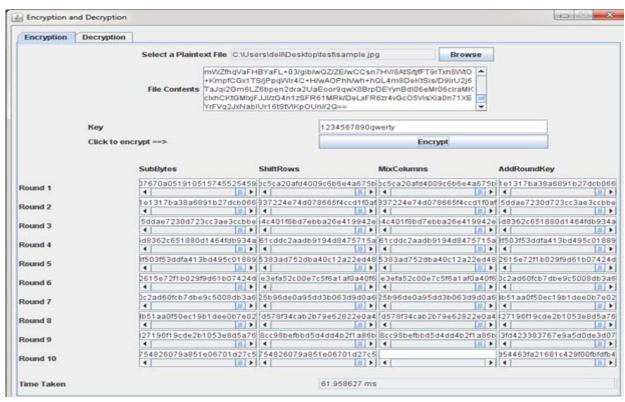


Figure 6 : AES Decryption: Input as Audio





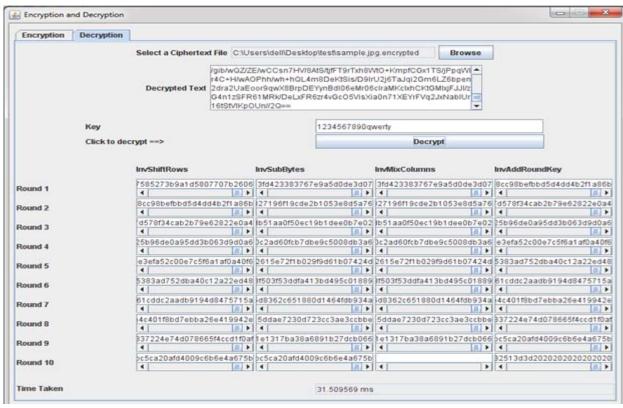


Figure 8 : AES Decryption Process: Input as Image

#### c) Attack on AES implementation

After successful implementation of AES algorithm. The work implemented attack in such a way that at the time of decryption, receivers cannot get the

decrypted file as a plain text file instead of that the user will get the file which is in the human non-readable format which is shown in the Fig.9.

Encryption	Decryption								
		Select a Ciphertex	t File sers\d	dell\Desktop\fina	l attack\docum	ent.txt.encrypted	Browse		
				∣Yg≇M öÏËI§°ấ∷ ù^_§:œ( Áë9µ ál		r 6, ,7 ‰(Ð?Cíjkj	},= ;m≉		
	Key				1234567890qv	werty			
	Click to dea	:rypt ==>			Decrypt				
		InvShiftRows		InvSubBytes		InvMixColumns		InvAddRoun	-
Round 1		ce9738388c3b275	ic227c6b5d	iec9605a7f0854	188d944976ba	iec9605a7f08548		ac387bd608	87dd87617b3e8
Round 2		ac387bd60887dd8	7617b3e81	aa55d1eabf760	6a91d8ea0393	aa55d1eabf766a	191d8ea0393	3d819b090a	b73833d3c9det
Round 3		Bd819b090ab7383	3d3c9deb7	0b4cf9c66a391	4020a920e8cf	0b4cf9c66a3914	020a920e8cf	ed286ec1e	56cdfda01b20d4
Round 4		ed286ec1e56cdfd	a01b20d45	1531df37a2aee	2c6809b845e2	1531df37a2aee2d	:6809b845e2	abb0412ffdl	)c702a09def8a8
Round 5		abb0412ffd0c702a						· ·	100
Round 6		29665729582c8e50				135f51c0a11bce2		b27df68462	100
		•		<b>_</b>		<u> </u>			3edcOad7254ab

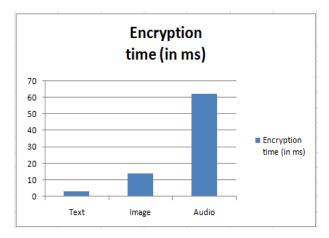
Figure 9 : A on AES implementation

#### IV. EXPERIMENTAL RESULTS

In this section The work presented result graph of our proposed system, implementation of the AES algorithm by taking text, image and audio as input. The work used 10 rounds technique for implementing AES 128- bit algorithm.

#### a) Result graph for encryption time

In Fig. 10. The graph shows the time needed to encrypt the input as a text, image and audio data file by the proposed system .





b) Result graph for decryption time

In Fig. 11. The graph shows the time needed to decrypt the input as a text, image and audio data file by the proposed system .

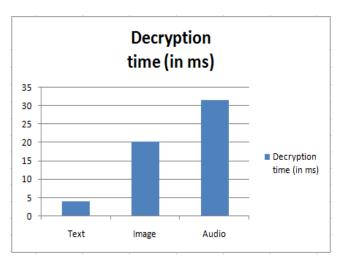


Figure 11: Decryption time taken by AES algorithm

#### Conclusion Andfuture Scope

Due to the increasing needs for secure communications а safer and more secured cryptographic algorithm has to be proposed and implemented. The Advanced Encryption Standard (AES-128bit) is widely used nowadays in many applications. In this paper, the work implemented an efficient AES128 bit encryption and decryption algorithm. The execution time for AES encryption and decryption is calculated by performing 10 round functions. The system presented an attack on AES software implementations. Future work will focus on investigating and implementing a number of countermeasures against side channel attack on AES implementation and have evaluated their performance and soundness to prevent possible vulnerabilities and develop more secure systems.

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# A Cross Layer Model to Support Qos for Multimedia Applications on Wireless Networks

#### By Vijayalakshmi M & Linganagouda Kulkarni

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Abstract- Supporting multimedia application over wireless networks poses multiple challenges. Currently the use of cross layer architectures and Scalable Video Coding (*svc*) techniques are considered to support multimedia applications. The current architectures fail to address the tradeoff that exists between the end to end delay and the Quality of Service (*Qos*) provisioning of the video data to be delivered. To address this issue this paper introduces the *Qos* improvement scheme in video transmission model based on a cross layer architecture. A novel *mac* encoding of the SVC video is considered in the proposed model. Based on the physical layer conditions and the *Qos* achievable the model adapts to meet the stringent delay requirements of video delivery. Routing layer optimization is achieved by accounting for the pending packets queues in every neighboring node. The experimental study conducted prove the robustness of the proposed model by comparing with the existing schemes. Comparisons in terms of the transmission error rates, system utility and quality of reconstruction are presented.

Keywords: scalable video coding (SVC), cross layer, H.264, multimedia, quality of service (QoS), MAC, routing, encoding.

GJCST-E Classification : C.2.1



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## A Cross Layer Model to Support Qos for Multimedia Applications on Wireless Networks

Vijayalakshmi M <sup>a</sup> & Linganagouda Kulkarni <sup>o</sup>

Abstract- Supporting multimedia application over wireless networks poses multiple challenges. Currently the use of cross layer architectures and Scalable Video Coding (SVC) techniques are considered to support multimedia applications. The current architectures fail to address the tradeoff that exists between the end to end delay and the Quality of Service (0oS) provisioning of the video data to be delivered. To address this issue this paper introduces the *QoS* improvement scheme in video transmission model based on a cross layer architecture. A novel MAC encoding of the SVC video is considered in the proposed model. Based on the physical layer conditions and the QoS achievable the model adapts to meet the stringent delay requirements of video delivery. Routing layer optimization is achieved by accounting for the pending packets queues in every neighboring node. The experimental study conducted prove the robustness of the proposed model by comparing with the existing schemes. Comparisons in terms of the transmission error rates, system utility and quality of reconstruction are presented.

*Keywords:* scalable video coding (SVC), cross layer, H.264, multimedia, quality of service (QoS), MAC, routing, encoding.

#### I. INTRODUCTION

he increasing demand by of users to access infotainment solutions on wireless networks aid development of novel models to support applications [1] [2]. To support such multimedia application delivery on wireless networks high bandwidth [3], quality of service (QoS) [4], stringent delay requirements have to be accounted for. Wireless networks are characterized by limited bandwidth, hop based routing and error prone nature. This nature tends to induce transmission loss, delayed delivery and high jitter in supporting video streaming applications [5] [6]. The ISO/IEC MPEG [7] group and the ITU - T VCEG [8] groups have standardized the Scalable Video Coding (SVC) extension to the existing H. 264video compression standard which can be adopted to support multimedia wireless applications on networks [9]. The SVC compression technique enables video encoding taking into account the varied quality, spatial and temporal parameters, thus providing adaptability. Considering wireless networks based on the network conditions, network configuration, application demands and QoS parameters the SVC video encoding can be adopted support multimedia transmissions. to

Adoption of the SVC for multimedia data delivery on wireless networks cannot be considered as a holistic solution. Video transmissions are delay bound and delivery of the data packets within the delay deadlines is of most importance [1] [5] [11] [14]. The multimedia data delivery is achieved through hop based mechanisms in wireless networks. The distortion and the available channel capacity vary during data delivery which needs to be accounted for. The end to end delay varies based on the physical layer condition and the buffering mechanism at the medium access. The induced transmission errors cause packet retransmission overheads. Based on the physical layer conditions the next hop routing mechanism also requires constant updation. In short it can be stated that, delivery of delay sensitive data on wireless networks put forth variations in the physical layer, medium access control layer (MAC) and the routing layer. Apart from these variations observed it is also critical to establish a balance between the data delivery and the QoS provided. Providing QoS at the cost of delayed data delivery is ineffective in the case of multimedia data [1] [14]. To address these issues researchers have proposed a cross layer architectures to account for the dynamics observed at the physical, MAC and routing layer for multimedia data [3] [12] [13] Combining cross layer optimization [14]. and SVC encoding for multimedia data delivery has been considered in [11], [15] and the results obtained prove the efficiency and assure *QoS* provisioning.

The existing models fail to address the tradeoff relation that exist between the *OoS* of the *SVC* encoded data transmission and end to delay i.e. if the QoS to be provisioned is high the end to end delays are high proved in [14]. To address this issue this paper introduces the QoS improvement scheme in video transmission (QIVST) model adopting a cross layer optimization approach. The QIVST model considers the SVC encoded video streams for transmissions. Based on the physical layer conditions of the wireless network, the quality adaptation specifier  $(S_{s1})$  and the physical layer knowledge specifier  $(S_{s2})$  are identified. A novel encoding scheme of the SVC video utilizing the  $S_{s1}$  and  $S_{s2}$  is considered at the *MAC* layer. The packets constructed at the MAC layer are routed through the next hop node based on the  $S_{s1}, S_{s2}$  and pending packets in that node. A similar approach is adopted at every intermediate hop node. The OIVST model

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proposed is designed to address the tradeoff between *QoS* provisioning and delivery of the delay bound multimedia data. The cross layer optimization adopted in the *QIVST* model provides adaptability to achieve better *QoS* in wireless networks and ensures the essential delay bound multimedia data delivery.

The remaining manuscript is organized as follows. A brief of the literature review discussing the state of the art mechanisms that currently exist is discussed in section 2. The proposed *QIVST* model is presented in Section 3 of this paper. The simulation study with performance comparisons is discussed in the penultimate section of this paper. The conclusions and future work is discussed in the Section 5.

#### II. LITERATURE REVIEW

Numerous work considering multimedia data delivery on wireless networks has been proposed by researchers. A brief of the literature studied during the course of the research presented here is discussed in this section.

An ant colony optimization algorithm to support video streaming services on wireless mobile networks is proposed in [3]. A dual layer architecture constituting of the mini-community network layer and the community member layer is considered in [3]. The mini-community layer enables robust video data delivery and access methodologies. The resource and member management is achieved by the community member layer. The results presented prove the efficiency of the biologically inspired ant colony optimization.

A cross layer optimization technique to support video transmissions on wireless networks has been proposed by Yuanzhang Xiao et al [12]. The importance of resource allocation to support video transmissions is discussed. The cross layer architecture proposed by Yuanzhang Xiao et al enables dynamic scheduling and resource allocations among the wireless user nodes based on the physical channel conditions and the dynamics of video transmissions.

The cross layer fairness driven stream control transmission protocol based concurrent multipath transfer solution (CMT - CL/FD) is proposed in [13]. The efficiency of utilizing multipaths for video content delivery is highlighted. Optimizations were adopted at the physical, data link and transport layer in the CMT - CL/FD to support video applications on heterogeneous wireless networks. In CMT - CL/FD the cross layer optimization is adopted only at the transmitter.

Hypertext Transfer Protocol (*HTTP*) based Dynamic Adaptive Streaming (*DASH*) of *SVC* video in wireless networks is discussed in [11]. A cross layer optimization based on the Lagrangian method is adopted in *DASH* to support streaming of *SVC* video. A novel resource allocation and packet scheduling algorithm is considered in *DASH*. The tradeoff that exists between data delivery and *QoS* of video transmissions is discussed. The tradeoff issue is addressed by Mincheng Zhao et al through a proxy based bitrate stabilization algorithm introduced in *DASH*.

Transmission of SVC video data in multi input multi output (MIMO) wireless systems is proposed by Xiang Chen et al [15]. A cross layer approach adopting optimizations based on the physical and application layer is proposed by Xiang Chen et al. To reduce transmission errors and reduce the number of retransmissions FEC mechanisms are also employed by the authors in [15]. An adaptive channel power allocation scheme is used in [15] to improve the QoS of video transmissions. The work proposed by Xiang Chen et al bears the closest similarity to the work proposed here and is further used for performance comparisons with our proposed QIVST model. The major drawback of the cross layer approach proposed in [15] is that the tradeoff that exists between QoS provisioning and video data delivery is not addressed.

#### III. *Qos* Improvement Scheme in Video Transmission *Qisvt*

#### a) Wireless Network Modelling

Let us consider a wireless network  $\mathcal{N}$  deployed over an area of  $\mathcal{A}$  sq.meters. The network N consists of а set of *I* nodes sharing the multimedia content D with J receiver nodes. The channel matrix of the  $b^{th}$  node is represented as C[b] where  $b \in I \parallel J$ . The wireless channel Bandwidth considered is  $R_c$  and the channel error rate is represented by  $R_{e}$ . The channel noise is represented as N. The SVC video data [1] is considered as the multimedia content. Video transmissions are bulky and require efficient transmission mechanisms to meet the desired OoS. In the OISVT model introduced in this paper the video content is initially encoded using the MPEG video coder. The MPEG video coder considered adopts the Group of Pictures (GOP) structure described in [2] [14]. The GOP structure is shown in Fig.1. of this paper.

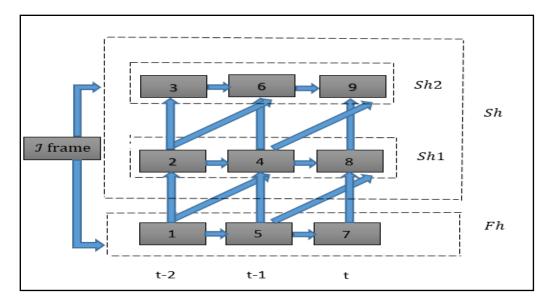


Figure 1 : Packet loss compensation by error correction in video transmission using QISVT scheme

The encoded frames are indexed by g =1,2,3 ...... In the QISVT model introduced the initial frame/reference frame  $\mathcal{I}$  is transmitted first. The base layer B frame is donated as Fh and the enhancement/quality layer P frame is represented asSh. In the existing mechanisms discussed earlier a loss of the FH and SH frame results in a retransmission enhancing end to end delays and reducing the wireless transmission QoS. To improve the QoS in multimedia content delivery over the network  $\mathcal{N}$  the *QISVT* model introduces a novel cross layer adaption technique[3]. By acquiring the prevailing physical layer properties of the node, the MAC layer packetization techniques and the routing to the neighboring nodes are accordingly adapted to achieve a cross layer design discussed in the proceeding sub-section of the paper.

#### b) Cross-Layer Design Of The QISVT model

A discrete time based model to describe the cross layer architecture of the *QISVT* model is considered. Let us consider a node  $i \in I$  transmitting content *D* to its neighbor  $j \in J$ . At time t - 2 the *I* frame is transmitted. The  $X_{t-1}$  frame consisting of  $X_{t-1}^{F_h}$  and  $X_{t-1}^{S_h}$  is transmitted at the  $(t-1)^{th}$ time instance. In the *QISVT* model the *Sh* frame is assumed to consist of two sub-frames namely *Sh*1 and *Sh*2 i.e. Sh = Sh1 + Sh2. The sub frame into the *Sh*1 and transmit it wirelessly to the node *j* at time *t*. The adoption of the sub-framing technique enables reconstruction of the *Fh* in case of transmission errors. The encoded frame  $X_{1t}^{S_h,e}$  is defined as

$$X_{1t}^{S_{h},e} = \left( (1 - S_{s1}) X_{t-1}^{F_{h}} \right) + \left( S_{s1} \times X_{t-1}^{S_{h}} \right)$$
(1)

Where  $S_{s1}$  is the Quality layer adaptation specifier introduced in the *QISVT* model. Based on the physical layer parameters, the node bandwidth supported, the pending packets in the *MAC* queue and channel noise thevalue of  $S_{s1}$  is established on runtime. The quality adaptation specifier is constrained by the set  $S_{s1} \in$  $\{0,0.1,0.2...1\}$ . The  $S_{s1}$  specifier enables in controlling the quality of the video transmission between the nodes *i* and *j*. Considering  $S_{s1} = 1$  the best *QoS* can be achieved. When  $S_{s1} = 0$  only the *Fh* is transmitted resulting in lower quality.

To account for the physical layer conditions in the MAC encoder the Physical Layer Knowledge Specifier  $S_{s2}$  parameter is introduced and is defined as

$$S_{s2} = Val : Val = \{0, \dots 1\}$$
 (2)

introducing the  $S_{s2}$  parameter By the composition of the Sh1 and Sh2 sub frames is achieved accounting for the physical layer parameters. If  $S_{s2} = 0$ then Sh1 = Fh and  $Sh2 = \emptyset$  i.e. the physical layer exhibits high distortion and the transmission of the Fh layer is only considered. If  $S_{s2} = 1$  then Sh1 = Sh and  $Sh2 = \emptyset$  is considered as an ideal condition when the physical channel exhibits no signal distortion hence the entire Sh layer is considered for transmission. The  $S_{s2}$  and  $S_{s1}$  parameters are derived based on the physical layer measurements carried out at  $\Delta t$  intervals. The channel noise, packet delay and the error rate observed in transmitting the frame  $\mathcal{I}$  enables in initialization. The proposed MAC layer encoding can be now defined as

$$X_{1t}^{S_{h},e,S_{s2}} = \left( (1 - S_{s1}) X_{t-1}^{F_{h}} \right) + \left( S_{s1} \times X_{t-1.}^{S_{h},S_{s2}} \right)$$
(3)

Where  $X_{1_t}^{S_h,e,S_{s2}}$  represents the *MAC* encoded data derived from the previous  $X_{t-1}^{F_h}$  and  $X_{t-1}^{S_h,S_{s2}}$  frame to be transmitted.

The MAC encoding is presented in Fig.2. of this paper.

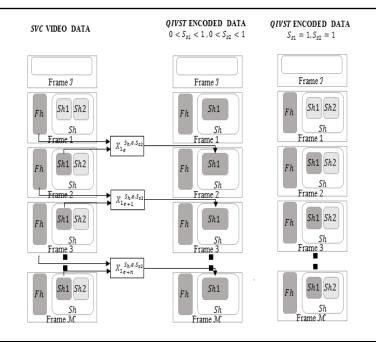


Figure 2 : Adaptive MAC encoding based on the QISVT model

Based on the MAC queues pending,  $S_{s1}$  and the  $S_{s2}$  parameter the routing layer is optimized to select the next hop neighbor node to achieve *QoS*.

The *QISVT* model can be summarized as follows:

Step 1: Initialize Encoded Multimedia Data D

Step 2: Initialize Transmitting Node i and Receiving Node j

Step 2: Extract the frame  $\Im$  and transmit from Node  $i \rightarrow j$ 

Step 3: Measure Error Rate, Delay.

Step 4: Based on the measurements initialize  $\mathcal{S}_{s2}$  and  $\mathcal{S}_{s1}$ 

Step 5: Based on D the Fh and Sh frame Data is derived.

Step 6: Based on  $S_{s2}$  and  $S_{s1}$  derive *Sh*1 and *Sh*2 and perform MAC encoding using Equation 3.

Step 7: Based on the MAC packet Queues Pending,  $S_{s2}$  and  $S_{s1}$  perform routing optimization to select hop node.

#### i. Video Distortion in the QIVST model

Transmission over wireless channels induces errors in transmission. The transmission errors result in a huge number of video packet errors and losses. On packet error or loss occurrences, packet retransmission request and response messages are propagated. This phenomena induces huge amounts of overheads and the video packet delivery time increases effecting *QoS*.

To improve *QoS* the cross layer *QIVST* model to reduce packet delivery delays is introduced in this paper. The distortion observed at the receiver is proportional to the channel noise. When the channel noise observed is large, the QIVST adapts to enable successful transmissions compromising QoS as video data delivery is delay bound. Packets delivered beyond the delay bound possess no significance and are generally dropped. The QIVST model proposed provides a delicate tradeoff between timely delivery of data packets and QoS .The encoding at the MAC layer  $X_{1_{*}}^{S_h,e,S_{S2}}$ enables recovery of the Fh from the encoded enhancement layer packet in case the base layer packet is lost. The encoding enables to achieve optimal QoS in noisy environments. In this section the modelling of the packet error probabilities, video frame transmissions, frame reconstruction, frame decoding, frame errors and the distortions observed is discussed.

Let the data *D* to be transmitted using the *QISVT* model form  $\mathcal{M}^{total}$  encoded packets. Each packet consists of *b* symbols. The symbols*b* need to be transmitted on the wireless Radio Layer Switching mode of  $T_{(b)}$  through a channel which has an allocated bandwidth based channel rate of  $R_{c(b)}$ . The additive white gaussian noise present in the wireless channels induces transmission errors. The error rate experienced by the symbol *b* is given by  $R_{e(b)}(R_{c(b)}, T_{(b)})$ .

Let us assume that there are  $\mathcal{M}^{total}$  number of video packets formed from the video to be

transmitted. In error free enviromets the complete  $\mathcal{M}^{total}$  packets when transmitted will be received, decoding which would form the data D. In practical enviromets or actual conditions transmission errors occour due to the noise present in the wireless transmission medium. If out of  $\mathcal{M}^{total}$  Packets only  $\mathcal{M}$  packets are transmitted successfully and remaining  $(\mathcal{M}^{total} - \mathcal{M})$  packets are lost during transmission such that error occurs at the

 $(\mathcal{M}+1)th$  packet, the probability of such an occurrence if  $\mathcal{M}=0$  is

$$Occ(\mathcal{M}|\mathcal{M}^{total}) = R_{e(b)}(R_{c(b)}, T_{(b)})$$
(4)

The probability of occurrence during the transmission being active i.e.  $0 < \mathcal{M} < \mathcal{M}^{total}$  is given by

$$Occ(\mathcal{M}|\mathcal{M}^{total}) = \prod_{d=1}^{\mathcal{M}} \left( 1 - R_{e(d)}(R_{c(d)}, T_{(d)}) \right) \times \left( R_{e(p+1)}(R_{c(p+1)}, T_{(p+1)}) \right)$$

Considering  $\mathcal{M} = \mathcal{M}^{total}$  the error probablity occurance is defined as

$$Occ(\mathcal{M}|\mathcal{M}^{total}) = \prod_{d=1}^{\mathcal{M}^{total}} \left(1 - R_{e(d)}(R_{c(d)}, T_{(d)})\right)$$
(6)

Let us consider at the  $t^{th}$  time the receiver node receives  $\mathcal{M}$  video packets successfully out of the  $\mathcal{M}^{total}$  packets. If  $A_d$  represents the number of symbols in the  $d^{th}$  packet, then the cumulative video data available at the receiver i.e.  $R^{rx}$  can be computed using

$$R^{rx} = \sum_{d=1}^{\mathcal{M}} (R_{c(d)} \times A_d)$$
<sup>(7)</sup>

The QIVST model priorities the delivery of the Fh ensuring the delay constraints are attained. If the *Fh* encoded packet at the  $t^{th}$  time instance i.e.  $X_{\star}^{F_h}$  is lost then its recovery is possible from the  ${X_1}_{t+1}^{S_h,e,S_{s2}}$ encoded packet. Let the function  $Dt(R^{rx}, S_{s1}, S_{s2})$  represent the distortion observed per frame at the receiver post decoding considering all the symbols of the Fh layer, the quality layer adaptation specifier  $S_{s1}$  , the physical layer knowledge specifier  $S_{s2}$  and  $R^{rx}$  is the total symbols of the Sh layer. Based on the error probabilities and the distortions of the individual frames the average distortion  $Dt_{avg}[(S_{s1}, S_{s2})]$  can be computed using

$$Dt_{avg}[(S_{s1}, S_{s2})] = Dt(0, S_{s1}, S_{s2}) Occ(0 \mid \mathcal{M}^{total}) + \sum_{\mathcal{M}=1}^{\mathcal{M}^{total}} \{Dt(R^{rx}, S_{s1}, S_{s2}) Occ(\mathcal{M} \mid \mathcal{M}^{total})\}$$

where  $Dt(0, S_{s1}, S_{s2})$  represents the distortion observed with respect to the reference frame **J**.

The parameter  $S_{s2}$  controls the composition of the *Sh* data. In the case when channel noise is present and the channel bandwidth cannot support the transmission of the entire *Sh* layer i.e.  $0 < S_{s1} <$ 1 and  $0 < S_{s2} <$  1, a part of the *Sh*<sub>2</sub> is not considered for encoding and transmission and is defined as

$$V_t^{S_h} = X_t - \left(X_t^{F_h} - X_{t-1}^{F_h}\right) - X_1 t^{S_h, e, S_{s2}}$$
(9)

Where  $X_t$  is the original frame considered at the  $t^{th}$  time instance.

To achieve optimum QoS the *Fh* layer is transmitted and the *Sh*1 is encoded at the *MAC* layer and transmitted. Let  $V_t^{S_h1}$  represent the *MAC* encoded data of *Sh*1 and  $V_t^{S_h2}$  denote the decoded *Sh*1 data at the receiver. In the *QIVST* model the packet loss probability of the *Fh* is assumed to be 0. The frame reconstructed at the decoder at the  $t^{th}$  time instance is defined as

$$X_{2_{t}} = V_{t}^{S_{h}2} + \left(X_{t}^{F_{h}} - X_{t-1}^{F_{h}}\right) + X_{2_{t}}^{S_{h},d,S_{s2}}$$
(10)

where  $X_{2_t}^{S_{h,d},S_{s2}}$  represents the data at the receiver on performing the *MAC* layer decoding on the encoded data  $X_{1_t}^{S_{h,e},S_{s2}}$ .

The decoded version of  $X_{1_t}^{S_h,e,S_{S^2}}$  at the receiver on the basis of the partially decoded data of the *Sh* data i.e.  $X_{2_{t-1}}^{S_h,S_{S^2}}$  is defined as

$$X_{2_{t}}^{S_{h},d,S_{s2}} = \left( (1 - S_{s1}) X_{t-1}^{F_{h}} \right) + \left( S_{s1} \times X_{2_{t-1}}^{S_{h},S_{s2}} \right)$$
(11)

Utilizing the above definition in Equation 10 we obtain

$$X_{2_{t}} = V_{t}^{S_{h}2} + \left(S_{s1} \times \left(X_{2_{t-1}}^{S_{h},S_{s2}} - X_{t-1}^{F_{h}}\right)\right) + X_{t}^{F_{h}}$$
(12)

where  $X_{2t-1}^{S_h,S_{s2}}$  is the partially decoded data of the *Sh* layer at the  $(t-1)^{th}$  time instance and is defined as

$$X_{2t-1}^{S_h, S_{s2}} = V_{t-1}^{S_{h2}, S_{s2}} + \left(X_{t-1}^{F_h} - X_{t-2}^{F_h}\right) + X_{2t-1}^{S_h, d, S_{s2}}$$
(13)

(8)

(5)

The partially encoded data of the *Sh* layer i.e. *Sh*1 at the  $(t-1)^{th}$  time instance is defined as

$$X_{t-1.}^{S_h, S_{s2}} = V_{t-1}^{S_h, S_{s2}} + \left(X_{t-1}^{F_h} - X_{t-2}^{F_h}\right) + X_1_{t-1}^{S_h, e, S_{s2}}$$
(14)

To compute the transmission error the difference between the encoded video frame at the transmitter  $X_t$  and the decoded video frame at the receiver  $X_{2_t}$  is considered and is defined as

$$T_e = X_t - X_{2_t} = V_t^{S_h} - V_t^{S_h^2} + X_{1_t}^{S_h, e, S_{s^2}} - X_{2_t}^{S_h, d, S_{s^2}}$$
(15)

Using Equation 8 and Equation 12 the error can be simplified as

Using Equations 13 and equation 14  $(X_{t-1}^{S_h, S_{s2}})$  cab be represented as

$$T_e = V_t^{S_h} - V_t^{S_h^2} + \left(S_{s1} \times \left(X_{t-1}^{S_h, S_{s2}} - X_2^{S_h, S_{s2}}\right)\right)$$
(16)

$$\left(X_{t-1}^{S_h, S_{s2}} - X_{2t-1}^{S_h, S_{s2}}\right) = V_{t-1}^{S_h, S_{s2}} - V_{t-1}^{S_{h2}, S_{s2}} + \left(S_{s1} \times \left(X_{t-2}^{S_h, S_{s2}} - X_{2t-2}^{S_h, S_{s2}}\right)\right)$$
(17)

The distortion of the  $m^{th}$  frame at the receiver post decoding at the at the  $t^{th}$  time instance is computed using

$$Dt_m(\mathcal{T}M, S_{s1}, S_{s2}) = Avg[(T_e)^2]$$
 (18)

where  $\mathcal{T}M$  is the throughputs observed as the receiver post decoding considering all the frames from the reference frame  $\mathcal I$  to the  $m^{th}$  frame

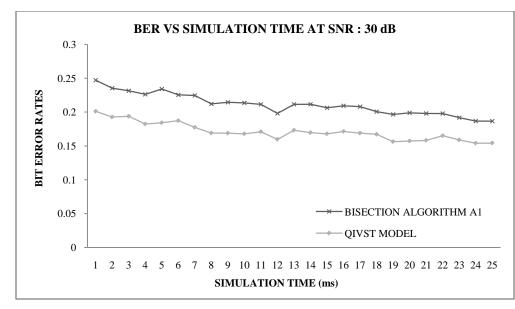
From equation 18 it can be observed that the transmission errors effect the throughput observed and also induce distortion in video reconstruction at the receiver. The *QIVST* model adapts based on the physical layer conditions to minimize the transmission errors by adopting adaptive *MAC* encoding and route optimization.

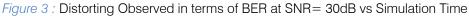
#### IV. EXPERIMENTAL STUDY

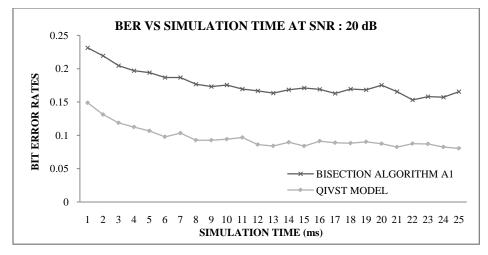
In this section the experimental study conducted to evaluate the performance of the proposed *QIVST* model is discussed. The experimental study was conducted using Matlab. The performance of the *QIVST* model is compared with the state of art *Bisection Algorithm* – *A1* proposed by Xiang Chen et al [15]. Video clips 'City' and 'Stefan' of Common Interchange Format are considered for the experimental study. The video clips 'City' and 'Stefan' are encoded by the reference *SVC* codec JSVM (Joint Scalable Video Model). The 'City' video consists of 300 frames and the 'Stefan' video of 90 frames. The frame rate considered for both the videos is 30 frames per second. The *SVC* codec considers the *GOP* structure.

A wireless network consisting of 15 nodes is considered. The simulation study considers 4 transmitter nodes and 4 receiver nodes. Experiments considering the 'City' and 'Stefan' video were independently conducted. The M-QAM modulation and demodulation schemes were considered in the experimental study. An additive white Gaussian wireless noise channel is considered and the signal to noise ratio 0, 10, 20 and 30 dB is considered. The video transmissions carried out are monitored and the video is reconstructed at the receiver. An average of the monitored values considering 4 transmitters and 4 receivers is presented.

In the prevision section it has been stated that the distortion observed Dt is directly proportional to the transmission errors  $T_e$  i.e.  $Dt \propto T_e$ . The transmission errors observed per frame is represented in terms of the bit error rates (BER) observed for the duration of the simulation. The BER observed considering the 90 frames of the "Stefan" video transmitted is shown in Figure 3, 4 and 5. From the figures it is clear that as the channel noise i.e. SNR increased the distortion increases considering the QIVST model and the *Bisection Algorithm* – A1 . At a  $SNR = 30 \, dB$  (in Figure 3) it is observed that the average BER considering the Bisection Algorithm - A1 is 0.21 and for the QIVST model is 0.17. When the channel noise induced in the simulation environment is 20 dB (in Figure 4) and 10 dB (in Figure 5) the proposed QIVST video transmissions model based achieves a BER reduction of 45.7% and 36.99% when compared to the Bisection Algorithm - A1. Based on the BER results it is evident that the proposed QIVST model is adaptive and performs better than the existing the *Bisection Algorithm - A1* under varying channel noise conditions. Lower BER's observed tend to enhance the QoS provided to multimedia data delivery over wireless networks.







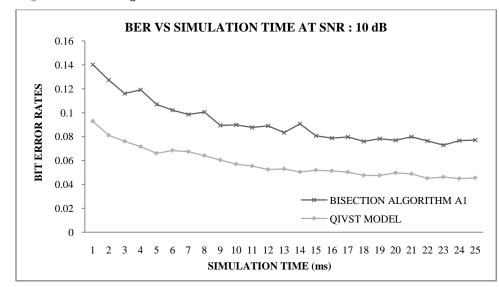


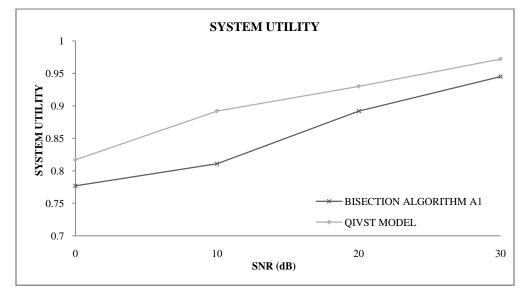
Figure 4 : Distorting Observed in terms of BER at SNR= 20dB vs Simulation Time

Figure 5 : Distorting Observed in terms of BER at SNR= 10dB vs Simulation Time

From equation 18 it can be observed that the transmission errors effect the throughput observed and also induce distortion in video reconstruction at the receiver. The *QIVST* model adapts based on the physical layer conditions to minimize the transmission errors by adopting adaptive *MAC* encoding and route optimization.

In [15] the authors have introduced the "System utility" parameter for performance evaluation. Considering the *GOP* of video "Stefan" the system utility computed using the *QIVST* model and

the *Bisection Algorithm* – *A*1. The results obtained are graphically shown in Figure 6 of this paper. The system utility increases as the channel noise increases due to transmission errors. The increase in transmission errors induce an additional network overhead by introducing retransmission messages. From the figure it is evident that the proposed *QIVST* model exhibits a higher system utility when compared to the the *Bisection Algorithm* – *A*1. The adaptive encoding and the cross layer architecture of the QIVST model also contribute to the increased system utility observations.



*Figure 6 :* System Utility

To evaluate the performance of the video delivery on the wireless network the 'City' video and the 'Stefan' video are transmitted. The reconstruction quality is observed in terms of the *SNR* per frame. For the City video a sample frame reconstructed at the receiver is shown in Figure 7 a considering the *QIVST* model and Figure 7 b considering the *Bisection Algorithm* – *A*1. The reconstruction quality observed at the receiver considering the 300 frames of the city video based on

the proposed QIVST model is shown in Figure 8. The reconstruction quality considering the Bisection Algorithm - A1 is shown in Figure 9. The reconstruction quality is computed per frame reconstructed at the receiver and is expressed in terms of the PSNR observed. From Figure 8 and 9 it is evident the outperforms that QIVST model the *Bisection Algorithm – A1* in terms of the quality of video transmitted.



*Figure 7 :* A sample reconstructed frame at the receiver for the "City" video considering the a. QIVST model proposed and b. Bisection Algorithm – A1

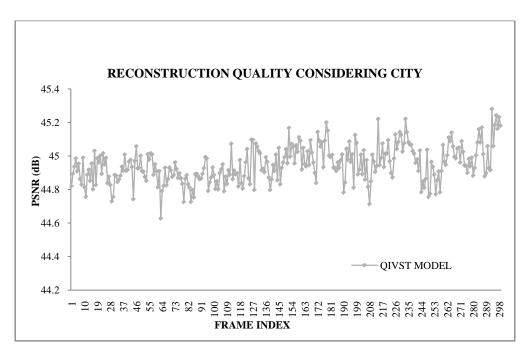
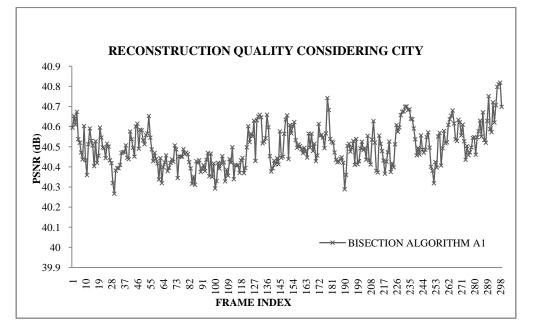
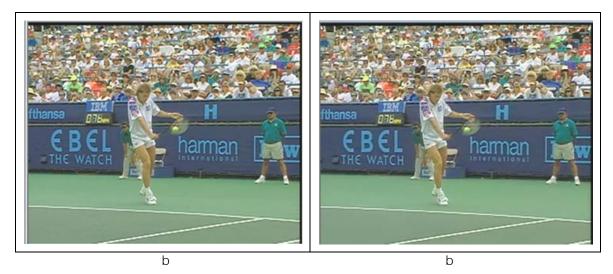


Figure 8 : Reconstruction quality per frame in terms of PSNR for the City video based on the QIVST model



*Figure 9 :* Reconstruction quality per frame in terms of PSNR for the City video based on the Bisection Algorithm – A1

Considering the "Stefan" video a sample frame reconstructed using the *QIVST* model and the *BisectionAlgorithm* – *A*1 is shown in figure 10 of this paper. The per frame *PSNR* computed depicting the quality of reconstruction is shown in Figure 11 and 12. From the reconstruction results considering the "Stefan" video shown in this paper it is clear that the *QIVST* model provides better quality in video delivery over wireless networks when compared to the the *Bisection Algorithm* – *A*1.



*Figure 10 :* A sample reconstructed frame at the receiver for the "Stefan" video considering the **a**. QIVST model proposed and **b**.Bisection Algorithm – A1

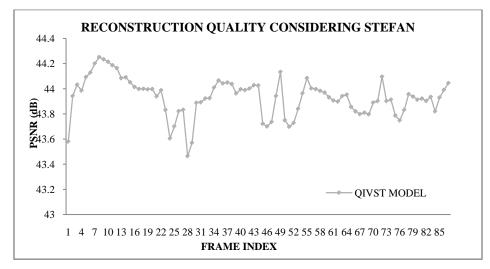
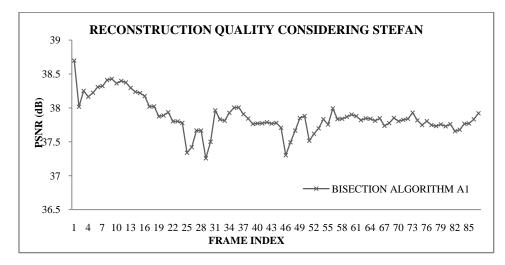
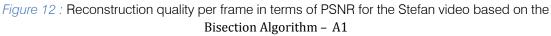


Figure 11 : Reconstruction quality per frame in terms of PSNR for the Stefan video based on the QIVST model





The experimental study presented in this paper prove that the cross layer design based *QIVST* model proposed is robust and adaptable proved in terms of lower *BER's* observed. The *QIVST* model induces an additional overhead due to the novel encoding scheme (proved by higher system utility observations) and improves the quality of video transmissions in wireless networks. The results also prove the proposed model superiority when compared to the state of art video delivery algorithm the *Bisection Algorithm – A1*.

#### V. CONCLUSION

High bandwidth requirements, delay sensitive nature and QoS measures of multimedia data delivery on wireless networks put forth numerous challenges. The use of SVC encoded streams on cross layer architectures have been proposed by researchers. The existing mechanisms fail to address the tradeoff between QoS and data delivery delays that exists. In this paper the QIVST model is introduced that adopts a cross layer design. The SVC video data considered in the QIVST model is further encoded at the MAC layer based on the physical layer conditions and the OoS achievable, to address the tradeoff issue highlighted. The distortion observed based on the **OIVST** model is presented. Based on the pending packet queues observed optimization of the routing layer is considered in the OIVST to minimize the end to end delay. The extensive results presented in the experimental study considering SVC video traces prove the robustness and efficiency of the proposed QIVST model when compared to the state of art existing system.

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# The Encryption Algorithm AES-RFWKIDEA32-1 based on Network RFWKIDEA32-1

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*Abstract-* In this article we developed a new block encryption algorithm based on network RFWKIDEA32-1 using of the transformations of the encryption algorithm AES, which is called AES-RFWKIDEA32-1. The block's length of this encryption algorithm is 256 bits, the number of rounds are 10, 12 and 14. The advantages of the encryption algorithms are that, when encryption and decryption process used the same algorithm. In addition, the encryption algorithm AES-RFWKIDEA32-1 encrypts faster than AES.

Keywords: advanced encryption standard, feystel network, lai-massey scheme, round function, round keys, output transformation, multiplica- tion, addition, multiplicative inverse, additive inverse.

GJCST-E Classification : C.2.1



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*Abstract-* In this article we developed a new block encryption algorithm based on network RFWKIDEA32-1 using of the transformations of the encryption algorithm AES, which is called AES-RFWKIDEA32-1. The block's length of this encryption algorithm is 256 bits, the number of rounds are 10, 12 and 14. The advantages of the encryption algorithms are that, when encryption and decryption process used the same algorithm. In addition, the encryption algorithm AES-RFWKIDEA32-1 encrypts faster than AES.

Keywords: advanced encryption standard, feystel network, lai-massey scheme, round function, round keys, output transformation, multiplica- tion, addition, multiplicative inverse, additive inverse.

#### I. INTRODUCTION

n September 1997 the National Institute of Standards and Technology issued a public call for proposals for a new block cipher to succeed the Data Encryption Standard [41]. Out of 15 submitted algorithms the Rijndael cipher by Daemen and Rijmen [13] was chosen to become the new Advanced Encryption Standard in November 2001 [28]. The Advanced Encryption Standard is a block cipher with a fixed block length of 128 bits. It supports three different key lengths: 128 bits, 192 bits, and 256 bits. Encrypting a 128-bit block means transforming it in n rounds into a 128-bit output block. The number of rounds n depends on the key length: n =10 for 128-bit keys, n = 12 for 192-bit keys, and n = 14for 256-bit keys. The 16-byte input block  $(t_0, t_1, \ldots, t_{15})$ which is transformed during encryption is usually written as a 4x4 byte matrix, the called AES State.

t <sub>o</sub>	t <sub>4</sub>	t <sub>8</sub>	$t_{12}$
t <sub>1</sub>	t <sub>s</sub>	t <sub>9</sub>	t <sub>13</sub>
$t_2$	t <sub>6</sub>	t <sub>10</sub>	t <sub>14</sub>
t <sub>3</sub>	t,	t <sub>11</sub>	t <sub>15</sub>

The structure of each round of AES can be reduced to four basic transfor-mations occurring to the elements of the State. Each round consists in applying

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successively to the State the SubBytes(), ShiftRows(), MixColumns() and AddRoundKey() transformations. The first round does the same with an extra AddRoundKey() at the beginning whereas the last round excludes the Mix-Columns() transformation.

The SubBytes() transformation is a nonlinear byte substitution that operates independently on each byte of the State using a substitution table (S-box). Figure 1 illustrates the SubBytes() transformation on the State.

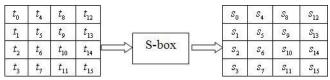


Figure 1: SubBytes() transformation

In the ShiftRows() transformation operates on the rows of the State; it cyclically shifts the bytes in each row by a certain o\_set. For AES, the first row is left unchanged. Each byte of the second row is shifted one to the left. Similarly, the third and fourth rows are shifted by o\_sets of two and three respectively. Figure 2 illustrates the ShiftRows() transformation.

$S_0$	S4	S <sub>8</sub>	S <sub>12</sub>		<i>s</i> ′ <sub>0</sub>	s'4	s' <sub>8</sub>	S'12
$S_1$	S5	S <sub>9</sub>	S <sub>13</sub>	cyclically shifts	<i>s</i> ' <sub>1</sub>	s'5	s' <sub>9</sub>	S' <sub>13</sub>
<i>S</i> <sub>2</sub>	<i>S</i> 6	S <sub>10</sub>	S <sub>14</sub>	cyclically shifts	<i>s</i> ′ <sub>2</sub>	S'6	S' <sub>10</sub>	S'14
<i>S</i> <sub>3</sub>	S7	<i>S</i> <sub>11</sub>	S <sub>15</sub>	cyclically shifts	<i>s</i> ′ <sub>3</sub>	s'7	s' <sub>11</sub>	s' 15

Figure 2 : ShiftRows() transformation

The MixColumns() transformation operates on the State column-by-column, treating each column as a four-term polynomial. The columns are considered as polynomials over GF(2<sup>8</sup>) and multiplied modulo  $x^4 + 1$  with a fixed polynomial a(x), given by a(x) =  $3x^2 + x^2 + x + 2$ . Let  $p = a(x) \otimes s'$ :

	$\begin{bmatrix} 02 & 03 & 01 & 01 \\ 01 & 02 & 03 & 01 \\ 01 & 01 & 02 & 03 \\ 03 & 01 & 01 & 02 \end{bmatrix}$	$\begin{bmatrix} s'_{4i} \\ s'_{4i+1} \\ s'_{4i+2} \\ s'_{4i+3} \end{bmatrix}$	, $i = \overline{03}$
--	--	--	-----------------------

As a result of this multiplication, the four bytes in a column are replaced by the following: Figure 3 . illustrates the MixColumns() transformation

$$y_{4i} = (\{02\} \bullet s'_{4i}) \oplus (\{03\} \bullet s'_{4i+1}) \oplus s'_{4i+2} \oplus s'_{4i+3}$$
  

$$y_{4i+1} = s'_{4i} \oplus (\{02\} \bullet s'_{4i+1}) \oplus (\{03\} \bullet s'_{4i+2}) \oplus s'_{4i+3}$$
  

$$y_{4i+2} = s'_{4i} \oplus s'_{4i+1} \oplus (\{02\} \bullet s'_{4i+2}) \oplus (\{03\} \bullet s'_{4i+3})$$
  

$$y_{4i+4} = (\{03\} \bullet s'_{4i}) \oplus s'_{4i+1} \oplus s'_{4i+2} \oplus (\{02\} \bullet s'_{4i+3}).$$

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*Figure 3 :* MixColumns() transformation

#### II. Analysis of Aes, pes and Idea

The first attack is a SQUARE attack suggested in [15] which uses  $2^{128} - 2^{119}$  chosen plaintexts and 2120 encryptions. The second attack is a meet-in-the- middle attack proposed in [16] that requires 2<sup>32</sup> chosen plaintexts and has a time complexity equivalent to almost 2<sup>128</sup> encryptions. Recently, another at- tack on 7round AES-128 was presented in [1]. The new attack is an impossible diferential attack that requires 2117:5 chosen plaintexts and has a running time of 2121 encryptions. Similar results, but with better attack algorithms and lower complexities were reported in [42]. The resulting impossible diferential attack on 7-round AES-192 has a data complexity of 292 chosen plaintexts and time complexity of 2<sup>162</sup> encryptions, while the attack on AES-256 uses 2116:5 chosen plaintexts and running time of 2<sup>247:5</sup> encryptions.

There are several attacks on AES-192 [1, 14, 15, 24, 29, 42]. The two most no-table ones are the SQUARE attack on 8-round AES-192 presented in [15] that requires almost the entire code book and has a running time of 2188 encryptions and the meet in the middle attack on 7-round AES-192 in [14] that requires 2<sup>34+n</sup> chosen plaintexts and has a running time of 2<sup>208</sup>-n + 2<sup>82+n</sup> encryptions. Legitimate values for n in the meet in the middle attack on AES-192 are 94 i n i 17, thus, the minimal data complexity is 2<sup>51</sup> chosen plaintexts (with time complexity equivalent to exhaustive search), and the minimal time complexity is 2146 (with data complexity of 297 chosen plaintexts). AES-256 is analyzed in [1,14, 15, 24, 42]. The best attack is the meet in the middle attack in [14] which uses 2<sup>32</sup> chosen plaintexts and has a total running time of 2209 encryptions. Finally, we would like to note the existence of many related-key attacks on AES-192 and AES-256. As the main issue of this paper is not related-key attacks, and as we deal with the single key model, we do not elaborate on the matter here, but the reader is referred to [43] for the latest results on related-key impossible di er-ential attacks on AES and to [20] for the latest results on related-key rectangle attacks on AES.

The strength of AES with respect to impossible di\_erentials was challenged several times. The first attack of this kind is a 5-round attack presented in [4]. This attack is improved in [11] to a 6-round attack. In [29], an impossible diferential attack on 7-round AES-192 and AES-256 is presented. The latter attack uses 2<sup>92</sup> chosen plaintexts (or 2<sup>92.5</sup> chosen plaintexts for AES-256) and has a running time of 2186 encryptions (or

2<sup>250:5</sup> encryptions for AES-256). The tim 4 Lecture Notes in Computer Science: Authors' Instructions for AES-192. In [1] a new 7-round impossible diferential attack was presented. The new attack uses a diferent impossible diferential, which is of the same general type as the one used in previous attacks (but has a slightly diferent structure). Using the new impossible diferential leads to an attack that requires 2<sup>117:5</sup> chosen plaintexts and has a running time of 2<sup>121</sup> encryptions. This attack was later improved in [2, 42] to use 2<sup>115:5</sup> chosen plaintexts with time complexity of 2<sup>119</sup> encryptions.

The last application of impossible diferential cryptanalysis to AES was the extension of the 7-round attack from [1] to 8-round AES-256 in [42]. The extended attack has a data complexity of 2116:5 chosen plaintexts and time com-plexity of 2<sup>247:5</sup> encryption. We note that there were three more claimed impossible diferential attacks on AES in [8{10]. However, as all these attacks are awed [7]. In paper [25] present a new attack on 7-round AES-128, a new attack on 7-round AES-192, and two attacks on 8-round AES-256. The attacks are based on the attacks proposed in [1, 29] but use additional techniques, including the early abort technique and key schedule considerations.

The best attack we present on 8-round AES-256 requires 2<sup>89:1</sup> chosen plain-texts and has a time complexity of 2<sup>129:7</sup> memory accesses. These results are significantly better than any previously published impossible differential attack on AES. We summarize results along with previously known results in Table 1.

Number	complexity		Attack type
of rounds			
	Data (CP)	Time	
AES-128			
7	$2^{128} - 2^{119}$	$2^{120}$	Square [15]
7	$2^{117.5}$	$2^{121}$	Impossible Differential [15]
7	$2^{117.5}$	$2^{119}$	Impossible Differential [2, 42]
7	$2^{32}$	$2^{128}$	Meet in the middle [16]
7	$2^{112.2}$	$2^{117.2}$ MA	Impossible Differential [25]
AES-192			
7	$2^{32}$	$2^{184}$	Square [24]
7	$19^{*}2^{32}$	$2^{155}$	Square [15]
7	292	$2^{186.2}$	Impossible Differential [29]
7	$2^{115.5}$	$2^{119}$	Impossible Differential [42]
7	2 <sup>92</sup>	$2^{162}$	Impossible Differential [42]
7	$2^{34+n}$	$2^{208-n} + 2^{82+n}$	Meet in the middle [14]
8	2128 - 2119	$2^{188}$	Square [15]
7	$2^{113.8}$	2 <sup>118.8</sup> MA	Impossible Differential [25]
7	$2^{91.2}$	$2^{139.2}$	Impossible Differential [25]
AES-256		1	
7	$2^{32}$	$2^{200}$	Square [24]
7	$21^{*}2^{32}$	$2^{172}$	Square [15]
7	$2^{92.5}$	$2^{250.5}$	Impossible Differential [29]
7	$2^{32}$	$2^{208}$	Meet in the middle [14]
7	$2^{34+n}$	$2^{208-n} + 2^{82+n}$	Meet in the middle [14]
7	$2^{115.5}$	$2^{119}$	Impossible Differential [42]
8	$2^{116.5}$	$2^{247.5}$	Impossible Differential [42]
8	$2^{128} - 2119$	$2^{204}$	Square [15]
8	$2^{32}$	$2^{209}$	Meet in the middle [14]
7	$2^{113.8}$	2 <sup>118.8</sup> MA	Impossible Differential [25]
7	2 <sup>92</sup>	2 <sup>163</sup> MA	Impossible Differential [25]
8	$2^{111.1}$	2 <sup>227.8</sup> MA	Impossible Differential [25]
8	289.1	2 <sup>229.7</sup> MA	Impossible Differential [25]

#### Table 1: A Summary of the Attacks on AES

The Proposed Encryption Standard (PES) is a 64-bit block cipher, using a 128-bit key, designed by Lai and Massey in 1990 (see [22]) and was a predecessor to IDEA (International Data Encryption Algorithm) [21].

IDEA was originally called IPES (Improved PES). PES iterates eight rounds plus an output trans- formation. The cryptanalysis of PES and IDEA presented on Table 2 and Table 3.

Attack Type	Year	Attacked	Key Bits	Chosen	Time
		Rounds	round	Plaintext	
Differential [26]	1993	2	32	210	242
Differential [12]	1993	2.5	32	210	232
Differential [26]	1993	2.5	96	210	2106
Related-Key Differential	1996	3	32	6	6 * 232
[18]					
Differential-Linear [6]	1996	3	32	230	244
Differential [5]	1996	3	32	230	0.75 * 244
Truncated Differential [19,	1997	3.5	48	256	267
[6]					
Miss-in-the-middle [3]	1998	3.5	64	238.5	253
Miss-in-the-middle [3]	1998	4	69	237	270

Table 2: A Summary of the Attacks on IDEA

Related-Key Differential-	1998	4	15	38.3	-
Linear [17]					
Miss-in-the-Middle [3]	1998	4.5	80	264	2112
Square attack [27]	2000	2.5	77	3 * 216	262 + 247
Square attack [27]	2000	2.5	31	232	262
Square [27]	2000	2.5	31	248	279
Related-Key Square [27]	2001	2.5	32	2	241

Attack Type	Year	Attacked	Key Bits	Chosen	Time
		Rounds	round	Plaintext	
Differential [23]	1991	7	96	264	2160
Square [27]	2000	2.5	31	217	247
Square [27]	2001	2.5	31	232	263
Related-Key Square [27]	2001	2.5	32	2	241

Global Journal of Computer Science and Technology (E) Volume XV Issue IV Version I 😡 Year 2015

On the basis of encryption algorithm IDEAnd scheme Lai-Massey developed the networks IDEA32-1 and RFWKIDEA32-1, consisting from one round function [30, 31]. In the networks IDEA32-1 and RFWKIDEA32-1, similarly as in the Feistel network, when it encryption and decryption using the same algorithm. In the networks used one round function having 16 input and output blocks and as the round function can use any transformation.

Using transformation SubBytes(), ShiftRows(), MixColumns(), AddRound-Key() AES encryption algorithm as a round function networks IDEA8-1 [32], RFWKIDEA8-1 [32], PES8-1 [33], RFWKPES8-1 [34], IDEA16-1 [35], created encryption algorithms AES-IDEA8-1 [36], AES-RFWKIDEA8-1 [37], AES-PES8-1 [38], AES-RFWKPES8-1 [39], AES-IDEA16-1 [40].

In this paper developed block encryption algorithm AES-RFWKIDEA32-1 based network RFWKIDEA32-1 using transformation of the encryption algorithm AES. The length of block of the encryption algorithms is 256 bits, the number of rounds n equal to 10, 12, 14 and the length of key is variable from 256 bits to 1024 bits in steps 128 bits, i.e., key length is equal to 256, 384, 512,640, 768, 896 and 1024 bits.

### III. The Encryption Algorithm Aes-Rfwkidea32-1

#### a) The structure of the encryption algorithm AES-RFWKIDEA32-1

In the encryption algorithm AES-RFWKIDEA32-1 as the round function used SubBytes(), ShiftRows(), MixColumns() transformation encryption algorithm AES. The scheme n-rounded encryption algorithm AES-RFWKIDEA32-1 shown in Figure 4, and the length of subblocks X<sup>0</sup>, X<sup>1</sup>, ..., X<sup>31</sup>, length of round keys K<sub>32(i-1)</sub>, K<sub>32(i-1)+1</sub>, ..., K<sub>32(i-1)+31</sub>, i = 1...n + 1 and K<sub>32n+32</sub>, K<sub>32n+33</sub>, ..., K<sub>32n+95</sub> are equal to 8-bits.

Consider the round function of the encryption algorithm AES-RFWKIDEA32-1. Initially 32-bit subblocks  $t_0, t_1, \ldots, t_{15}$  are written into the State array and are executed the above transformations SubBytes(), ShiftRows(), MixColumns(). After the AddRoundKey() transformation we obtain 8-bits subblocks  $y_0, y_1, ..., y_{15}$ .

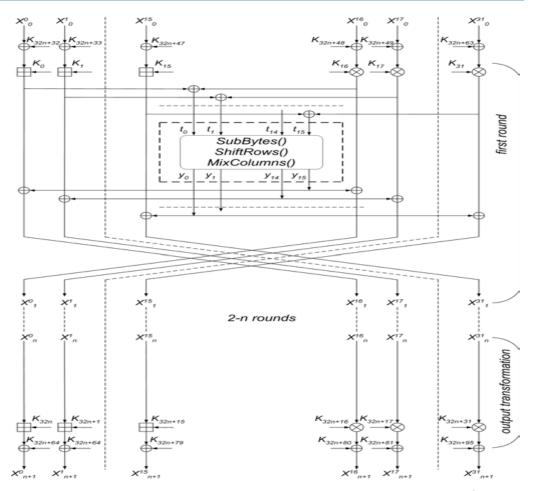


Figure 4: The scheme n-rounded encryption algorithm AES-RFWKIDEA32-1

The S-box SubBytes() transformation shown in Table 1 and is the only non-linear transformation. The length of the input and output blocks S-box is eight bits.

For example, if the input value the S-box is equal to 0xE7, then the output value is equal 0x79, i.e. selected elements of intersection row 0xE and column 0x7.

0x0 0x1	0x2 0x3	0x4 0x5	0x6 0x7	0x8 0x	(9  0xA	0xB  0	xC  0xD	0xE  0xF
0x0  0x87  0x1C	C 0x05 0x06	0x13 0x86	0x84 0xC9		EF  0x85	0xA6 0	x10  0x41	0xA2 0x15
0x1  0xD2 0xF3	0xCA0x0C		0xC5 0x1B	0xA8 0x	(59  0xB3	$0 \times A0   0$	x78  0xB9	0x17  0xDB
0x2  0x21  0x08		0x35 0x24						0x2F  0x6D
0x3  0xFD 0xC1		0x7E 0x71				0x93 0		0x33  0x0D
	0xBC 0x76					0x23 0	xAE 0x83	0xA4 0xF9
0x5  0x47  0x4B	0xFF  0x88	0xBF 0x18	0x2B 0x46	0x96 0x	C2 0x30	0x2E  0	xD6 0xDC	0x5E  0xC0
0x6  0x5B  0x80				0xCD 0x			x5F  0x3C	0x60  0xBA
	3 0xDF 0xE0						x7F  0x00	0x03  0x53
	C 0x67  0x98							
0x9  0x9F  0xEE	B 0xC4 0x58						x50  0x54	0xE6  0x42
0xA  0x9B  0x37	0x36 0xF6	0xCE 0xF5		0xD3 0x	(43  0xB8	0x97 0	x6B  0x69	0x99  0x0E
0xB  0x81  0xDA	A 0x25  0x8C	0xE8 0x49	0xD4 0xAA	4 0x9C 0x	c55  0x19	0x92 0	x8D 0x16	0xB0 0xFE
0xC  0x32  0x1E	l 0xAD 0xB4				48  0x1D	0x64   0	xC6 0x28	0xE2  0xF2
0xD 0x1F 0x34	0x29 0x95	0xDE 0xE7	0x11 0xF4		2D 0x45	0x2A 0	xF1  0xCB	0x6C 0x70
0xE  0x8B  0x1A	0x7A 0x6F	0x8E 0x4A			(74  0xE1	0x8A 0		0xBE 0x40
0xF  0xF8  0xAI	BOXEA 0xEC	10x20 0x91	0xD7 0x9E	0xCF 0x	6A 0xAC	0xE4 0	x3B 0x5D	0x22  0x75

Table 1 : The S-box of encryption algorithm AES-RFWKIDEA32-1

Consider the encryption process of encryption algorithm AES-RFWKIDEA32-1. Initially the 256-bit plaintext X partitioned into subblocks of 8-bits  $X_0^0, X_0^1, \ldots, X_0^{31}$ , and performs the following steps:

1. subblocks  $X_0^0, X_0^1, \ldots, X_0^{31}$  summed by XOR respectively with round key K<sub>32n+32</sub>, K<sub>32n+33</sub>, ..., K<sub>32n+63</sub>.

$$X_0^j = X_0^j \oplus K_{32n+32+j}, \ j = \overline{0...31}.$$

2. subblocks  $X_0^0, X_0^1, \ldots, X_0^{31}$  multiplied and summed respectively with the round keys  $K_{32(i-1)}, K_{32(i-1)+31}$  and calculated 8-bit sub- blocks  $t_0, t_1, \ldots, t_{15}$ . This step can be represented as follows:

$$\begin{split} &t_0 = \left(X_{i-1}^0 + K_{32(i-1)}\right) \oplus \left(X_{i-1}^{16} \cdot K_{32(i-1)+16}\right), \\ &t_1 = \left(X_{i-1}^1 \cdot K_{32(i-1)+1}\right) \oplus \left(X_{i-1}^{17} + K_{32(i-1)+17}\right), \\ &t_2 = \left(X_{i-1}^2 + K_{32(i-1)+2}\right) \oplus \left(X_{i-1}^{19} \cdot K_{32(i-1)+18}\right), \\ &t_3 = \left(X_{i-1}^3 \cdot K_{32(i-1)+3}\right) \oplus \left(X_{i-1}^{19} + K_{32(i-1)+19}\right), \\ &t_4 = \left(X_{i-1}^4 + K_{32(i-1)+4}\right) \oplus \left(X_{i-1}^{20} \cdot K_{32(i-1)+20}\right), \\ &t_5 = \left(X_{i-1}^5 \cdot K_{32(i-1)+5}\right) \oplus \left(X_{i-1}^{21} + K_{32(i-1)+21}\right), \\ &t_6 = \left(X_{i-1}^6 + K_{32(i-1)+6}\right) \oplus \left(X_{i-1}^{22} \cdot K_{32(i-1)+22}\right), \\ &t_7 = \left(X_{i-1}^7 \cdot K_{32(i-1)+7}\right) \oplus \left(X_{i-1}^{24} + K_{32(i-1)+23}\right), \\ &t_8 = \left(X_{i-1}^8 + K_{32(i-1)+8}\right) \oplus \left(X_{i-1}^{24} \cdot K_{32(i-1)+24}\right), \\ &t_9 = \left(X_{i-1}^9 \cdot K_{32(i-1)+9}\right) \oplus \left(X_{i-1}^{25} + K_{32(i-1)+25}\right), \\ &t_{10} = \left(X_{i-1}^{10} + K_{32(i-1)+10}\right) \oplus \left(X_{i-1}^{27} + K_{32(i-1)+26}\right), \\ &t_{11} = \left(X_{i-1}^{11} \cdot K_{32(i-1)+11}\right) \oplus \left(X_{i-1}^{27} + K_{32(i-1)+27}\right), \end{aligned}$$

$$\begin{split} t_{12} &= (X_{i-1}^{12} + K_{32(i-1)+12}) \oplus (X_{i-1}^{28} \cdot K_{32(i-1)+28}), \\ t_{13} &= (X_{i-1}^{13} \cdot K_{32(i-1)+13}) \oplus (X_{i-1}^{29} + K_{32(i-1)+29}), \\ t_{14} &= (X_{i-1}^{14} + K_{32(i-1)+14}) \oplus (X_{i-1}^{30} \cdot K_{32(i-1)+30}), \\ t_{15} &= (X_{i-1}^{15} \cdot K_{32(i-1)+15}) \oplus (X_{i-1}^{31} + K_{32(i-1)+31}), \, , i = 1. \end{split}$$
3. performed SubBytes(), ShiftRows(), MixColumns() transformation. Output subblocks of the round function of the encryption algorithm are  $y_0, y_1, \ldots, y_{31}$ . 4. subblocks  $y_0, y_1, \ldots, y_{31}$  are summed to XOR with subblocks  $X_{i-1}^0, X_{i-1}^1, \ldots, X_{i-1}^{31}, \text{i...} X_{i-1}^j = X_{i-1}^j \oplus y_{15-1}$  $_{j},X_{i-1}^{j+16}=X_{i-1}^{j+16}\oplus y_{15-j},\,j=\overline{0...15},\,i=1.$ 5. at the end of the round subblocks  $X_{i-1}^j$  and  $X_{i-1}^{31-j}$ , 15 swapped, i..,  $j = \overline{1...}$  $\begin{array}{l} X_{i}^{0} = X_{i-1}^{0}, \ X_{i}^{1} = X_{i-1}^{30}, \ X_{i}^{2} = X_{i-1}^{29}, \ X_{i}^{3} = X_{i-1}^{28}, \\ X_{i}^{3} = X_{i-1}^{27}, \ X_{i}^{5} = X_{i-1}^{26}, \ X_{i}^{6} = X_{i-1}^{25}, \ X_{i}^{7} = X_{i-1}^{24}, \\ X_{i}^{8} = X_{i-1}^{23}, \ X_{i}^{9} = X_{i-1}^{22}, \ X_{i}^{10} = X_{i-1}^{21}, \ X_{i}^{11} = X_{i-1}^{20}, \\ X_{i}^{12} = X_{i-1}^{19}, \ X_{i}^{13} = X_{i-1}^{18}, \ X_{i}^{14} = X_{i-1}^{17}, \ X_{i}^{15} = X_{i-1}^{16}, \\ X_{i}^{16} = X_{i-1}^{15}, \ X_{i}^{17} = X_{i-1}^{14}, \ X_{i}^{18} = X_{i-1}^{13}, \ X_{i}^{19} = X_{i-1}^{12}, \\ X_{i}^{20} = X_{i-1}^{11}, \ X_{i}^{21} = X_{i-1}^{10}, \ X_{i}^{22} = X_{i-1}^{9}, \ X_{i}^{23} = X_{i-1}^{8}, \\ X_{i}^{24} = X_{i-1}^{7}, \ X_{i}^{25} = X_{i-1}^{6}, \ X_{i}^{26} = X_{i-1}^{5}, \ X_{i}^{27} = X_{i-1}^{4}, \\ X_{i}^{28} = X_{i-1}^{3}, \ X_{i}^{29} = X_{i-1}^{2}, \ X_{i}^{30} = X_{i-1}^{1}, \ X_{i}^{31} = X_{i-1}^{31}, \\ i = 1. \end{array}$ 

i = 1.

6. repeating steps 2-5 n times, i.e., i = 2...n obtain subblocks  $X_n^0, X_n^1, ..., X_n^{31}$ .

7. in output transformation round keys are multiplied and summed into sub-blocks, i.e.

$$\begin{split} X_{n+1}^{0} &= X_{n}^{0} + K_{32n}, X_{n+1}^{1} = X_{n}^{30} \cdot K_{32n+1}, \\ X_{n+1}^{2} &= X_{n}^{29} + K_{32n+2}, X_{n+1}^{3} = X_{n}^{28} \cdot K_{32n+3}, \\ X_{n+1}^{4} &= X_{n}^{27} + K_{32n+4}, X_{n+1}^{5} = X_{n}^{26} \cdot K_{32n+5}, \\ X_{n+1}^{6} &= X_{n}^{25} + K_{32n+6}, X_{n+1}^{7} = X_{n}^{24} \cdot K_{32n+7}, \\ X_{n+1}^{8} &= X_{n}^{23} + K_{32n+8}, X_{n+1}^{9} = X_{n}^{22} \cdot K_{32n+9}, \\ X_{n+1}^{10} &= X_{n}^{21} + K_{32n+10}, X_{n+1}^{11} = X_{n}^{20} \cdot K_{32n+11}, \\ X_{n+1}^{12} &= X_{n}^{19} + K_{32n+12}, X_{n+1}^{13} = X_{n}^{18} \cdot K_{32n+13}, \\ X_{n+1}^{14} &= X_{n}^{17} + K_{32n+14}, X_{n+1}^{15} = X_{n}^{16} \cdot K_{32n+15}, \\ X_{n+1}^{16} &= X_{n}^{15} \cdot K_{32n+16}, X_{n+1}^{17} = X_{n}^{14} + K_{32n+17}, \\ X_{n+1}^{18} &= X_{n}^{13} \cdot K_{32n+10}, X_{n+1}^{21} = X_{n}^{10} + K_{32n+21}, \\ X_{n+1}^{20} &= X_{n}^{11} \cdot K_{32n+20}, X_{n+1}^{21} = X_{n}^{10} + K_{32n+21}, \\ X_{n+1}^{22} &= X_{n}^{9} \cdot K_{32n+22}, X_{n+1}^{23} = X_{n}^{8} + K_{32n+23}, \\ X_{n+1}^{24} &= X_{n}^{7} \cdot K_{32n+24}, X_{n+1}^{25} = X_{n}^{6} + K_{32n+25}, \\ X_{n+1}^{26} &= X_{n}^{5} \cdot K_{32n+26}, X_{n+1}^{27} = X_{n}^{4} + K_{32n+27}, \\ X_{n+1}^{28} &= X_{n}^{3} \cdot K_{32n+30}, X_{n+1}^{31} = X_{n}^{31} + K_{32n+31}, \\ 8. \text{ subblocks } X_{n+1}^{0} , X_{n+1}^{1} , \dots, X_{n+1}^{31} \text{ are summed} \\ \end{split}$$

K

to XOR with the roundkey key 32n+64, 32n+65, ...,  $K_{32n+95}$ :  $X_{n+1}^j = X_{n+1}^j \oplus \overline{K_{32n+64+j}}, \ j = 0...$  31. As ciphertext plaintext X receives the combined 16-bit subblocks  $X_{n+1}^0 ||X_{n+1}^1|| ... ||X_{n+1}^{31}|$ .

#### b) Key generation of the encryption algorithm AES-RFWKIDEA32-1

In n-round encryption algorithm AES-RFWKIDEA32-1 in each round we applied sixteen (32) round keys of the 8-bit and output transformation sixteen (32) round keys of the 8-bit. In addition, before the first round and after the output transformation we used sixteen (32) round keys of 8-bits. Total number of 8-bit round keys is equal to 32n+96. In Figure 4 encryption used encryption round keys  $K_i^c$  instead of K<sub>i</sub>, while decryption used decryption round keys  $K_i^d$ . If n=10 then need 416 to generate round keys, if n=12, you need to generate 480 round keys and if n=14 need 544 to generate round keys.

When generating round keys like the AES encryption algorithm uses an array Rcon: Rcon=[0x01, 0x02, 0x04, 0x08, 0x10, 0x20, 0x40, 0x80].

The key encryption algorithm K of length I (256  $\leq l \leq$ 1024) bits is divided into 8-bit round keys $K_0^c, K_1^c$ ,...,  $K^c$  Lenght-1, Lenght = l/8, here K =  $\{k_0, k_1, ..., k$  $_{l-1}$ ,  $K_0^c = \{k_0, k_1, ..., k_7\}, K_1^c = \{k_8, k_9, ..., k_{15}\}, ..., K_{l-1}$  ${}^{c}_{Lenght-1} = \{k_{l-8}, k_{l-7}, ..., k_{l-1}\} \text{ and } K = K_{0}^{c} || K_{1}^{c} || ... || ... || K_{1}^{c} || ... || ... || K_{1}^{c} || ... || K_{1}^{c} || ... || ... || K_{1}^{c} || ... || ... || ... || K_{1}^{c} || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ... || ..$  $\stackrel{c}{_{Lenght-1}}$ . Then we calculate  $K_L = K_0^c \oplus K_1^c \oplus ... \oplus K_{Lenght-1}^c$ 1. If  $K_L = 0$  then  $K_L$  is chosen as 0xC5, i.e.  $K_L = 0$ xC5.

When generating a round keys  $K_i^c$ , i = $\overline{Lenght...32n + 95}$ , we used transforma- tion SubBytes() and RotWord8(), here SubBytes()-is transformation 8-bit sub-block into S-box and RotWord8()-cyclic shift to the left of 1 bit of the 8-bit subblock. When the condition imod3 = 1 is true, then the round keys are com- puted as  $K_i^c$  = SubBytes  $(K_{i-Lenght+1}^c) \oplus$  SubBytes( RotWord8  $K_{i-Lenght}^{c}$ ))  $\oplus$  Rcon[imod8]  $\oplus K_{L}$  otherwise  $K_i^c = \text{SubBytes}(K_{i-Lenght}^c) \oplus SubBytes(K_{i-Lenght+1}^c) \oplus$  $K_L$ . After each round key generation the value  $K_L$ is cyclic shift to the left by 1 bit.

Decryption round keys are computed on the basis of encryption round keys and decryption round keys of the output transformation associate with of encryption round keys as follows:

$$\begin{split} & (K_{32n}^d, K_{32n+1}^d, K_{32n+2}^d, K_{32n+3}^d, K_{32n+4}^d, K_{32n+5}^d, K_{32n+6}^d, K_{32n+7}^d, \\ & K_{32n+8}^d, K_{32n+9}^d, K_{32n+10}^d, K_{32n+11}^d, K_{32n+12}^d, K_{32n+13}^d, K_{32n+14}^d, K_{32n+15}^d, \\ & K_{32n+16}^d, K_{32n+17}^d, K_{32n+18}^d, K_{32n+19}^d, K_{32n+20}^d, K_{32n+21}^d, K_{32n+22}^d, \\ & K_{32n+23}^d, K_{32n+24}^d, K_{32n+25}^d, K_{32n+26}^d, K_{32n+27}^d, K_{32n+28}^d, K_{32n+29}^d, \\ & K_{32n+30}^d, K_{32n+31}^d) = (-K_0^c, (K_1^c)^{-1}, -K_2^c, (K_3^c)^{-1}, -K_4^c, (K_5^c)^{-1}, \\ & -K_6^c, (K_7^c)^{-1}, -K_8^c, (K_9^c)^{-1}, -K_{10}^c, (K_{11}^c)^{-1}, -K_{12}^c, (K_{13}^c)^{-1}, -K_{14}^c, \\ & (K_{15}^c)^{-1}, (K_{16}^c)^{-1}, -K_{17}^c, (K_{18}^c)^{-1}, -K_{19}^c, (K_{20}^c)^{-1}, -K_{21}^c, (K_{22}^c)^{-1}, \\ & -K_{23}^c, (K_{24}^c)^{-1}, -K_{25}^c, (K_{26}^c)^{-1}, -K_{27}^c, (K_{28}^c)^{-1}, -K_{29}^c, (K_{30}^c)^{-1}, -K_{31}^c). \end{split}$$

$$\begin{split} & (K_{320}^d, K_{321}^d, K_{322}^d, K_{323}^d, K_{324}^d, K_{325}^d, K_{326}^d, K_{327}^d, K_{328}^d, K_{329}^d, K_{330}^d, K_{331}^d, \\ & K_{332}^d, K_{333}^d, K_{334}^d, K_{335}^d, K_{336}^d, K_{337}^d, K_{338}^d, K_{339}^d, K_{340}^d, K_{341}^d, K_{342}^d, K_{343}^d, \\ & K_{344}^d, K_{345}^d, K_{346}^d, K_{347}^d, K_{348}^d, K_{349}^d, K_{350}^d, K_{351}^d) = (-K_0^c, (K_1^c)^{-1}, -K_2^c, \\ & (K_3^c)^{-1}, -K_4^c, (K_5^c)^{-1}, -K_6^c, (K_7^c)^{-1}, -K_8^c, (K_9^c)^{-1}, -K_{10}^c, (K_{11}^c)^{-1}, -K_{12}^c, \\ & (K_{13}^c)^{-1}, -K_{14}^c, (K_{15}^c)^{-1}, (K_{16}^c)^{-1}, -K_{17}^c, (K_{18}^c)^{-1}, -K_{19}^c, (K_{20}^c)^{-1}, \\ & -K_{21}^c, (K_{22}^c)^{-1}, -K_{23}^c, (K_{24}^c)^{-1}, -K_{25}^c, (K_{26}^c)^{-1}, -K_{27}^c, (K_{28}^c)^{-1}, -K_{29}^c, \\ & (K_{30}^c)^{-1}, -K_{31}^c). \end{split}$$

For example, if the number of rounds is 10 the formula is as follows:

Decryption round keys of the first round associates with the encryption round keys as follows:

$$\begin{split} & (K_0^d, K_1^d, K_2^d, K_3^d, K_4^d, K_5^d, K_6^d, K_7^d, K_8^d, K_9^d, K_{10}^d, K_{11}^d, K_{12}^d, K_{13}^d, K_{14}^d, K_{15}^d, \\ & K_{16}^d, K_{17}^d, K_{18}^d, K_{19}^d, K_{20}^d, K_{21}^d, K_{22}^d, K_{23}^d, K_{24}^d, K_{25}^d, K_{26}^d, K_{27}^d, K_{28}^d, K_{29}^d, K_{30}^d, \\ & K_{31}^d) = (-K_{32n}^c, (K_{32n+1}^c)^{-1}, -K_{32n+2}^c, (K_{32n+3}^c)^{-1}, -K_{32n+4}^c, (K_{32n+5}^c)^{-1}, \\ & -K_{32n+6}^c, (K_{32n+7}^c)^{-1}, -K_{32n+8}^c, (K_{32n+9}^c)^{-1}, -K_{32n+10}^c, (K_{32n+11}^c)^{-1}, \\ & -K_{32n+12}^c, (K_{32n+13}^c)^{-1}, -K_{32n+14}^c, (K_{32n+15}^c)^{-1}, (K_{32n+16}^c)^{-1}, -K_{32n+23}^c, \\ & (K_{32n+18}^c)^{-1}, -K_{32n+19}^c, (K_{32n+20}^c)^{-1}, -K_{32n+21}^c, (K_{32n+22}^c)^{-1}, -K_{32n+23}^c, \\ & (K_{32n+24}^c)^{-1}, -K_{32n+25}^c, (K_{32n+26}^c)^{-1}, -K_{32n+27}^c, (K_{32n+28}^c)^{-1}, -K_{32n+29}^c, \\ & (K_{32n+30}^c)^{-1}, -K_{32n+31}^c). \end{split}$$

Likewise, the decryption round keys of the second, third and n{round associates with the encryption round keys as follows:

$$\begin{split} & (K_{32(i-1)}^d, K_{32(i-1)+1}^d, K_{32(i-1)+2}^d, K_{32(i-1)+3}^d, K_{32(i-1)+4}^d, K_{32(i-1)+5}^d, \\ & K_{32(i-1)+6}^d, K_{32(i-1)+7}^d, K_{32(i-1)+8}^d, K_{32(i-1)+9}^d, K_{32(i-1)+10}^d, K_{32(i-1)+11}^d, \\ & K_{32(i-1)+12}^d, K_{32(i-1)+13}^d, K_{32(i-1)+14}^d, K_{32(i-1)+15}^d, K_{32(i-1)+16}^d, K_{32(i-1)+17}^d, \\ & K_{32(i-1)+18}^d, K_{32(i-1)+19}^d, K_{32(i-1)+20}^d, K_{32(i-1)+21}^d, K_{32(i-1)+22}^d, K_{32(i-1)+23}^d, \\ & K_{32(i-1)+24}^d, K_{32(i-1)+25}^d, K_{32(i-1)+26}^d, K_{32(i-1)+27}^d, K_{32(i-1)+28}^d, K_{32(i-1)+29}^d, \\ & K_{32(i-1)+30}^d, K_{32(i-1)+31}^d) = (-K_{32(n-i+1)}^c, (K_{32(n-i+1)+30}^c)^{-1}, -K_{32(n-i+1)+29}^c, \\ & (K_{32(n-i+1)+28}^c)^{-1}, -K_{32(n-i+1)+27}^c, (K_{32(n-i+1)+26}^c)^{-1}, -K_{32(n-i+1)+27}^c, \\ & (K_{32(n-i+1)+24}^c)^{-1}, -K_{32(n-i+1)+19}^c, (K_{32(n-i+1)+22}^c)^{-1}, -K_{32(n-i+1)+21}^c, \\ & (K_{32(n-i+1)+20}^c)^{-1}, -K_{32(n-i+1)+19}^c, (K_{32(n-i+1)+18}^c)^{-1}, -K_{32(n-i+1)+17}^c, \\ & (K_{32(n-i+1)+16}^c)^{-1}, (K_{32(n-i+1)+17}^c)^{-1}, -K_{32(n-i+1)+18}^c)^{-1}, -K_{32(n-i+1)+19}^c, (K_{32(n-i+1)+19}^c)^{-1}, \\ & -K_{32(n-i+1)+18}^c, (K_{32(n-i+1)+17}^c)^{-1}, -K_{32(n-i+1)+16}^c, (K_{32(n-i+1)+5}^c)^{-1}, \\ & -K_{32(n-i+1)+8}^c, (K_{32(n-i+1)+3}^c)^{-1}, -K_{32(n-i+1)+27}^c, (K_{32(n-i+1)+19}^c)^{-1}, \\ & -K_{32(n-i+1)+8}^c, (K_{32(n-i+1)+17}^c)^{-1}, -K_{32(n-i+1)+19}^c, (K_{32(n-i+1)+19}^c)^{-1}, \\ & -K_{32(n-i+1)+18}^c, (K_{32(n-i+1)+17}^c)^{-1}, -K_{32(n-i+1)+16}^c, (K_{32(n-i+1)+5}^c)^{-1}, \\ & -K_{32(n-i+1)+8}^c, (K_{32(n-i+1)+3}^c)^{-1}, -K_{32(n-i+1)+27}^c, (K_{32(n-i+1)+17}^c)^{-1}, \\ & -K_{32(n-i+1)+8}^c, (K_{32(n-i+1)+3}^c)^{-1}, -K_{32(n-i+1)+27}^c, (K_{32(n-i+1)+5}^c)^{-1}, \\ & -K_{32(n-i+1)+8}^c, (K_{32(n-i+1)+3}^c)^{-1}, -K_{32(n-i+1)+27}^c, (K_{32(n-i+1)+5}^c)^{-1}, \\ & -K_{32(n-i+1)+8}^c, (K_{32(n-i+1)+3}^c)^{-1}, -K_{32(n-i+1)+27}^c, (K_{32(n-i+1)+17}^c)^{-1}, \\ & -K_{32(n-i+1)+31}^c, (K_{32(n-i+1)+3}^c, K_{32(n-i+1)+27}^c, (K_{32(n-i+1)+17}^c, K_{32(n-i+1)+17}^c, K_{32(n-i+1)+3}^c, K_{32(n-i+1)+37}^c, K_{32(n-i+1)+37}^c, K_$$

Decryption round keys applied to the \_rst round and after the output transformation associated with the encryption round keys as follows:  $K^d_{32n+32+j} = K^c_{32n+64+j}$ ,  $K^d_{32n+64+j} = K^c_{32n+32+j}$ ,  $j = \overline{0...31}$ .

#### IV. Results

Using the transformations SubBytes(), ShiftRows(), MixColumns() of the encryption algorithm AES as the round function network RFWKIDEA32-1 we developed encryption algorithm AES-RFWKIDEA32-1. In the algorithm, the number of rounds of encryption and key's length is variable and the user can select the number of rounds and the key's length in dependence of the degree of secrecy of information and speed encryption.

As in the encryption algorithms based on the Feistel network, the advantages of the encryption

algorithm AES-RFWKIDEA32-1 are that, when encryption and decryption process used the same algorithm. In the encryption algorithm AES-RFWKIDEA32-1 in decryption process encryption round keys are used in reverse order, thus on the basis of operations necessary to compute the inverse. For example, if the round key is multiplied by the subblock, while decryption is is necessary to calculate the multiplicative inverse, if summarized, it is necessary to calculate the additive inverse.

It is known that the resistance of AES encryption algorithm is closely associated with resistance S-box, applied in the algorithm. In the S-box's encryption algorithm AES algebraic degree of nonlinearity deg = 7, nonlinearity NL = 112, resistance to linear cryptanalysis  $\lambda = 32 = 256$ , resistance to differential cryptanal ysis  $\delta = 4/256$ , strict avalanche criterion SAC = 8, bit independence criterion BIC = 8.

In the encryption algorithm AES-RFWKIDEA32-1 resistance S-box is equal to resistance S-box's encryption algorithm AES, i.e., deg = 7, NL = 112, \_ = 32=256, \_ = 4=256, SAC= BIC=8.

#### V. Conclusions

It is known that as a network-based algorithms Feystel the resistance algorithm based on network RFWKIDEA32-1 closely associated with resistance round function. Therefore, selecting the transformations SubBytes(), ShiftRows(), Mix-Columns() of the encryption algorithm AES, based on round function network RFWKIDEA32-1 we developed relatively resistant encryption algorithm.

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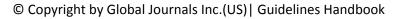
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References	Complete and correct format, well organized	Beside the point, Incomplete	Wrong format and structuring

# INDEX

### С

Cullagh · 6

#### Κ

Kalaichelvan, • 23 Khosroshahy • 21, 26 Korowajcszuk • 21

### Ρ

PengXie · 18

# S

Scarfone · 22, 24, 26, 27, 30

# T

Teixeira,  $\cdot$  16 Tripartite  $\cdot$  3, 6, 10, 12

# R

Rijndael · 14, 16, 24, 26, 45, 53, 55

# T

Tuychiev · 45, 55, 56



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