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Probability of Semantic Similarity

Graphic Interface Applied

Highlights

High Speed AES Algorithm

Fast Stereo Images Compression

Discovering Thoughts, Inventing Future

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Probability of Semantic Similarity and *N-Grams* Pattern Learning for Data Classification

By V. Vineeth Kumar & Dr. N. Satyanarayana

Jawaharlal Nehru Technological University

Abstract- Semantic learning is an important mechanism for the document classification, but most classification approaches are only considered the content and words distribution. Traditional classification algorithms cannot accurately represent the meaning of a document because it does not take into account semantic relations between words. In this paper, we present an approach for classification of documents by incorporating two similarity computing score method. First, a semantic similarity method which computes the probable similarity based on the Bayes' method and second, n-grams pairs based on the frequent terms probability similarity score. Since, both semantic and N-grams pairs can play important roles in a separated views for the classification of the document, we design a semantic similarity learning (SSL) algorithm to improves the performance of document classification for a huge quantity of unclassified documents.

Keywords: *semantic similarity, classification, naive bayes, n-grams pattern.*

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Probability of Semantic Similarity and *N*-Grams Pattern Learning for Data Classification

V. Vineeth Kumar ^α & Dr. N. Satyanarayana ^σ

Abstract- Semantic learning is an important mechanism for the document classification, but most classification approaches are only considered the content and words distribution. Traditional classification algorithms cannot accurately represent the meaning of a document because it does not take into account semantic relations between words. In this paper, we present an approach for classification of documents by incorporating two similarity computing score method. First, a semantic similarity method which computes the probable similarity based on the Bayes' method and second, *n*-grams pairs based on the frequent terms probability similarity score. Since, both semantic and *N*-grams pairs can play important roles in a separated views for the classification of the document, we design a semantic similarity learning (SSL) algorithm to improves the performance of document classification for a huge quantity of unclassified documents. The experiment evaluation shows an improvisation in accuracy and effectiveness of the proposal for the unclassified documents.

Keywords: semantic similarity, classification, naive bayes, *n*-grams pattern.

1. INTRODUCTION

Web mining is facing an important problem in measuring the semantic similarity among the words in the process of information retrieval and language processing. The most semantic based application requires the accurate measuring of semantic similarity among the document concepts and words. In information search, one of the most important problems is to semantically to get a number of documents correlated to a user's request. Semantic similarity between the words such as "word sense disambiguation" (WSD) can be an efficient assessment for the text entailment and automatic document classification, it is also important for the variety of natural language processing tasks. Automatic classification of documents is an important part of the research in the vision, and an enormous prospective for numerous applications around the text, such as search and analysis. Its purpose is to allocate a document given to the group of default to which it is in the right places. So far, applications have different types of algorithms based on the study or automatic calculation in this process and showed how much work [2], [3], [5]. However, mainly of the work functional to this task used aneffortless word-collection representation where each attribute

communicates to a particular word. That is, assume that words are independent and utilize only the distribution of content words.

Over the past few years, we've seen the Web evolve into a semantic Web. The amount of information posted with linked data has consistently increased. With this increase, annotation and classification systems have created new opportunities to reuse this data as a semantic knowledge base and can be interconnected and structured to increase the accuracy and recovery of annotation and classification mechanisms. The Semantic web aims to explain the meaning of the information posted on the Web in order to make it possible to search by understanding the meaning of the information accurately. In this regard, document text learning and classification is most common, by assigning text to one or more existing class. This development determines the class membership of a text document that has a separate set of classes with profiles and different features. Criteria for deciding appropriate features for classification are important and are determined by the priority of the classifier. Semantic classification occurs when the target document element or term of the classification represents the meaning of the document.

Measuring the semantic similarity among texts is a basic task and can be capable of being utilized for a variety of applications, together with "text clustering" [1] and "text classification" [2]. The challenge in evaluation similarities among texts is infrequent, that is, there will be no coincidence of terms between the two texts. For example, two texts "Apple's New Product" and "iPhone-6" refer to related topics, even though they do not use similar terms.

To overcome scarcity, we need to use external data or knowledge to enrich the semantic representation of text. The semantically associated words of a particular word are listed in a manually created universal dictionary vocabulary ontology such as "Word Net". In this, a synset includes a set of synonyms for a specific word sense. However, semantic similarities among individual transform more than time and across domains. For example, apples are often associated with computers on the web. However, this apple sensation is not listed in most universal thesauri or dictionaries. Users searching for apples on the web may be concerned in the meaning of "apple" and "not apple" as a fruit. Innovative words are stably generated and new

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senses are dispensed to existing words. Preserving ontology manually to confined these innovative words and senses is costly, if not impracticable.

In this paper, we contribute an automated semantic similarity learning (SSL) move towards to compute the probability of semantic similarity among terms or entities of documents with the class knowledge set entities. Here, we define two probabilistic scores, a semantic similarity (SS) score and *N*-grams pair similarity (GS) score enhancing Naive Bayes probabilistic method to aggregate the relation between document and class entities. Semantic Similarity method relates the trained class entities terms with the extracted document key terms to compute the document probable SS score against each class entities, and *N*-grams pair similarity method relate a document with each trained class entity with the constructed *N*-grams pairs, which is constructed using most frequent terms extracted from the document and the probable GS score is the summation of all individual *N*-grams pairs, i.e., $sum(GS_1, GS_2, \dots, GS_n)$. We perform an experiment evaluation on Reuters-21578 Datasets to demonstrate the effectiveness of the proposal.

This papers organized in 6 sections. Section-1 above describes the introduction, section-2 discuss the background works, section-3 presents the proposed works outline, probabilistic semantic and N-gram pairs pattern learning, section-4 discuss the semantic similarity classification approach, section-5 present experiment methodology and results and finally section-6 presents the conclusion of the work.

II. BACKGROUND STUDY

Semantic similarity plays an significant responsibility in "natural language processing", "information retrieval", "text summarization", "text classification", and "text clustering". Particularly, "Explicit Semantic Analysis" (ESA) [6] is extensively utilized because of its accessibility and diversity. ESA was build up to calculate word relationship as well as text comparison in natural language. ESA creates a "weighted index" that maps each phrase to the listing of articles that appears and calculates the similarity among the two words or a vector of text.

Naive Bayes [1] classification performance using semantic similarity has made various efforts. An approach that is often used to mitigate naive independent assumptions is to express attribute addiction in a graph-based model called a "Bayesian network", where nodes correspond to attributes. Oriented arch is weighted by the circumstances probability for each node specified a close relation. Because "Bayesian network learning" is NP-hard [6], numerous approaches recommend imposing model constraints to formulate it easier to deal with learning problems.

Subsequent approaches in [8], [9], [17], [18] have brought considerable improvements. For example, in [21], an ensemble of Tree Augmented Naive-Bayes (TANs) was be trained, each rooted in a dissimilar attribute. It then compiles the classifications of all eligible TANs to predict class labels. In [8], we assume that the entire Bayesian network structure is learned first and all attributes are dependent. Unlike [18], the "Markov network model" is utilized to express characteristic dependencies that are estimated similar to [39] by taking advantage of the conditional log probability intention purpose. However, performing andtake advantage operation can be computationally demanding. Many methods are used to inherit the structural simplicity of Naive Bayes classifiers to keep away from the complication of the construction learning process [9],[10],[12],[13]. While the "Naive Bayes classification" is functional at the "decision tree leaves level" and is act upon on a subset of the training data, the data set properties are divided into two collections as in [11], where one group is assigned a class probability based on "Naive Bayes", and the other is supported on a "decision table".

Despite its effortlessness, the previously point out the classifier still shows a few constraints in handling very much related data. In [12], [13], the features are weighted dissimilarly depending on the involvement to the classification. A comparable approach was applied to the most effective "Bayesian Network classifier" and "Hidden Naive Bayes" [9]. In [9], the authors recommended generating a hidden close qualified that correspond to the effect of everything else on each property. The effect is computed as a linear arrangement of circumstance common information among attribute pairs, similar to [8]. Therefore, the parent correlation is ignored.

Dissimilarity like [10], [11], [19], "En Bay" [2] implements a new, uncomplicated, and useful approach that unites the generation of conditionally independent decision models and the reliable probability approximation by class. En Bay is a pattern-based Bayesian classifier that frequently uses a set of items frequently to estimate Bayesian probabilities. En Bay uses new and effective probabilistic approximation estimates that adhere to the conditional independence model. The set of extended, normal and separate items to be comprised in a class-based approximation is chosen by entropy-based heuristics, and the set of properties is conditionally mutually independent, depending on the class being evaluated. We extend En Bay probability computation methods to computes the semantic similarity probability score based on the terms dependency over the trained class terms entities, as discussed in section 3.2 below.

The "Large Bayes classifier" [11] performed the primary challenge to mitigate well-built independent

assumptions using a lengthy and frequent set of items to estimate the probability through product form approximation [12]. However, all preceding pattern-based Bayesian advance create inimitable product approximations for all test cases. Thus, estimations are only tied to the considered grade. Moreover, since it is necessary to extract an immense number of long and redundant repeated item sets, the superiority of the approximation is sensitive to changes in the "support threshold", and the classification algorithm cannot cope with a large data set. We extend this constructing *N*-grams pairs using frequent items to estimate the *N*-

grams similarity probability score, as discussed in section 3.3 below.

III. PROPOSED APPROACH

a) Outline

The Semantic Similarity Learning (SSL) method, which uses probabilistic performances to describe probabilistic scores and put together scores supported on Bayes' method for accurate document classification to measure the robust discovery and semantic similarity of related entities to document.

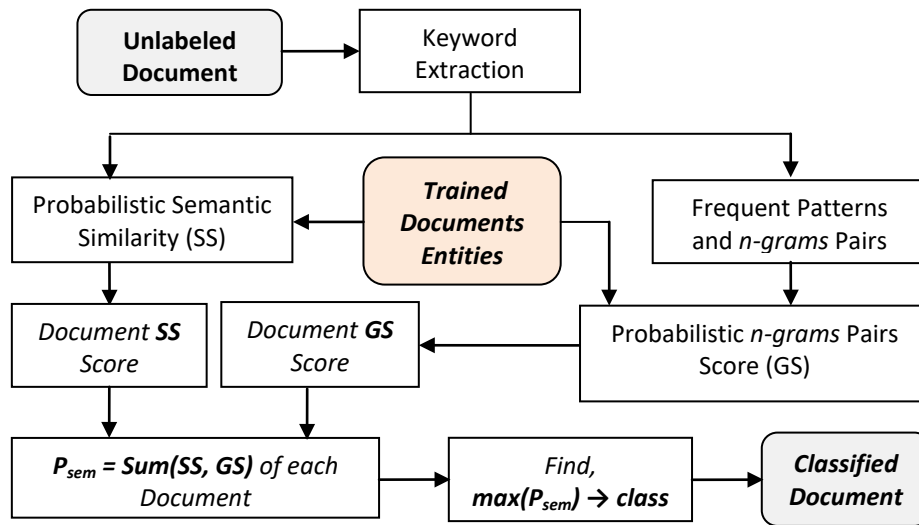


Figure 1: Outline of the proposed Approach

Fig.1. outlines our the proposed approach method. Our method obtains the main points of the probabilistic analysis of associations and related documents on the basis of trained document entities. The approach performs two probabilistic score computation method. First, Semantic Similarity Method which measures the similarity of their associated entity of a document with the list of trained class entity terms by $SS_k = P(d_k(t) | C_m)$, i.e., probability of a document $d_k(t)$ terms associated with a set of class C_m terms by means of cosine similarity.

The second method extracts the most frequent terms F from the extracted document terms using term frequency (tf) and using F we construct *N*-grams pairs. In general, an *N*-gram method slice a longer text into *n*-characters, but we customized this to slice a pattern into number words pairs (*V-Pair*) based on n which we term as *N*-gram pattern, an illustration is shown in Fig. 2. Using the constructed pairs we compute, $GS_k = \sum_{i=1}^n W_i$, where n is the number of pairs and $W_i = P(V-Pair_n | C_m)$ i.e., probability of *N*-gram pair terms related to the set of class C_m terms using cosine similarity.

Now, we compute the final probability of semantic similarity $P_{sem} = \text{sum}(SS_k, GS_k)$ for each document against each trained class. To classify the document we find the $\max P_{sem}$ among the computed probability of semantic similarity of each class. The class which has the $\max P_{sem}$ will be considered as the document class. We describe each method mechanism in the aspect in the following sections.

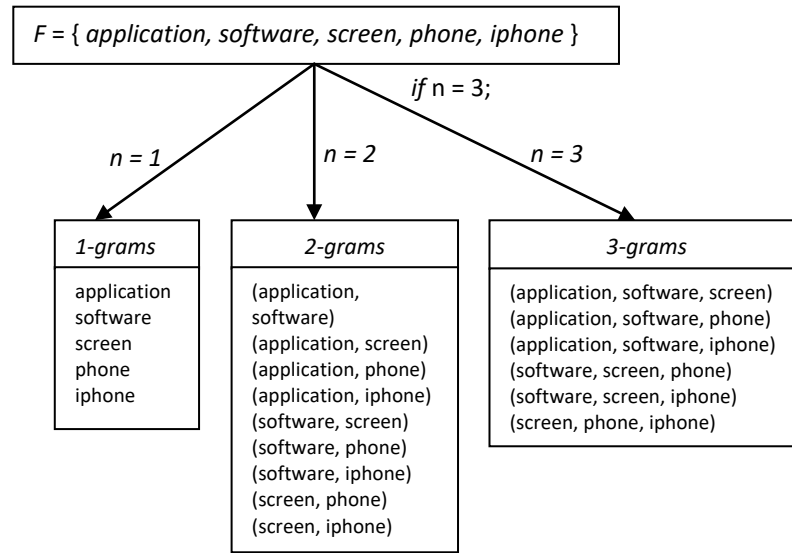


Figure 2: An illustration of *N*-gram pairs

b) Probabilistic Semantic Similarity

Classification of classifiers named a set of classes that train classifiers built from sets of the abstract model defined aims. Then use the categorizer to properly classify new data whose class labels are unknown. Various approaches have been proposed to make accurate classifiers such as, "Bayesian classifiers"

[1], "Decision Trees" [2], "SVMs"[3], "Rule-based" [4], and "Associative classifiers" [5].

Bayesian classification methods recognized a classification supported on the of "Bayes theorem" [1].It predicted that a class based on test documents previously un seen $T = \{ a_1, a_2, \dots, a_n \}$ by opting the class c_i that make the most of the subsequent formula:

$$P(c_i | T) = \frac{P(T, c_i)}{P(T)} = \frac{P(c_i) \cdot P(T | c_i)}{P(T)}, \tag{1}$$

Where $P(T | c_i)$ indicates the provisional possibility of the test document T of a given class c_i . Probability is approximate commencing from the training set. Since classification focuses on choosing the class that takes advantage based on the equation (1), relatively than assigning an unambiguous probability to each class, denominator $P(T)$ in (1) can be misplaced because it does not influence the comparative class instruct.

Despite the simplicity, the Bayesian approach is calculation intractable without compelling a powerful model simplification [1], [6], [7]. The most important instance of simplification is the "Naive Bayes classifier" [1], which solves the problem by assuming that all attributes are conditionally self-determined and given as the class c_i . Therefore, the join probability of (1), is based on the generated Naive Bayes model, which can be approximated as,

$$\begin{aligned} P(T, c_i) &= P(a_1, a_2, \dots, a_n, c_i) \\ &\simeq P(c_i)P(a_1 | c_i)P(a_2 | c_i) \cdots P(a_n | c_i) \\ &= P(c_i) \prod_{j=1}^n P(a_j | c_i). \end{aligned} \tag{2}$$

Based on the approximation we combine the probabilistic semantic similarity (SS) scores extracted from the training data to find the appropriate entities for the document. Let's assume that multiple key terms are entered as input. That is, we compute $P(c | T)$ for the set of core key terms $T = \{t_1, t_2, \dots, t_k\}$, where T is a key term, which are derived using traditional Naive Bayes for any related class c_i .

One possible approach to this task is a two-step method of determining the key terms first and then applying the existing Naive Bayes. However, this approach raises the question of how key terms are established. We have developed a probabilistic similarity method for finding related entities. It can be functional to a set with probability determined members.

For a particular, a set of key terms $T, P(c|T)$ is calculated for all probable states T . Fig.3, summarizes an illustration of the probabilistic semantic similarity method for a set of key terms of a document d_k as t_1, \dots, t_k . SS

method is utilized to calculate $P(t_k|C_m)$, which is the probability score SS_k of the set of key terms, T for the class C_m .

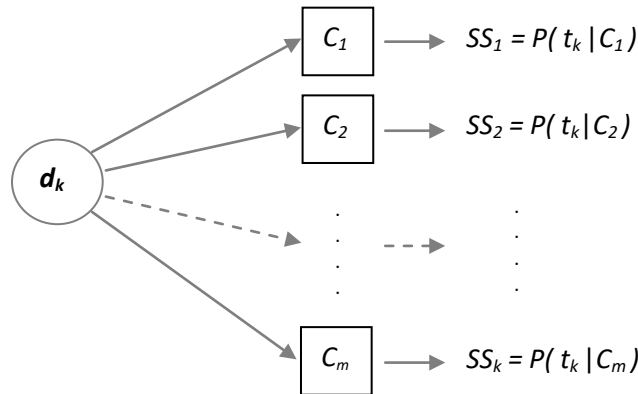


Figure 3: Probabilistic Semantic Similarity Method

Unfortunately, the circumstances of self-determination supposition prepared by Naive Bayes may not true always, to obelieves high-order associations for the period of the probability estimate, a parallel proposal is made based frequent pattern learning in T , and constructing a *N*-grams pairs to support accurate classification.

For a given document d having a T terms. Let's assume the frequent terms represent as F . Using the F terms we construct *N*-grams pairs as *V*-Pair. To learn the probability of *V*-pair pattern association W_n of a document with a class c_m we calculate $P(V\text{-Pair}_n|c_m)$ as shown in Fig .3. Here, the class must contain all the pair terms to match the association. To compute the *N*-grams probable similarity GS , we done the summation of all W_n as,

c) *N*-grams Pattern Learning

The *N*-gram is defined as a sequence of terms, the length is n , and the words taken are called terms. In the literature, we can see the definition of an *N*-gram as a concurrent set of terms, but only consecutive term sequences were used in this study. One word in the document is represented by a set of overlapping *N*-grams as shown in Fig. 2. The *N*-gram model can be fictional by introduction a small window over a sentence or text, where only n words can be seen at the same time. So the effort less *N*-gram model is the so-called "unigram model". This is a one-word model at a time. For example, the "Latest application and iPhone released." sentence contains five unigrams as, "Latest", "application", "and", "iPhone" and "released" Of course, this is not very beneficial information. It is just a word that makes up the sentence. In fact, *N*-grams are interesting when n is greater than 2 (bigram) or more.

$$GS_1 = \sum_{i=1}^n W_i$$

Each word happens in a document with a dissimilar frequency. The main thought of categorization utilized by Trenkle and Cavnar [5] is that they should have similar *N*-gram frequency distributions when comparing documents of the same category. We perform *N*-gram pattern learning through creating n pairs using frequent document terms.

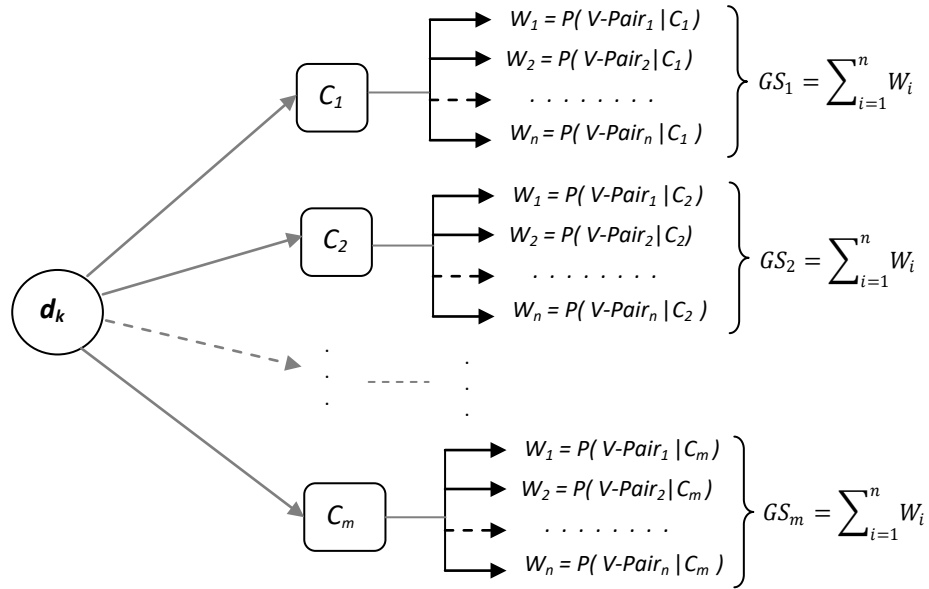


Figure 4: Probabilistic N-grams Similarity Method

IV. SSL BASED CLASSIFICATION

a) Training

In order to efficiently search for class-specific patterns, in the SSL training phase, an FP-growth data pattern representation [18] is separately created to store training data belonging to each class in a compressed form. The FP-growth pattern is a single-tree data

structure for the minimum support (*min_sup*) frequent item set used in the class pattern learning context. Algorithm-1 shows the pseudo-code in the SSL training phase. Minimum supported thresholds were applied to remove infrequently used items. In this case, items that do not meet the necessitated minimum support threshold are not consisted of in the FP-growth pattern.

Algorithm 1. SSL Training Phase (*D, min_sup*)

Input: The training set *D* and the minimum support threshold *min_sup*

Output: FP-G = { *T_i* } ∀ *c_i* ∈ *C*, a FP-Tree for each class belonging to the training class set *C*

for all *c_i* in *C* **do**

$ac_i = \text{set of all items belonging to class } c_i$

$FT_i = \text{ExtractPattern}(ac_i, min_sup)$

$FP-G = FP-TU \{ FT_i \}$

end for

return FP-G.

The obtained FP-G of each individual class *c_i*, will be used as a trained knowledge for the SSL classifier.

b) SSL Classification

The SSL classification approach is one of the accomplished algorithms managing unlabeled documents. It applies two probability computation as semantic similarity SS and N-gram Similarity G Son the dataset to perform the classification using the trained FP-growth pattern knowledge as shown in Fig.1. The SSL classifier initialized with anonly some trained class

item sets. At each iteration, it chose an unlabeled document and perform the computation to compute the SS and GS score. It learns separate similarity score over each class pattern learning, and support a set of class labels for the unlabeled documents. For each class *c_i* belonging to the training data set is corresponding to FP-growth is visited to construct the class-centric product estimation and calculated the probability $P(T, c_i)$.

Its ultimate prediction is through by coming together both SS and GS score, $P_{sem} = sum(SS_k, GS_k)$ which decline classification error spredictions. The massive is the P_{sem} of the class will be predicated as the

closer association. This prediction provides the performance of the algorithm classification accuracy. Since SSL classification uses two probability computation score with the FP-growth pattern, its presentation is better than any particular classifier. Algorithm-2 briefly summarizes the SSL classification algorithm.

Algorithm 2. SSL Classification

Input: Document Terms D and the class set $FP-G = \{FT_i\}$

Output: Classified class of D .

```

for all  $d$  in  $D$  do
  //-- For all document in  $D$  --
  // -- Probability of Semantic Similarity(SS) Score
   $d_k = \{T_k\} \forall D_k$ ;
  //-- For all class in FP-G vector --
  for all  $c_i$  in FP-G do
     $c_i = \{FT_i\} \forall FP-G_i$ ;
     $SS_i = P(d_k \in c_i)$ ;
     $V\_SS[i] = SS_i$ ;
  end for
   $VD_s[k] = V\_SS$ ;
  // -- Probability of N-grams Similarity(GS) Score
  //-- Most frequent terms--
   $F = frequent\_Terms(d_k, min\_sup)$ ;
  //-- Builds N-grams Patterns --
   $NP = BuildPattern(F, n)$ ;
  //-- For all n-grams patterns --
  for all  $ng_p$  in NP do
     $ng\_terms = ng_p$ ;
    //-- For all class in FP-G vector --
    for all  $c_i$  in FP-G do
       $c_i = \{FT_i\} \forall FP-G_i$ ;
       $GS_i = P(ng\_terms \in c_i)$ ;
       $V\_GS[i] = GS_i$ ;
    end for
  end for
   $VD_g[k] = V\_GS$ ;
  // -- Summation Probability --
  for all  $c_i$  in FP-G do
     $SS\_P_{sem} = VD_s[i]$ ;
     $GS\_P_{sem} = VD_g[i]$ ;
     $P_{sem} = sum(SS\_P_{sem}, GS\_P_{sem})$ ;
     $VP_{sem}[i] = P_{sem}$ ;
  end for
   $pmax = findMax(VP_{sem})$ ;
   $d_k\_class = getClass(pmax, C)$ ;
end for

```

The obtained d_k_class class from C is determined by the summation of two probabilities scores. It classifies the most excellent class of document d is set to the individual with the maximum probability:

V. EXPERIMENT

a) *Datasets*

The "Reuters-21578 corpus" is the mainly common utilized benchmark corpus in text classification. It consists of over 20,000 Reuters news stories from 1987 to 1991, and 135 subject classes are used in the experiment. This version contains "9603 training documents", "3299 test documents", and "27,863 inimitable words" after stopping stemming and word removal. We consider only 10 topics as classes of Reuters-21578 data for experimental evaluation measurements.

b) *Performance Measure*

To estimate the classification performance of the proposed method, we utilize the precision, recall, and accuracy. Let considered P is all relevant documents and N is all negative document. PC_+ as a positively classified, NC_+ as negatively classified documents. PC_- as a positively classified for an incorrect document, NC_- as negatively classified for correct documents. By constructing a confusion matrix for the above evaluation measure we compute the classifier performance.

To measure the classifier precision rate CP , the classifier recall rate CR and the classifier accuracy rate CA the following equation are used.

$$CP = \frac{PC_+}{PC_+ + NC_+} \tag{4}$$

$$CR = \frac{PC_+}{PC_+ + NC_-} \tag{5}$$

$$CA = \frac{PC_+ + NC_+}{P + N} \tag{6}$$

c) *Evaluation Results*

In the Reuters-21578 datasets we do consider both labeled and unlabeled documents, the effect of using two probabilistic semantic similarity learning is given in Table 1. We initially evaluate with Semantic Similarity Score (SS), then with N-grams patterns pairs (GS) and finally with both. The classification performance using both the Semantic Similarity and the N-gram pattern pairs learning outperforms over the one using any single learning for most classes.



Table 1: The accuracy enhancement by using semantic similarity learning on "Reuters-21578 corpus".

Class	Relevant documents	Semantic Similarity	N-gram pattern pairs	Both
Acq	1650	1591	861	1629
Corn	181	93	92	168
Crude	389	328	146	354
Earn	2895	2765	1621	2825
Grain	433	396	208	426
Interest	347	284	159	341
Money-fx	538	327	179	493
Ship	197	106	68	188
Trade	369	235	97	355
Wheat	212	184	102	206

We found that the greater the number of related documents in the training set, the higher the accuracy of using *N*-gram pattern pair learning. This is because

Naive Bayes has a low error rate and high accuracy when there are many documents in the class. The classification comparison result is shown in Fig.4.

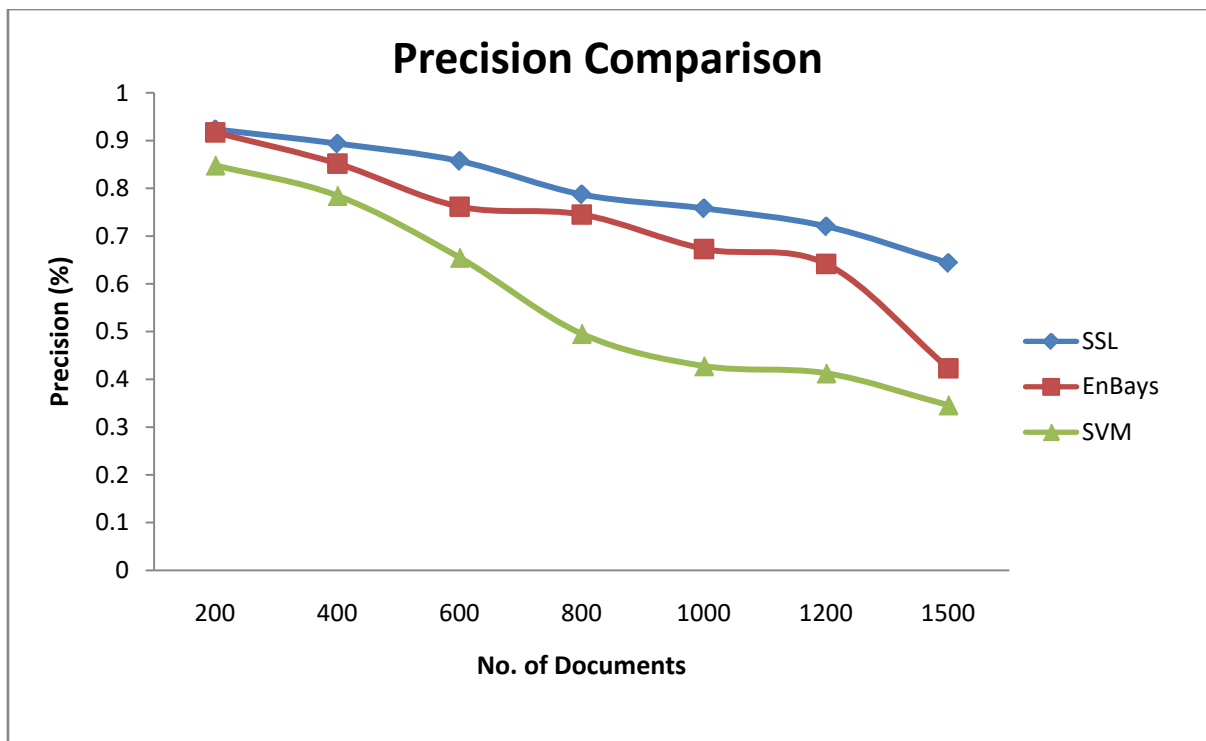


Figure 4: Classification Comparison

Accuracy measures the capability of a taxonomy to correctly classify unlabeled data. The ratio of the number of correctly categorized data to the number of given data, including accurate and incorrect classification. Experimental results show that average SSL outperforms other classified segments. The statistical significance of improving SSL accuracy is discussed below.

At first, we performed comparisons with state-of-the-art Bayesian classifiers. And because our approach is pattern-based, we compare it with the well-known associative classifiers SVM and the new improved Bayesian approach known as En Bays [2].

Finally, we performed a comparative assessment of precision, recall, and accuracy rates as a classifier for classifiers.

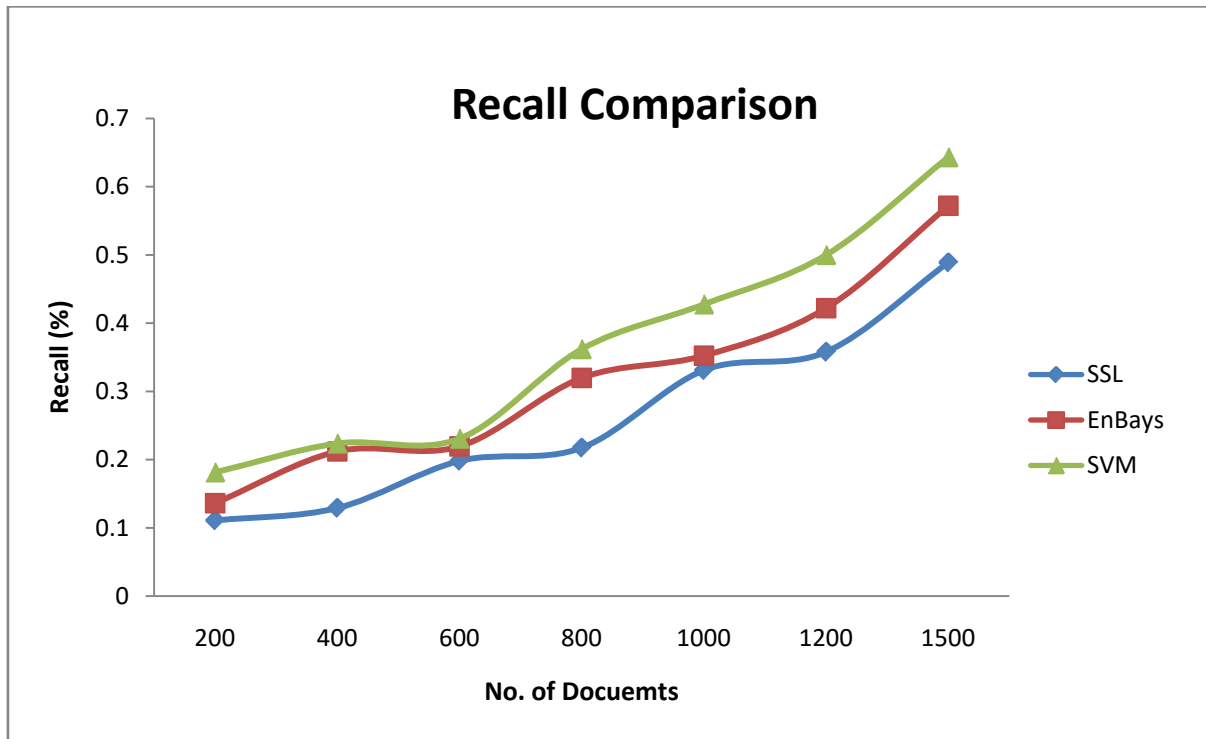


Figure 5: Precision Comparison

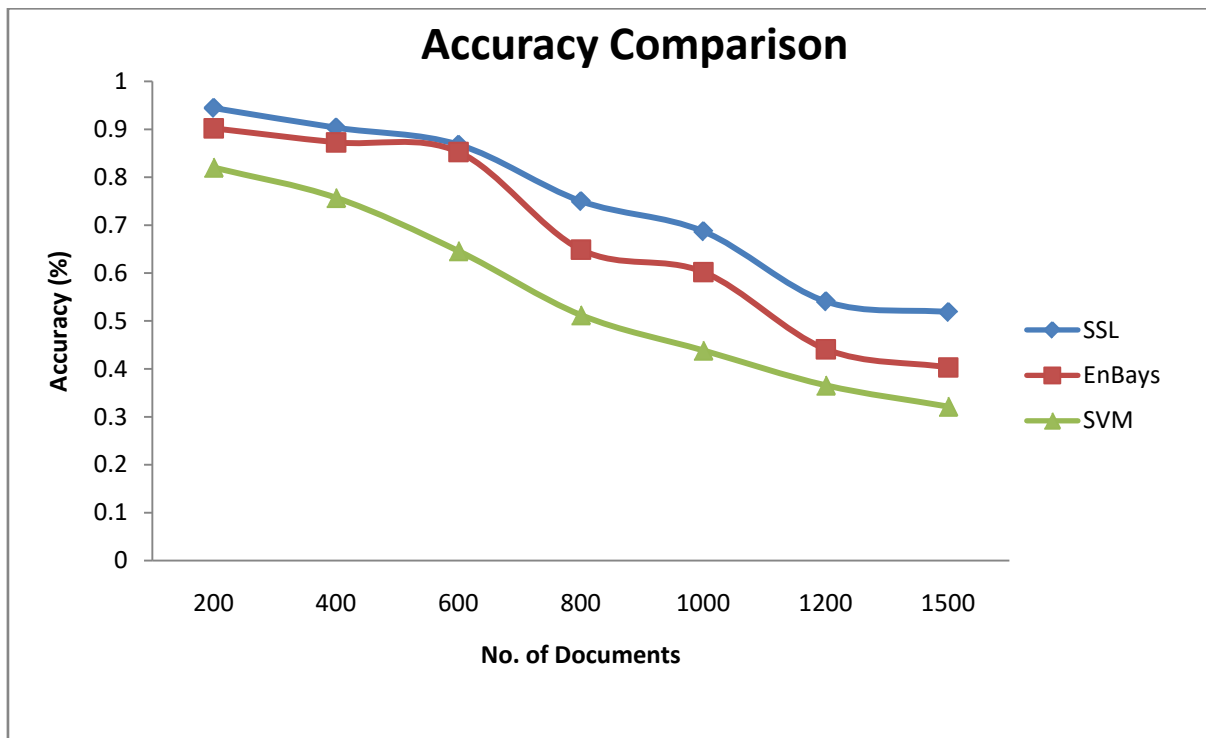


Figure 6: Recall Comparison

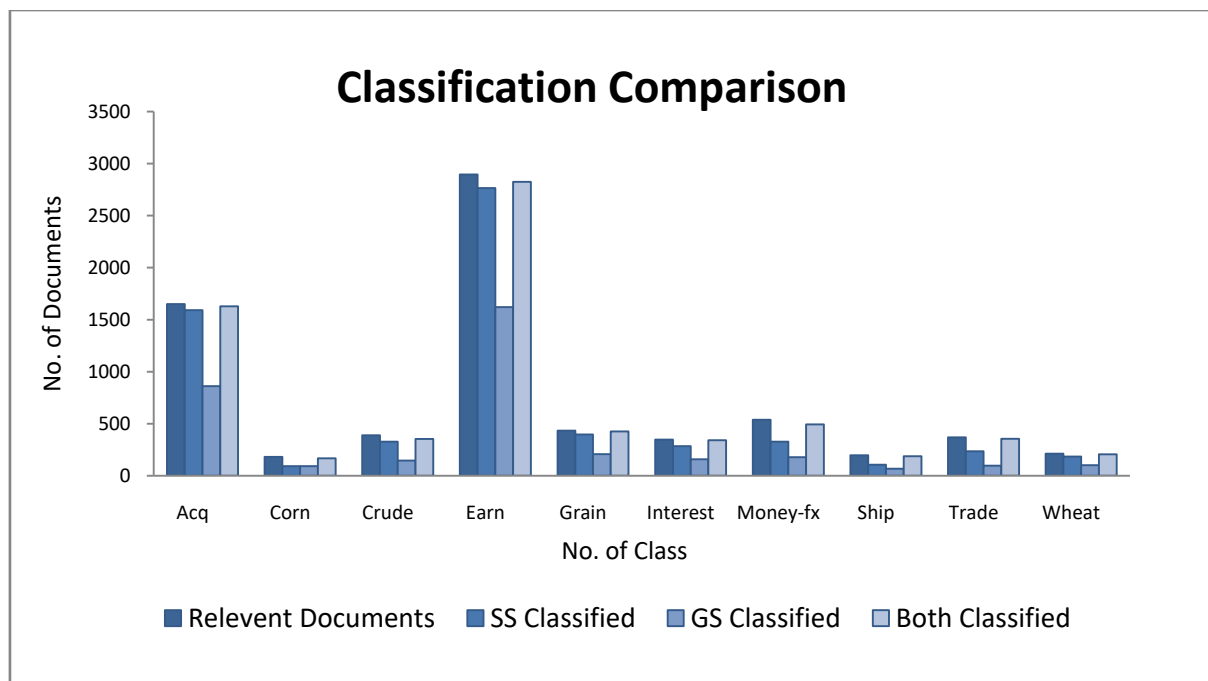


Figure 7: Accuracy Comparison

The rate of precision and recall in Fig. 5 and 6 shows an improvisation in compared to SVM and En Bays method. The effects of both SS and GS score in probability similarity measure shows SSL precision improvisation. Fig. 7 shows the classifier accuracy measures comparison. It also shows an improvisation of SSL approach in compare to others. The falling of accuracy with increasing of the document due to the limitation of trained class knowledge. As both the method has a dependency on the trained data knowledge for performing probability similarity computation cause the falling of the rate.

VI. CONCLUSION

In this paper, we propose a semantic similarity and *N*-gram pattern learning method based on the Bayesian classifier, which approximates Bayesian probability using frequent itemsets. It utilized new and more efficient probability approximations that adhere to the conditional independence model. A long, frequent, and separate set of items to be included in a class-based approximation is selected. It is based on the Baye's theorem and semantic similarity computation approach. Our method is a sort of probabilistic semantic similarity learning (SSL) that uses vectors to generate vectors of related entities as semantic representations of specific text and to measure semantic similarities. SSL combines vectors using expanded Naive Bayes, while SSL simply adds up the vectors for each term occurring in the text based on the majority of rules. This method uses both Semantic Similarity Learning for SSL algorithms and *N*-gram pattern learning and applies algorithms to unstructured document classification.

Experiments on the Reuters-21578 document show that the SSL approach improves classification performance, and unlabeled documents are a good resource to overcome documents with a limited number of labels.

Future developments in this work will address the integration of generalized item aggregation mining algorithms to further improve classification and accuracy in noise-prone areas of data where there liability of probability estimation is particularly important.

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Comparative Study of Symmetric Key Algorithms-Des, AES and Blowfish

By H. Fathima, K.S.R. Matriculation & K.S.R. Kalvi nagar

KSRMHSS

Abstract- This paper presents a peer analysis in the field of encryption algorithms, concentrating on private key block ciphers which are generally used for bulk data and link encryption. We have initially surveyed some of the popular and efficient algorithms currently in use. This paper focuses mainly on the different kinds of encryption techniques that are existing, and comparative study together as a literature survey. This study extends to the performance parameters used in encryption processes and analyzing on their security issues. Cryptography is the practice and study of hiding information. Prior to the modern age, cryptography was almost synonymous with encryption i.e. the conversion of information from a readable state to unreadable state. In order to avoid unwanted persons being able to read the information, senders retain the ability to decrypt the information. There are three types of Cryptography.

Keywords: encryption, decryption, cipher text, permutation, symmetric, substitution bytes.

GJCST-H Classification: B.7.1, I.1.2



Strictly as per the compliance and regulations of:



Comparative Study of Symmetric Key Algorithms-Des, AES and Blowfish

H. Fathima ^α, K.S.R. Matriculation ^σ & K.S.R. Kalvi nagar ^ρ

Abstract- This paper presents a peer analysis in the field of encryption algorithms, concentrating on private key block ciphers which are generally used for bulk data and link encryption. We have initially surveyed some of the popular and efficient algorithms currently in use. This paper focuses mainly on the different kinds of encryption techniques that are existing, and comparative study together as a literature survey. This study extends to the performance parameters used in encryption processes and analyzing on their security issues. Cryptography is the practice and study of hiding information. Prior to the modern age, cryptography was almost synonymous with encryption i.e. the conversion of information from a readable state to unreadable state. In order to avoid unwanted persons being able to read the information, senders retain the ability to decrypt the information. There are three types of Cryptography. They are Asymmetric-key cryptography, symmetric key cryptography and hashing. Encryption methods in which both the sender and receiver share the same key are referred to as symmetric key cryptography. This paper provides a comparison between symmetric key algorithms such as DES, AES, and Blowfish. The comparison is made on the basis of these parameters such as block size and key size.

Keywords: encryption, decryption, cipher text, permutation, symmetric, substitution bytes.

I. INTRODUCTION

Symmetric-key algorithms [1] are algorithms that use the same cryptographic keys for both encryption of plaintext and decryption of cipher text. The keys may be identical or there may be a simple transformation to go between the two keys. The keys, in practice, represent a shared secret between two or more parties that can be used to maintain a private information link.[2] This requirement that both parties have access to the secret key is one of the main drawbacks of symmetric key encryption, in comparison to public-key encryption (also known as asymmetric key encryption).[3]Symmetric-key encryption can use either stream ciphers or block ciphers.[4]Stream ciphers encrypt the digits (typically bytes) of a message one at a time.

Block ciphers take a number of bits and encrypt them as a single unit, padding the plaintext so that it is a multiple of the block size. Blocks of 64 bits have been commonly used. The Advanced Encryption Standard

(AES) algorithm approved by NIST in December 2001 uses 128-bit blocks.

II. DATA ENCRYPTION STANDARD

Data Encryption standard (DES) adopted in 1997 by the National Bureau of Standards. For DES data are encrypted in 64 bit blocks using a 56-bit key. The algorithm transforms 64-bit input in a series of steps into a 64-bit output.

III. DES ENCRYPTION

There are two inputs in the encryption function: the plaintext to be encrypted and the key. In this case, the plaintext must be 64 bits in the length and the key is 56 bits in length. The 64-bit plaintext passes through an initial permutation (IP) that rearranges the bits to produce the permuted input. This is followed by a phase consisting of 16 rounds of the same function, which involves both permutation and substitution functions.

The output of the last (sixteenth) round consists of 64 bits that there are a function of the input plaintext and the key. The left and right halves of the output are swapped to produce the preoutput. Finally the preoutput is passed through a permutation (IP^{-1}) that is the inverse of the initial permutation function, to produce the 64-bit cipher text.

IV. INITIAL PERMUTATION

The input to a table consists of 64 bits numbered from 1 to 64. The 64 entries in the permutation table contain a permutation of the numbers from 1 to 64. Each entry in the permutation table indicates the position of a numbered input bit in the output which also consists of 64 bits.

V. DETAILS OF SINGLE ROUND

The round key K_i is 48 bits. The R input is 32 bits. This R input is first expanded to 48 bits by using a table that defines a permutation plus an expansion that involves duplication of 16 of the R bits. The resulting 48 bits are XORed with K_i . This 48 bit result passes through a substitution function that produces a 32-bit output.

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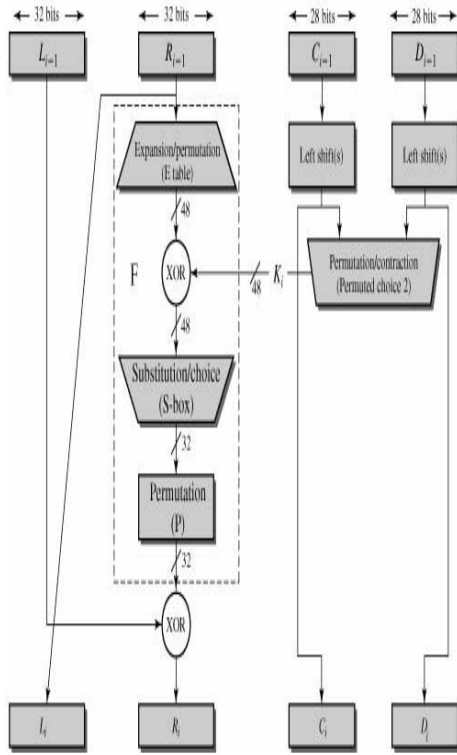


Figure 1: Single Round DES

The substitution consists of a set of eight S-boxes, each of which accepts 6 bits as input and produces 4 bits as output. The first and last bits of the input to box S_i from a 2-bit binary number to select one of four substitutions defined by the four rows in the table for S_i the middle four bits select one of the sixteen columns.

DES: Single Round

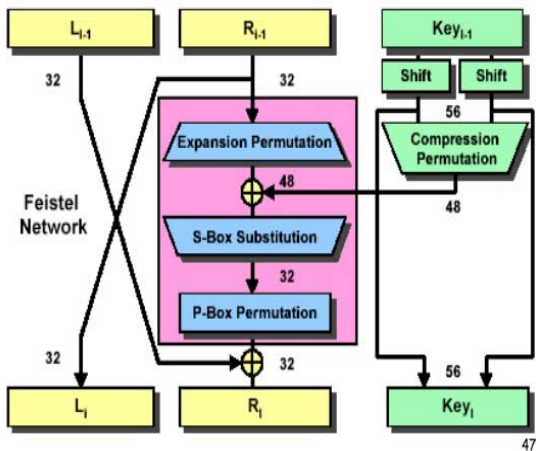


Figure 2: Single Round DES

The decimal value in the cell selected by the row and column is then converted to its 4-bit

representation to produce the output. The outer two bits of each group select one of four possible substitutions (one row of an s- box). Then a 4 bit output value is substituted for the particular 4-bit input (the middle four input bits). The 32-bit output from the eight S-boxes is then permuted.

VI. AVALANCHE EFFECT

A desirable property of any encryption algorithm is that a small change in either the plaintext or the key should produce a significant change in the cipher text. In particular, a change in one bit of the plain text or one bit of the key should produce a change in many bits of the cipher text. If the change were small, this might provide a way to reduce the size of the plaintext or key space to be searched.

VII. ADVANCED ENCRYPTION STANDARD

NIST in 1997 issued a call for proposals for a new Advanced Encryption Standard (AES). NIST specified that AES must be a symmetric block cipher with a block length of 128 bits and support for key lengths of 128, 192, and 256 bits. The AES specification uses the same three key size alternatives but limits the block length to 128 bits. A number of AES parameters depend on key length. Substitute byte uses an S-box to perform a byte-by-byte substitution of the block.

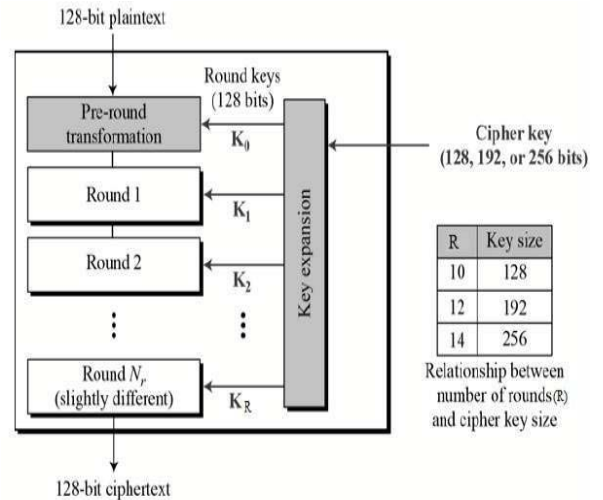


Figure 3: AES

VIII. SUBSTITUTE BYTES TRANSFORMATION

AES defines a 16×16 matrix of byte values called an S-box that contains a permutation of all possible 256 8-bit values. The leftmost 4 bits of the byte are used as a row value and the rightmost 4 bits are used as a column value serve as indexes into the S-box to select a unique 8-bit output value.

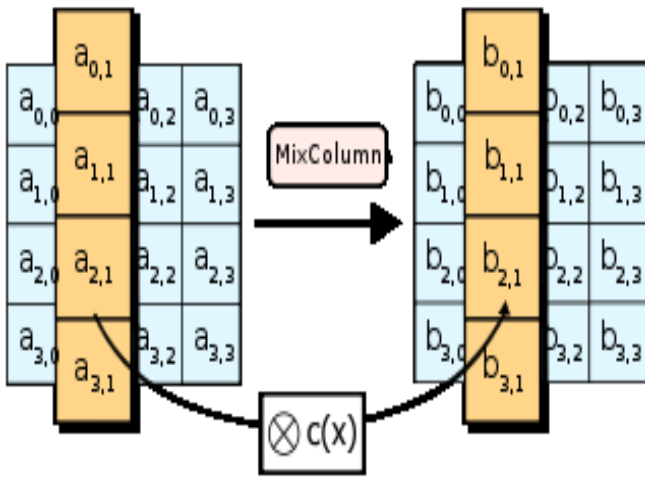


Figure 4: substitute bytes transformation

IX. SHIFT ROW TRANSFORMATION

a) Forward and Inverse transformations

The forward shift row transformation, called shift rows. The Inverse shift row transformation called Inv shift Rows, Perform the circular shifts in the opposite direction for each of the last three rows, with one-byte circular right shift for the second row.

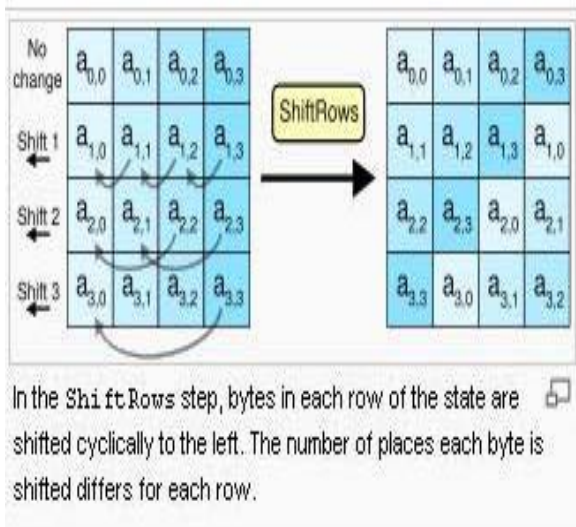


Figure 5: shift row transformation

The Forward mix column transformation, called Mix columns, operates on each column individually. Each byte of a column is mapped into a new value that is a function of all four bytes in the column. The Inverse add round key transformation is identical to the forward add round key transformation, because the XOR operations its own inverse.

X. BLOW FISH

Blowfish is a symmetric cipher developed by Bruce Schneier [SCHM93, SCHN94]. Blowfish was

designed to have the following characteristics such as Fast, Compact, Simple and variably secure. The key length is variable and can be as long as 48 bits. This allows a tradeoff between higher speed and higher security.

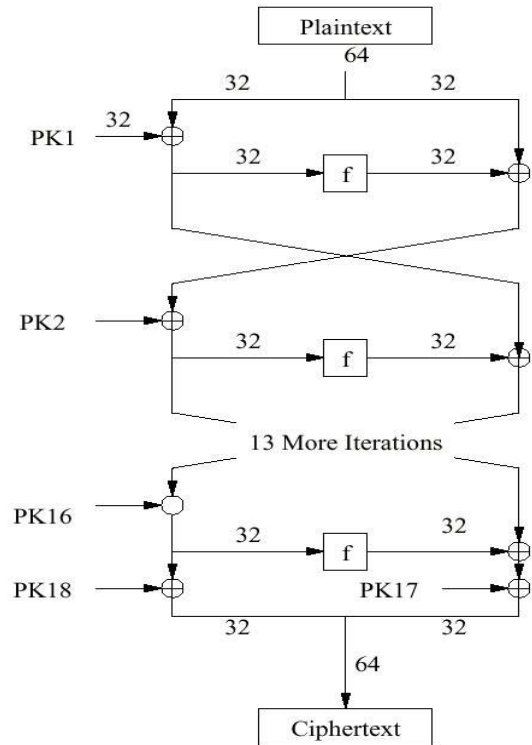


Figure 6: Blow fish

Blow fish encrypts 64-bit blocks of plaintext into 64-bit blocks of cipher text. Blowfish is implemented in numerous products and has received a fair amount of scrutiny.

XI. ENCRYPTION AND DECRYPTION

a) Blowfish uses two primitive operations

Addition: Addition of words, denoted by +, is performed by modulo 2^{32} . Blowfish decryption involves using the sub keys in reverse order. However, unlike most block ciphers, Blowfish decryption occurs in the same algorithmic directions as encryption, rather than the reverse.

Blowfish is a formidable symmetric cipher. Unlike DES, the S-boxes in Blowfish are key dependent. The blowfish design is that operations are performed on both halves of the data in each round, compared to performing an operation on just half the data in each round in the classic Feistel cipher. This should provide greater cryptographic strength, even though the additional operation is linear (XOR).

XII. EXPERIMENTAL RESULTS

Table1: Block Size

ALGORITHM	BLOCK SIZE
DES	64
AES	128
BLOW FISH	64

Graph1: Block size

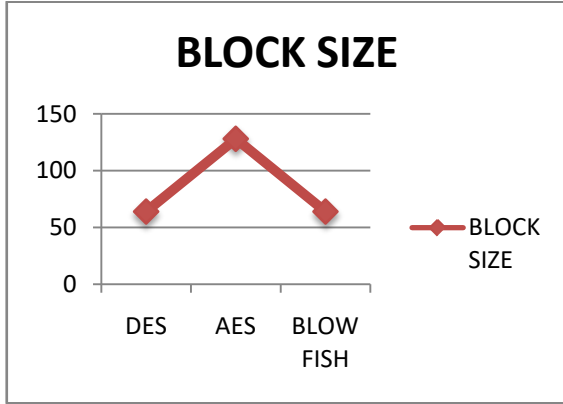
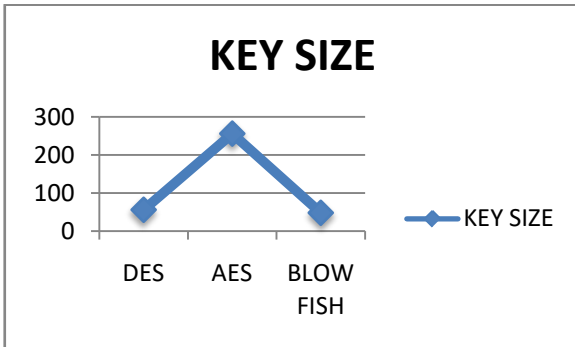


Table 1: Key Size

ALGORITHM	KEY SIZE
DES	56
AES	256
BLOW FISH	48

Graph 2: Key Size



XIII. CONCLUSION

This paper gives a detailed study of the popular symmetric key encryption algorithms such as DES, AES and Blowfish. Further, symmetric key encryption provides more security. This paper presents the performance evaluation of selected symmetric algorithms. From the presented simulation we can conclude that AES has better performance than other algorithms. Secondly, AES has advantage over the DES in terms of throughput & decryption time except Blowfish. In future the work may be extended by including the schemes and techniques over different

types of data such as image, sound and video and developing a stronger encryption algorithm with high speed and minimum energy consumption.

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Fast Stereo Images Compression Method based on Wavelet Transform and Two Dimensional Logarithmic (TDL) Algorithm

By Marwah Kamil Hussien

University of Basrah

Abstract- In this paper, a fast stereo images compression method has been proposed. In proposed method, Firstly, stereo images were transformed using Discrete Wavelet Transform (DWT) in order to reduce computation time. The disparities between these images were estimated by Two Dimensional Logarithmic (TDL) algorithm. The result of the Motion Vector (MV) was encoded into a bit stream by Huffman encoding while the remaining part is compressed like the compression that is used in still image. The proposed method produced good results in terms of Peak Signal-to-Noise Ratio (PSNR), CR, and computation time.

Keywords: *stereo imaging, stereoscopy, discrete wavelet transform, motion estimation, two dimensional logarithmic.*

GJCST-H Classification: *E.4, I.4.2, I.4.8*



FAST STEREO IMAGES COMPRESSION METHOD BASED ON WAVELET TRANSFORM AND TWO DIMENSIONAL LOGARITHMIC TDL ALGORITHM

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Fast Stereo Images Compression Method based on Wavelet Transform and Two Dimensional Logarithmic (TDL) Algorithm

Marwah Kamil Hussien

Abstract- In this paper, a fast stereo images compression method has been proposed. In proposed method, Firstly, stereo images were transformed using Discrete Wavelet Transform (DWT) in order to reduce computation time. The disparities between these images were estimated by Two Dimensional Logarithmic (TDL) algorithm. The result of the Motion Vector (MV) was encoded into a bit stream by Huffman encoding while the remaining part is compressed like the compression that is used in still image. The proposed method produced good results in terms of Peak Signal-to-Noise Ratio (PSNR), CR, and computation time.

Keywords: stereo imaging, stereoscopy, discrete wavelet transform, motion estimation, two dimensional logarithmic.

الخلاصة

في هذا البحث، تم اقتراح طريقة سريعة وبسيطة لضغط زوج من الصور المسجلة. الخطوة الأولى في الطريقة المقترحة، استخدام التحويل المويجي لغرض تحويل اشارة الصور المستخدمة الى مستويات بهدف تقليل وقت المعالجة المطلوب. ثم استخدام خوارزمية البحث (Two Dimensional Logarithmic) لغرض ايجاد متجه الحركة (Motion Vector) والذي يمثل الفرق (الاختلاف) بهدف تقدير الحركة ومن ثم تعويضها. اما الجزء المتبقي فيتم ضغطه كصورة ثابتة (ضغط الصور). الطريقة المقترحة اعطت نتائج جيدة من حيث قمة الاشارة الى الضوضاء (PSNR) ونسبة الضغط وكذلك من حيث وقت المعالجة المستغرق في عملية الضغط. **الكلمات الدالة:** الصور المسجلة، التحويل المويجي المنفصل، تخمين الحركة، خوارزمية لوغاريتم ثنائي البعد.

1. INTRODUCTION

A pair of stereo images is very similar each other as they are the images of a stationary object taken from two different angles. This is why compressing both images independently is an inefficient way of compressing stereo images [1].

In this research, has been selected a pair of stereo images which are very similar to each other are taken from two different angles (and this is why the pressure of each of the images independently, which means in the efficiency of the stereo image compression). We can get the sequence of these images by film cameras or generated by demand sequentially. Compress these pictures is the foundation necessary to reduce this data through the difference between the two images Account (matching), also known as disparity estimation, then squeeze one image independently. This is known as image as a reference, and can either is the right image or the left image, then

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use the reference image and vector disparity to rebuild the second image.

The work aims to propose an efficient technique for stereo images compression by transformed using Discrete Wavelet Transform technique (DWT) in order to reduce computation times, we show that in Section 2. The disparity vectors between them (The left and right image after transform in to DWT levels) were estimated by Two Dimensional Logarithmic (TDL). The remaining image is compressed as still image; we show that in Section 3. The two images are very similar to each other; so that the disparity vectors between the two images are estimated. Section 4 and Section 5 are gives the proposed method and evaluation criteria. Experimental results show in Section 6. Finally, the paper has been concluded in Section 7.

II. DISCRETE WAVELET TRANSFORM (DWT)

Wavelet transform is one of important and useful computation tools for a variety of signal and image processing applications. In image processing field, the main process in wavelet transform is to filter signal of image by two filters, namely, low pass filter (L) and high pass filter. Then, it will down sampled by factor of two leading to compose transform of one level. Repeating of one level transform on the part of low pass output only, results multiple level transform. Two dimensional (2-D) wavelet transform can be obtained by applying 1-D wavelet transform, wavelet filter separately. This computation is done by carrying out 1- D transform on the rows signals one time and on the columns signal another time. As a result of that, it separates image signals into four sub-band images: LL (low frequency in horizon and vertical), LH (low frequency in horizon and high frequency in vertical), HL (high frequency in horizon and low frequency in vertical), HH (high frequency in horizon and vertical).

Therefore, it is possible to use different methods for the sake of enhancement of the details in different frequency domain [2]. LL sub-band image often contains the most important information of the original image and it is usually called approximations the three other sub-band images are named as details. HH sub-band normally includes the small coefficients which are more likely due to undesirable noise [3]. Fig. 1 shows Foreman image and its three levels DWT.

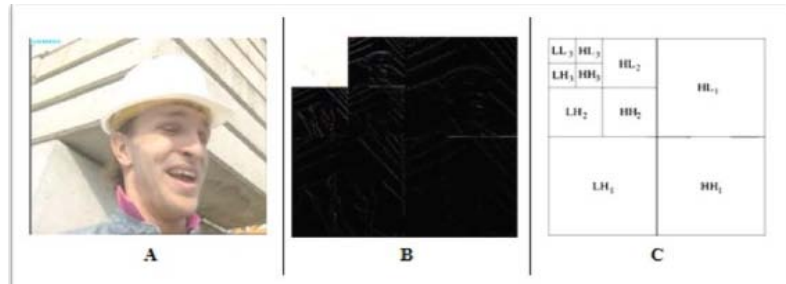


Figure 1: A) Foreman image B) Three levels Discrete Wavelet Transform of Lena image C) Low and High sub-bands resulted from three levels DWT[3].

III. MOTION ESTIMATION

Motion Estimation (ME) is the process of analyzing successive frames in any image sequence to identify objects motion. In this paper, motion estimation used to process of analyzing two stereo images using TDL.

The motion of an object is usually described by a two-dimensional motion vector, which is the placement of the co-ordinate of the best similar block in previous frame for the block in current frame. This placement is represented by the length and direction of motion [4, 5].

a) Three Step Search (TSS)

TSS is one of the earliest attempts at fast block matching algorithms and dates back to mid1980s. The

TSS is the algorithm that limits the number of checking points in a search area. The general idea is represented in Fig. 2, it starts with the search location at the center and sets the „step size“ $S = 4$, for a usual search parameter value of 7. It then searches at eight locations $\pm S$ pixels around location (0,0). From these nine locations searched so far it picks the one giving least cost and makes it the new search origin. It then sets the new step size $S = S/2$, and repeats similar search for two more iterations until $S = 1$. At that point, it finds the location with the least cost function and the macro block at that location is the best match. The calculated motion vector is then saved for transmission. It gives a flat reduction in computation by a factor of 9 [6, 7].

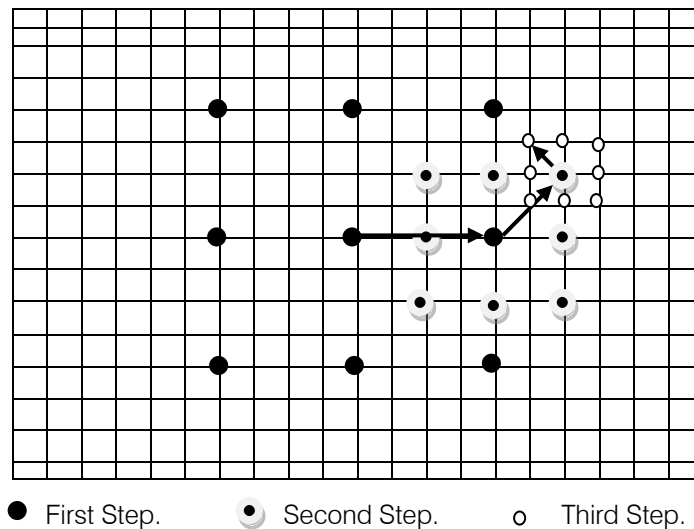


Figure 2: Example Path for Convergence of Three Step Search.

b) Disparity Estimation Using the Two Dimensional Logarithmic Algorithm

TDL Algorithm was introduced by Jain and around the same time that the Three Step Search was introduced and is closely related to it. Although this algorithm requires more steps than the Three Step Search, it can be more accurate, especially when the search window is large[2]. The algorithm may be described as:

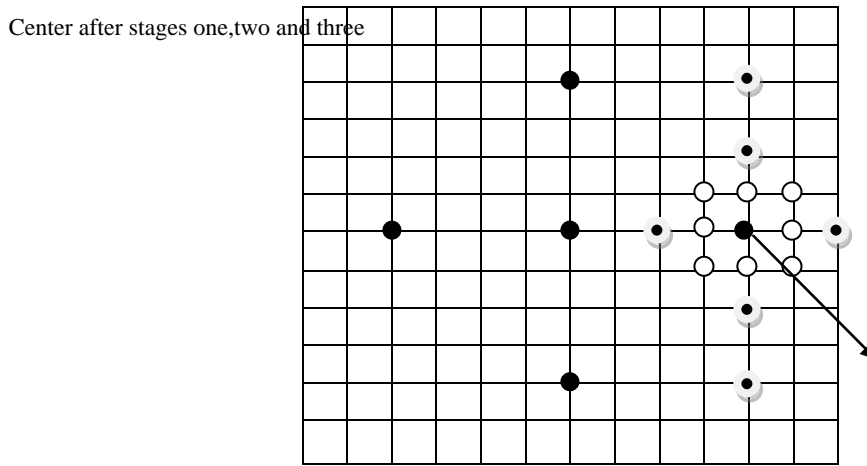
Step1- Pick an initial step size. Look at the block at the center the search are and the four blocks at a distance

of s from this one the X and Y axes. (The five positions from a + sign)

Step 2- If the position of best match is at the center, halve the step size. If however, one of the other four points is the best match, then it becomes the center and step 1 is repeated.

Step 3- When the step size becomes 1, all the nine blocks around the center are chosen for the search and the best among them is picked as the required block.

A particular path for the convergence of the algorithm is shown in the following figure:



- Blocks chosen for first stage
- ◐ Blocks chosen for second stage
- Blocks chosen for third stage

Figure 3: Example Path for Convergence of Two Dimensional Logarithmic Search.

A lot of variations of this algorithm exist and they differ mainly in the way in which the step size is changed [6, 7].

Some people argue that the step size should be halved at every stage. Some people believe that the step size should also be halved if an edge of the search space is reached. However, this last idea has been found to fail sometimes.

IV. THE PROPOSED METHOD

In proposed method, there are four main steps. The first step we process the images used to convert its

signal to levels using discrete wavelet Transform separately. In the second step, we match the two images the director of the first stage using TSS and TDL algorithms to find the movement between the two images and estimate the motion vector for the remaining images. Then, the remaining image will be compressed as a still image. Fig.4 shows flowchart of compression a pair of stereo images.

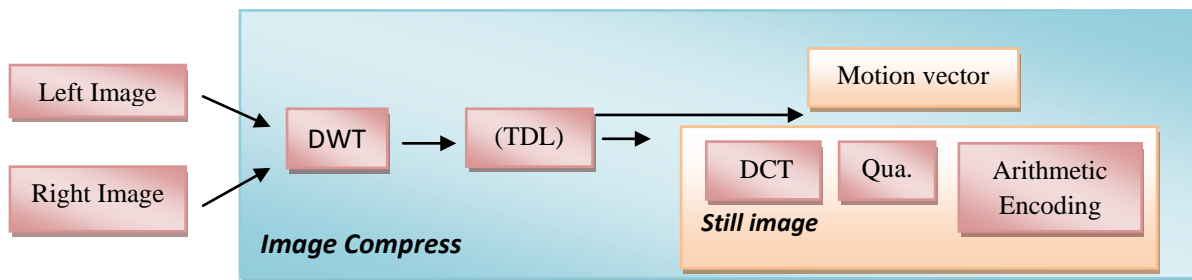


Figure 4: Flow chart of proposed method.

V. EVALUATION CRITERIA

Peak signal-to-noise ratio (PSNR) is the standard method for quantitatively comparing a compressed image with the original. For an 8-bit grayscale image, the peak signal value is 255. Hence, the PSNR of an $M \times N$ 8-bit grayscale image C_{ij} and its reconstruction R_{ij} is calculated as [8,9]:

$$PSNR = 10 \log_{10} \frac{255^2}{MSE} \quad (2)$$

where the Mean Square Error (MSE) is defined as [10]:

$$MSE = \frac{1}{N^2} \sum_{i=0}^{N-1} \sum_{j=0}^{N-1} [C_{ij}(m,n) - R_{ij}(m,n)]^2 \quad (3)$$

PSNR is measured in decibels (dB), M: height of the image, N: width of the image.

VI. EXPERIMENTAL RESULTS

This section explains the experiments which have been implemented on two stereo images, Aloe, child and chosen image from personal camera as test images; each one of them is in size of 256×256 and of JPEG format. MATLAB version 7.4.0.287 (R2007a) was

used as a work environment to carry out these experiments.

Table (1): display the results of data (PSNR, CR and computation Time) for the TSS algorithm of stress selected three images recorded after using discrete wavelet transform.

Table 2: display the results of data (PSNR, CR and computation Time) for the proposed method of stress selected three images recorded after using discrete wavelet transform.

The decoded left and right images were compared with the original left and right images. The Mean Square Error (MSE) between the original and decoded left and right images was referred in Equ. (3). The MSE of the image is the average of the MSE of the left image and the MSE of the right image as show in Equ. (4)[10].

$$MSE = (MSE_L + MSE_R) / 2 \quad (4)$$

a) Results of Images

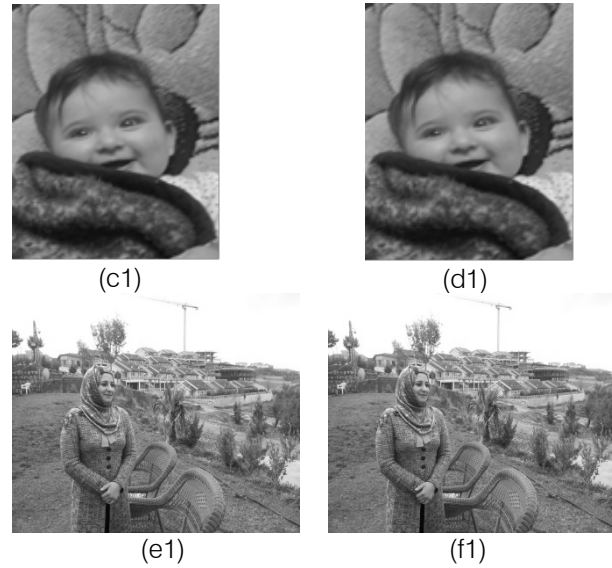
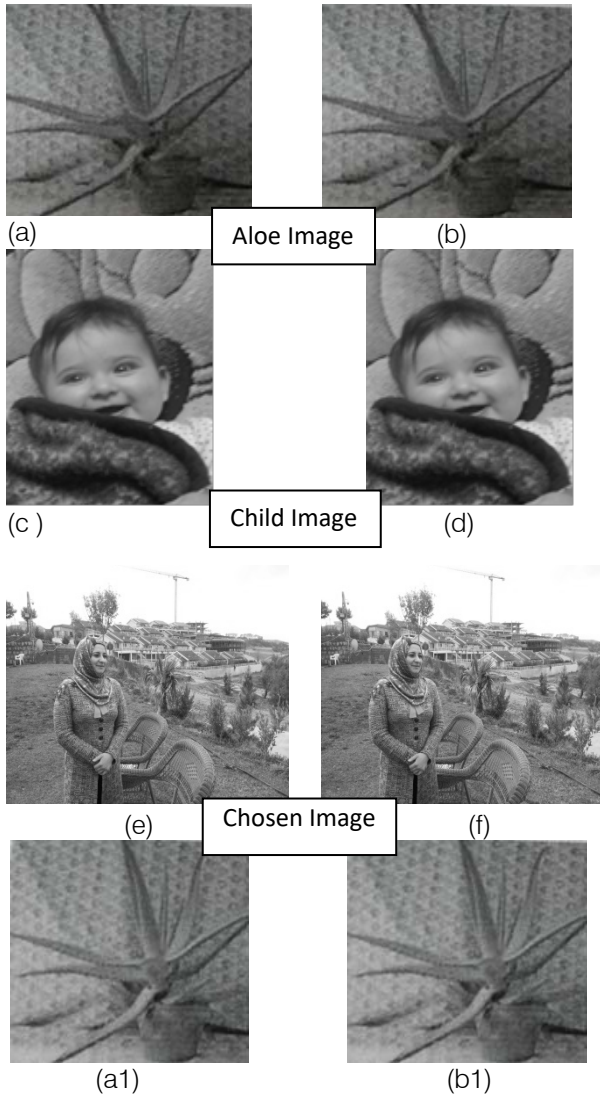


Figure 5: (a), (b), (c), (d), (e) and(f) Original Left and Right Images. (a1), (b1), (c1), (d1), (e1) and(f1) Reconstructed Left and Right images.



Table1: Data for TSS Algorithm.

Images	PSNR (db)	CR	Time (sec)
Aloe	32.222	0.432	66.51
Child	33.321	0.522	72.22
Chosen Image	34.411	0.643	100.33

Table 2: Data for the Proposed Method.

Images	PSNR (db)	CR	Time (sec)
Aloe	45.32	0.566	50.32
Child	47.45	0.6.98	59.44
Chosen Image	50.28	0.789	88.76

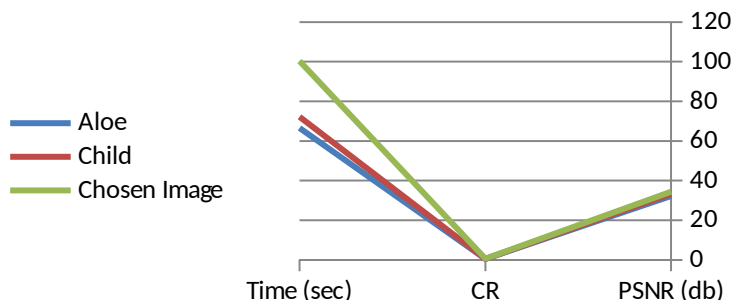


Figure 6: PSNR vs Bitrate for TSS Algorithm.

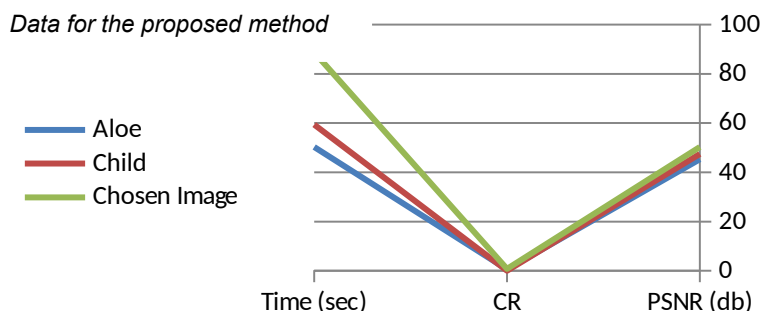


Figure 7: PSNR vs Bitrate for the Proposed Method.

VII. CONCLUSIONS

In this paper, a method for stereo images has been proposed to decrease the computation time without much influence on PSNR and compression ratio. Referring to the results that are shown in Table 1, and Table 2, it is obviously that the values of PSNR, CR, and computation time are affected by the length and the resolution of each pair from the images.

Additionally, we can notice clearly that the use of DWT minimized the processing time approximately 45%.

Three pair of images were compressed and then reconstructed by reversing the steps followed to compress the images.

The reconstructed images were then compared with the original images.

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High Speed AES Algorithm to Detect Fault Injection Attacks and Implementation using FPGA

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Abstract- Information security is an essential issue in communication system. Advance Encryption Standard (AES) is utilized as a part of many embedded applications to give data security. Different counter measures are present in AES against fault injection attacks. Plain text and key of 128-bit is given as an input to the system and encryption and decryption operations are performed. Flag error shows the status of fault. Fault is produced randomly during encryption and decryption. For this reason, round transformation is broken into two sections and a pipeline stage is inserted in between. After fault detection one operation is performed that is redundancy check. Detected error or fault is corrected using redundancy check. The scheme is implemented using FPGA.

Keywords: security, fault injection, confidential, wncryption, decryption, redundancy.

GJCST-H Classification: B.2.4, B.7.1



H I G H S P E E D A E S A L G O R I T H M T O D E T E C T F A U L T I N J E C T I O N A T T A C K S A N D I M P L E M E N T A T I O N U S I N G F P G A

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High Speed AES Algorithm to Detect Fault Injection Attacks and Implementation using FPGA

Prof. Dr. S. S Chorage ^α & Somwanshi V. A. ^σ

Abstract- Information security is an essential issue in communication system. Advance Encryption Standard (AES) is utilized as a part of many embedded applications to give data security. Different counter measures are present in AES against fault injection attacks. Plain text and key of 128-bit is given as an input to the system and encryption and decryption operations are performed. Flag error shows the status of fault. Fault is produced randomly during encryption and decryption. For this reason, round transformation is broken into two sections and a pipeline stage is inserted in between. After fault detection one operation is performed that is redundancy check. Detected error or fault is corrected using redundancy check. The scheme is implemented using FPGA.

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I. INTRODUCTION

Cryptography is used in the data communication system to secure the information. The national institute of standards and technology (NIST) finalized the advance encryption standard in October 2000. AES is introduced after the data encryption standard (DES). AES algorithm is most frequently used due to its high frequency and simplicity.

In AES during encryption it accepts a plain text input. Plain text input is limited to 128 bits and a key that can be specified to be 128 bit (AES-128) 192 or 256 bits to generate the cipher text. Round transformations are performed in AES. The four transformations includes sub bytes shift rows, mixed columns and add round keys.

The objective of AES is to secure the information being transferred from a user and only the desired receiver with a secret key would retrieve the original data. But sometimes some malicious faults injected during the implementation of AES algorithm. Due to these faults AES does not ensure that the information is transferred reliably. There are several fault attacks on AES. To obtain the confidential information the differential fault analysis (DFA) attacks are based on injecting faults into the structure of AES.

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II. RELATED WORK

Mestiri et al. [1] introduced a fault detection scheme, which is based on modified temporal redundancy for AES round it is used to detect transient single and multiple faults occurring at rub time. Round transformations are performed to detect the faults. The authors give the new scheme for fault detection in sub bytes and the inverted sub bytes using the relation between the input and output of S-box and inverted S-box.

Chu et al. [2] focused on the new method called as polynomial residue number system (PRNS) that is error detection method to secure the AES implementation. This scheme yields very good coverage and the distribution and parallelism characteristic of a PRNS error detecting system yields intrinsic resistance to some side channel attacks.

Rajendran et al. [3] proposed a new mechanism called as CED which is based on the slide attacks. This mechanism is independent of the S-box scheme. It is applicable to all symmetric block ciphers.

A. Reyhani -Masoleh et al. [4] proposed a structure independent low cost fault detection scheme for implementation of AES. The authors introduced new formulations for the fault detection in sub bytes and inverted sub bytes using arithmetic relations. The arithmetic relations are in between the input and the output of the S-box and inverted S-box. These schemes are independent of the way the S-box and the inverted S-box are implemented.

From this related search, it is observed that the new fault detection scheme is used for AES implantation. This scheme gives reliable implementation with new architecture of AES for checking sub bytes, inverted sub bytes and the other transformation in the inscription and the decryption process.

III. ADVANCE ENCRYPTION STANDARD

Advance encryption standard (AES) is a non-feistel block cipher that encrypts and decrypts a data block of 128, 192 and 256 bits each data blocks consist of 4×4 array of bytes this array of bytes is called as states. AES is a round-based algorithm. The number of round is 10, 12 or 14. These rounds use key length of 128,192 and 256 bits respectively.

The different operations are performed in AES like sub bytes, shift rows, mix columns and add round keys. But in the final round doesn't have the mix column transformation. The separate key scheduling module help to initial key to generate the round key which is used in each round.

1. In this process, each byte is replaced with another based on LUT in non-linear substitution step called as Sub bytes.
2. Each row of the state is shifted cyclically a certain number of steps which happens in the transposition

step that operation is called as rows called as Shift rows.

3. Combining the four bytes in each column by linear transformation during column interchange that is called Mix column operation.
4. The cipher key generates a round key by using the key schedule and the round combines each byte of state. This process is known as Add round key.

Fig.1 shows the general structure of AES which includes the different round transformation that is sub bytes, shift rows and mix columns.

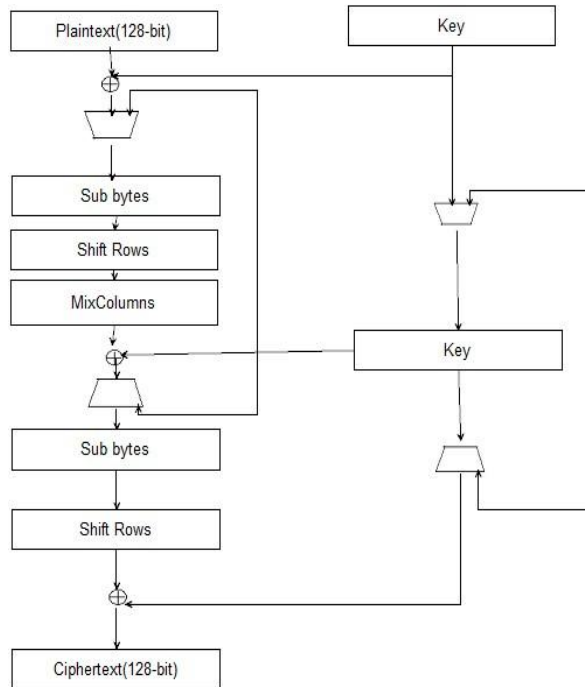


Figure 1: General Structure of AES [1]

For generating key schedule AES algorithm takes the cipher key and performs a key expansion routine. In the decryption process the inverse of corresponding transformation in encryption is performed i.e. Inv_shiftRows, Inv_SubBytes and Inv_MixColumns.

IV. AES IMPLEMENTATION

In AES 32-bit implementation, it takes four 32-bit words for the input data and four 32-bit words for the cipher key. Then it performs the encryption or decryption process and the output data it as four 32-bit words. The architecture of AES is composed of six modules:

1. *Input interface*- It is used to load and store the input blocks for encryption and decryption process.
2. *Controller*- It generates the control signals for all other units in the implementation.
3. *AES round*- It is used to perform the round operations in encryption and decryption of the input data.

4. *Key Expander*- To compute the set of internal cipher keys based on single external key one block is used called as key expander.
5. *Output interface*- It takes the output with 128-bit length and then it converts into the four 32-bit words.
6. Input data buffer and Input key buffer are used to load the data and key.
7. *AES library*- To perform the basic operations one library is used called AES library which contains the basic function used in implementation of AES.

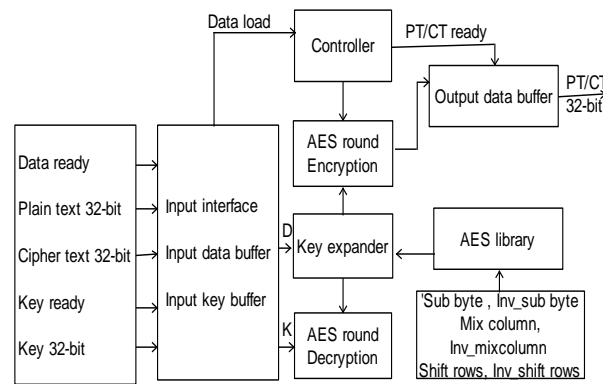


Figure 2: Block diagram of AES 32-bit [1]

V. FAULT INJECTION ATTACKS

The errors that are introduced during implementation of cryptographic algorithms are called as fault injection attacks. During implementation of AES one or several faults are injected and faulty output is used to obtain information on the secret key stored in secured component.

Many authors introduced series of simulation for evaluation of robustness of unprotected AES algorithm against fault injection attacks. After a certain numbers of fault injection those attacks can retrieve the secret key of AES. So it is necessary to protect AES from those fault injection attacks. To protect AES from the faults different techniques are introduced.

VI. FAULT DETECTION SCHEME FOR AES

In related work, it shows that, no. of fault detection schemes against fault injection attacks are based on some sort of redundancy. The redundancies are hardware, temporal, and information redundancy.

In case of AES basic temporal redundancy is used it is related to hardware. Fig.6 is used to perform both the normal encryption and re-encryption using same input. The results are compared and every discrepancy is considered as an error at the end of encryption execution.

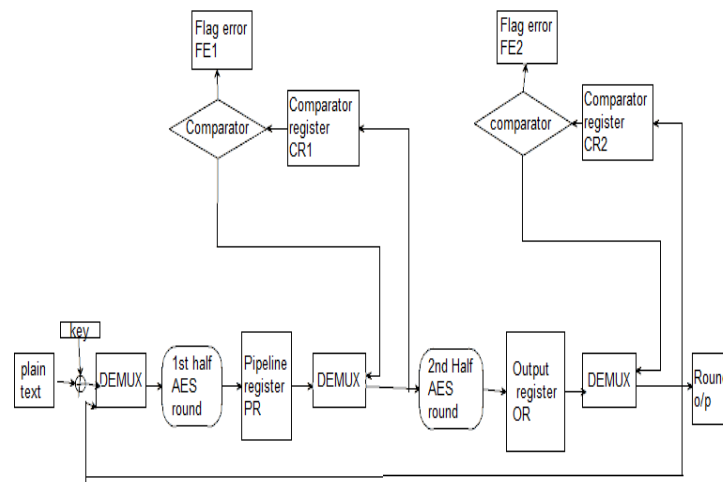


Figure 3: AES round with fault detection scheme [1]

In the proposed fault detection scheme modified temporal redundancy technique used for the AES round to detect transient single and multiple faults occurring at runtime. So, for this purpose the AES round transformation is broken into two parts and pipeline register inserted in between. In that the first-round operation is checked against errors while second half round is performed and vice versa. Every round is required two clock cycles: the first cycle is to perform normal encryption while second is to realize the re-

encryption of the same input and to compare the results. The registers are loaded in each clock cycle to perform the round operation and the fault detection process is shown in table 1

In first clock cycle, the plain text is XORed with the initial key, round 0 is processing. In the second clock cycle (k=2, 3) the state message goes through the first half of the first AES round (R1,1). The R1,1 starts with the second clock cycle. In third clock cycle, while the second half round is processing the second half of the

first AES round $R2,1$, the first half round perform the re-encryption of $R1,1$ using the same input [1]. The $R1,2$ of the AES encryption starts at the fourth clock cycle, at the same clock cycle the second half round is reprocessing the second half of the first round $R2,1$. The $CR1$ and $CR2$ registers are used to store the output value of each

round to be compared with PR and OR registers, respectively. It should be noted that although the encryption is performed at second clock cycle, the result is not used till the third clock cycle where the output of the first half round is available for error checking [1].

Table 1: Sequence of operations for proposed architecture [1].

Clock cycle (k)	Register operation	1 st half round	2 nd half round
k = 1	$PT \oplus \text{Key}$	----	---
k = 2, 4, 6, ...	$CR2 \leftarrow PR$ $FE2 \leftarrow CR2 \oplus OR$	Encryption	Re-encryption
k = 3, 5, 7, ...	$CR2 \leftarrow OR$ $FE1 \leftarrow CR1 \oplus PR$	Re-encryption	Encryption

VII. IMPLEMENTATION DETAILS OF ROUNDS

a) Implementation of first half AES ($R1,j$)

In first half AES round to implement the S-box operation two methods are present, first is using LUT and second is by mathematical equations. LUT method is more suitable. All operations are in infinite Galois field. In first half sub byte and shift row operations are performed. For sub byte /inv_subbyte operation 16 S-box/inv_S-box are required.

The Shift row operation is a circular shifting operation on the rows of state having different no. of bytes.

b) Implementation of second half AES ($R2,j$)

In second half mix column and add round key operations are performed. Mix column operation is performed using following equations [1].

$$S'0,j = (02 \cdot S0,j) \oplus (03 \cdot S1,j) \oplus S2,j \oplus S3,j$$

$$S'1,j = S0,j \oplus (02 \cdot S1,j) \oplus (03 \cdot S2,j) \oplus S2,j \oplus S3,j$$

$$S'2,j = S0,j \oplus S1,j \oplus (02 \cdot S2,j) \oplus (03 \cdot S3,j)$$

$$S'3,j = (03 \cdot S2,j) \oplus S1,j \oplus S2,j \oplus (02 \cdot S3,j)$$

Considering $03 = 02 \oplus 01$ this rule the equations can be re-written as:

$$S'0,j = 02 \cdot (S0,j \oplus S1,j) \oplus S1,j \oplus S2,j \oplus S3,j$$

$$S'1,j = S0,j \oplus 02 \cdot (S1,j \oplus S2,j) \oplus S2,j \oplus S3,j$$

$$S'2,j = S0,j \oplus S1,j \oplus 02 \cdot (S2,j \oplus S3,j) \oplus S3,j$$

$$S'2,j = S0,j \oplus S1,j \oplus S2,j \oplus 02 \cdot (S3,j \oplus S0,j)$$

The Add round key is XOR operation that adds round key to the mix column output state and the round keys are generated during key expansion [1].

VIII. SIMULATION RESULTS

In AES algorithm some operations are performed. For these operations one look up table is used to assign values to the register that look up table is shown in table 2.

In AES algorithm, the encryption and decryption operations are performed. Plain text of 128-bit and key also of 128-bit are given as a input. During encryption sub byte, shift rows, mix column and add round key operations are performed. During decryption inv_sub byte, inv_shift row and inv_mixcolumn, operations are performed. The faults are generated randomly during the encryption and decryption process. Flag error in fig.3 shows the status of fault that is present or not.

Table 2: AES S-box look-up-table [12]

		Y															
		0	1	2	3	4	5	6	7	8	9	a	b	c	d	e	f
X	0	63	7c	77	7b	f2	6b	6f	e5	30	01	67	2b	fe	d7	ab	76
	1	ca	82	e9	7d	fa	59	47	f0	ad	d4	a2	af	9c	a4	72	c0
	2	b7	fd	93	26	36	3f	f7	cc	34	a5	e5	f1	71	d8	31	15
	3	04	c7	23	c3	18	96	05	9a	07	12	80	e2	eb	27	b2	75
	4	09	83	2c	1a	1b	6e	5a	a0	52	3b	d6	b3	29	e3	2f	84
	5	53	d1	00	ed	20	fc	b1	5b	6a	cb	be	39	4a	4c	58	cf
	6	d0	ef	aa	fb	43	4d	33	85	45	f9	02	7f	50	3c	9f	a8
	7	51	a3	40	8f	92	9d	38	f5	bc	b6	da	21	10	ff	f3	d2
	8	cd	0e	13	ec	5f	97	44	17	c4	a7	7e	3d	64	5d	19	73
	9	60	81	4f	dc	22	2a	90	88	46	ee	b8	14	de	5e	0b	db
	A	e0	32	3a	0a	49	06	24	5c	c2	d3	ac	62	91	95	e4	79
	B	e7	c8	37	6d	8d	d5	4e	a9	6c	56	f4	ea	65	7a	ae	08
	C	ba	78	25	2e	1c	a6	b4	c6	e8	dd	74	1f	4b	bd	8b	8a
	D	70	3e	b5	66	48	03	f6	0e	61	35	57	b9	86	c1	1d	9e
	E	e1	f8	98	11	69	d9	8e	94	9b	1e	87	e9	ce	55	28	df
	F	8c	a1	89	0d	bf	e6	42	68	41	99	2d	0f	b0	54	bb	16

Figure 4: shows the simulation result of round1 operation.

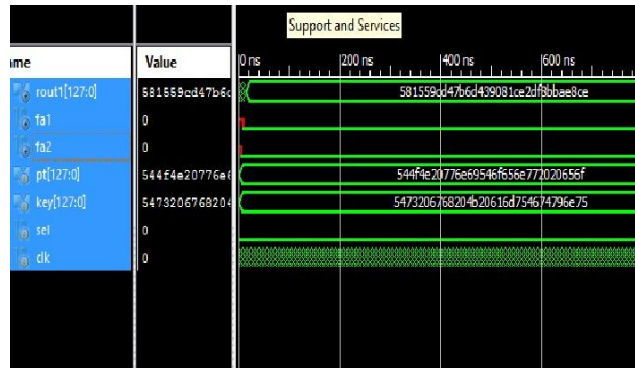


Figure 4: Simulation result of Round1 operation

This result shows the round1 operation, in which Sub byte, Shift rows, Mix column and add round key operations are performed. Similarly, all 10 rounds are performed in AES encryption and decryption. Fa1 and

Fa2 shows the status of fault in fig.4. If Fa=0, then no fault and if Fa=1, then fault is present. Fig.5 and Fig.6 shows simulation result of encryption and decryption operation.

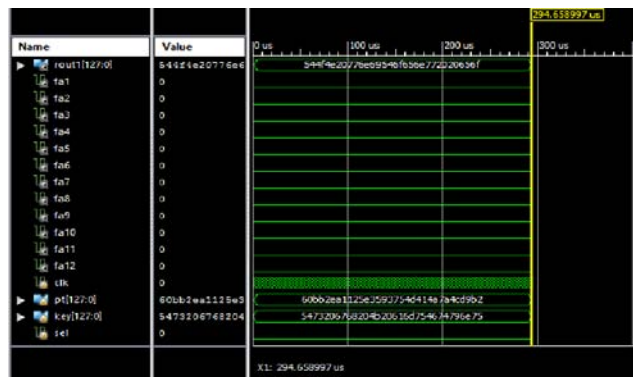


Figure 5: Simulation result of Encryption

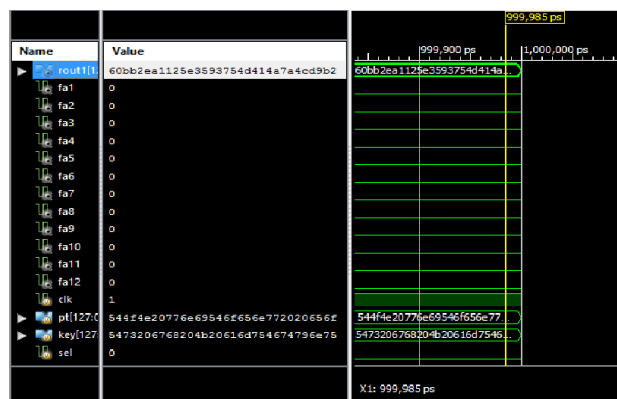


Figure 6: Simulation result of Decryption.

IX. CONCLUSION

In communication system information security is most important. AES algorithm, can resist any kinds of password attacks with a strong practicability and reliability. The AES algorithm can be efficiently implemented by using FPGA platform. During implementation of AES some natural and malicious

faults are injected. It is necessary to resist those faults for better performance of AES algorithm.

In fault detection scheme critical path of the AES round operation is divided into two halves and a pipeline register is inserted in between them and normal encryption and re-encryption operations are performed. Simulation results show the round1, encryption and decryption operations. During encryption and decryption

process faults are injected and the flag error shows the status of fault. This scheme can be implemented using Xilinx and Spartan-6 FPGA platform. Compared to some previous works, this method achieves 99.99% fault coverage. In future work text input, can be replaced with audio or video input.

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Graphic Interface Applied to Automated System to Manage the use of Tools in Machine

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Abstract- The processing industry has to find ways to reduce manufacturing costs, as a way to survive in the market with increasing competition. This competition has become increasingly driven by globalization, that is, an industry has to share the consumer market with other industries that are installed worldwide. One of the possible ways to reduce manufacturing costs is related to the efficient use of basic inputs. Among the main inputs used in the manufacture, some are specifically related to the tools that are installed on machines such as lathes, grinding machines, presses and others. Typically, the tools developed by the industry in the process engineering sector have dedicated characteristics that must be maintained to perform the appropriate transformation of the product being manufactured. The preservation of these characteristics is linked mainly with the specified service life for the use of each tool, in order to make the substitution before the product is affected by non conformities arising from the manufacturing process.

Keywords: *tool replacement, eccentric press, graphical interface, automation.*

GJCST-HClassification: *B.7.2, D.2.6*



GRAPHIC INTERFACE APPLIED TO AUTOMATED SYSTEM TO MANAGE THE USE OF TOOLS IN MACHINE

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Graphic Interface Applied to Automated System to Manage the use of Tools in Machine

Francisco C. P. Bizarria ^α, José W. P. Bizarria ^ο, Luis F. de Almeida ^ρ & Fernando M. R. S. e Santos ^ω

Abstract- The processing industry has to find ways to reduce manufacturing costs, as a way to survive in the market with increasing competition. This competition has become increasingly driven by globalization, that is, an industry has to share the consumer market with other industries that are installed worldwide. One of the possible ways to reduce manufacturing costs is related to the efficient use of basic inputs. Among the main inputs used in the manufacture, some are specifically related to the tools that are installed on machines such as lathes, grinding machines, presses and others. Typically, the tools developed by the industry in the process engineering sector have dedicated characteristics that must be maintained to perform the appropriate transformation of the product being manufactured. The preservation of these characteristics is linked mainly with the specified service life for the use of each tool, in order to make the substitution before the product is affected by non-conformities arising from the manufacturing process. The tool replacement at the right time becomes essential, but to perform this task should be considered aspects related to the early and late replacement, both of which can lead to increased costs for the purchase of tools or rework parts produced with different characteristics what was envisaged in the specification. In this context, this paper proposes a graphical interface to be integrated into the physical architecture of the automated system that makes managing the use of tools for industrial eccentric press. All virtual components designed for the windows of the graphical interface are significant and related to the procedures set out to make the replacement of each of the press tool. The validation of the functionality of the interface is obtained by means of tests on the prototype that adopts the basic elements provided in said architecture. The positive results observed in practical tests suggest that graphical interface is appropriate for the purpose which it is intended.

Keywords: tool replacement, eccentric press, graphical interface, automation.

1. INTRODUCTION

In the last decades the global consumer market is demanding products with competitive cost, minimum assured quality, configuration options, ease of maintenance, durability, ergonomic advantages, sustainability ecological characteristics consistent with diversified

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social values, and free of exploitation with human labor and/or animal in the manufacturing process [1].

In order to meet these characteristics, national and international industries are investing in the improvement of their manufacturing facilities and adopting the use of automation in their production lines as the main resource to: i) maximize the control performed at the various levels of the process, ii) to integrate production lines, iii) to reduce losses in the process, iv) to increase manufacturing capacity, v) to meet seasonal variations in production demands, vi) to minimize the number of production cycles, vii) to meet national and/or international standards; and viii) to reduce or even eliminate the use of human and/or animal labor in repetitive tasks and/or environments that are hostile and/or dangerous [2].

In this sense, the manufacturing industry also has to look for effective ways to reduce manufacturing costs as a way to survive in a market with growing competition. This competition has become increasingly stimulated by globalization, as an industry has to divide its consumer market with others installed in different parts of the world.

One of the possible ways to reduce manufacturing costs is related to the efficient and effective use of basic inputs. Among the main inputs used in manufacturing are those related to the tools that are installed in machines, such as: lathes, grinders, presses and others related. Typically, tools developed by an industry's process engineering sector have dedicated features that must be maintained to perform the proper transformation of the product being manufactured.

The preservation of these characteristics is mainly related to the useful life that is specified for the use of each tool, in order to replace them before the product is affected by non-conformities from the manufacturing process. The replacement of tools at the right moment becomes essential, but in order to perform this task, the aspects related to premature and late replacement must be considered, both of which may generate cost increases for tool acquisition or rework in parts that were produced with divergent characteristics.

In this context, this work proposes the use of a graphical interface to be integrated in the physical architecture of the automated system that performs management in the use of tools for an eccentric industrial press. The virtual components elaborated to

meet the windows of the graphical interface are expressive and related to the procedures defined to carry out the replacement of each tool of the press. The validation of the functional efficacy of this interface is obtained through tests carried out in prototype which adopts the basic elements provided for in architecture.

II. OBJECTIVES OF THE WORK

This work has as main goal to propose the windows and their respective virtual components for a graphical interface to be integrated in the architecture of automated system that performs the management in the use of eccentric press tools, in order to minimize the premature or late replacement of these tools.

To present the most expressive results obtained in the practical tests carried out with the prototype that was developed to validate the virtual resources contained in the windows of the mentioned interface.

III. REFERENCE ARCHITECTURE

The basic blocks provided in the physical architecture that is considered as reference to integrate the resources established in each window of the Graphical Interface (GI) in order to interact with the automated system that manages the use of tools in eccentric press are presented in Figure 1.

The acronyms defined for the blocks contained in the reference architecture, which is shown in Figure 1, have the following meanings: i) HC: Process Engineering Host Computer or Tool Preparation Room, ii) GI: Graphic Interface of Process Engineering or Tool Room, iii) TDB: Tool Database, iv) DCL: Data Communication Line, v) DPC: Dedicated Press Control, vi) CMP: Control and Monitoring Panel, and vii) EP: Eccentric Press.

The block called Host Computer (HC), which belongs to the Process Engineering Sector, has the following main functions: i) host, at the application layer, the window of the Graphical Interface (GI) that allows the system user to register the codes of the products that will be manufactured by the press, the codes of the tools available to meet the processes and the useful life times established for operations under nominal press conditions, in Tool Database (TDB), and ii) perform data communication with the Dedicated Press Control (DPC) and Host Computer (HC) of the Tool Preparation Room. The Dedicated Press Control (DPC) block has features that allow: i) to execute the Eccentric Press (EP) operational control algorithm, ii) to parameterize the Eccentric Press (EP) operating modes, iii) to control, monitor and interrupt the operation of the Eccentric Press (EP), through the local Control and Monitoring Panel (CMP), iv) send signals to control the actuators installed in the physical structure of the Eccentric Press (EP), v) receive signals from the installed sensor systems in the physical structure of the Eccentric Press

(EP); vi) to perform data communication with the Process Engineering Sector and the Tool Preparation Room.

The resources contained in the Host Computer (HC) of the Tool Preparation Room are directed to: i) host, at the application layer, the Graphical Interface (GI) window that allows the user to access the records that are registered in the Tool Database (TDB) for up-to-date information on the quantities of tools available, product codes, tool codes, and service life of each tool, ii) selecting and loading specific tool data in the Dedicated Press Control (DPC) to be used in the current manufacturing process, and iii) to perform data communication with the Process Engineering and Dedicated Press Control (DPC).

The Data Communication Line (DCL) is the physical means established to perform data communication, in a bidirectional way, with the Host Computer (HC) that belongs to the Sector of Process Engineering, Computer Host (HC) from the Tool Preparation Room, and Dedicated Press Control (DPC). It should be mentioned that this line is provided with galvanic separation and protection against electromagnetic interference.

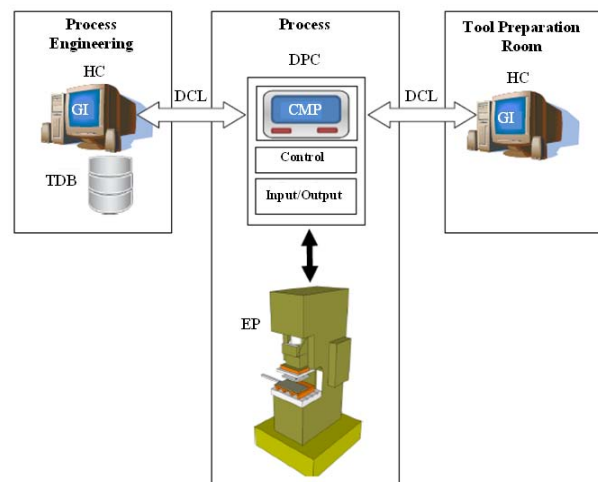


Figure 1: Reference Architecture Blocks.

In the Eccentric Press (EP), each production tool is installed, which should have its life cycle monitored to identify the appropriate moment of substitution, in order to avoid the negative consequences related to premature or late exchange.

IV. PROTOTYPE

A view of the components contained in the prototype that was assembled to evaluate the features established in the Graphical Interface (GI) windows, which is intended to be integrated into the physical architecture of the automated system that performs the management in the use of tools for an industrial eccentric press, is shown in Figure 2. In this prototype

the practical tests were carried out to validate the operational efficiency of the blocks of the reference architecture shown in this work, with special attention being given to the elaboration, operation and use of the virtual components that are contained in the windows of said interface.

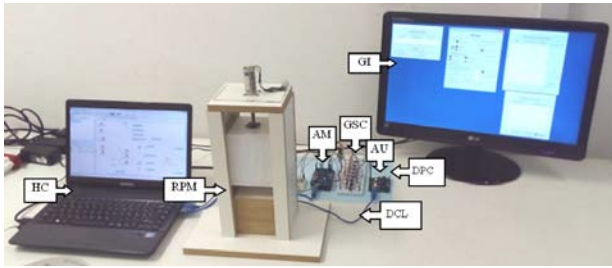


Figure 2: Prototype view.

As shown in Figure 2, the components established for the prototype are: i) Graphical Interface (GI), ii) Host Computer (HC), iii) Data Communication Line (DCL), iv) Dedicated Press Control (DPC), and v) Representative Press Model (RPM). It should be mentioned that the number of modules defined in the prototype is lower than that predicted in the reference architecture shown in Figure 1, but this condition is not limiting to prevent the validation of the virtual resources that are present in the Graphical Interface (GI) windows.

The Host Computer (HC), used in the prototype, is a portable (notebook) type, with Intel® 64-bit architecture and Windows 8.1™ operating system. The Graphical Interface (GI), installed on this computer, has been developed with the resources available in the integrated development environment that is called by MyOpenLab, build: 3.0.4.1, under license: GNU - general public license [3].

In the physical layer, the Data Communication Line (DCL) adopts the EIA (Electronic Industries Alliance) 232, and in the logic the protocol denominated by Firmata [4].

The Dedicated Press Control (DPC) consists of: i) microcontrolled unit (AU) of the Arduino type UNO [5], ii) Galvanic Separation Circuit (GSC), and iii) micro-controlled unit (AM) of the Arduino type MEGA 2560 [6].

The Arduino UNO unit (AU) performs the bidirectional data communication interface between the Galvanic Separation Circuit (GSC) and Host Computer (HC), through the protocol Firmata.

The Galvanic Separation Circuit (GSC) is a mean established to perform bidirectional communication of electrical signals between the Arduino UNO (AU) and Arduino MEGA 2560 (AM) unit in an irradiated way. It should be mentioned that the use of this circuit is intended to minimize possible incompatibilities and/or electrical faults of one unit affecting the operation of the other.

The purpose of the Arduino MEGA 2560 (AM) unit is to: i) execute the dedicated operation control of

the Representative Press Model (RPM) from the command signals sent by the Galvanic Separation Circuit (GSC), and ii) send signals related to the state of the sensors installed in the Representative Press Model (RPM) for the Galvanic Separation Circuit (GSC). This unit is equipped with a specific circuit to control the motor installed in the model, which is the Motor Shield L293D Driver H-Bridge [7].

a) Management software

In the development of the prototype, a version of the tool management program was developed for an eccentric industrial press in order to evaluate the virtual resources that were established for each Graphical Interface (GI) window. In this sense, the analytical flowchart that represents a specific sequence of actions foreseen in this management program, and that was used in the accomplishment of the practical tests of this work is presented in Figure 3.

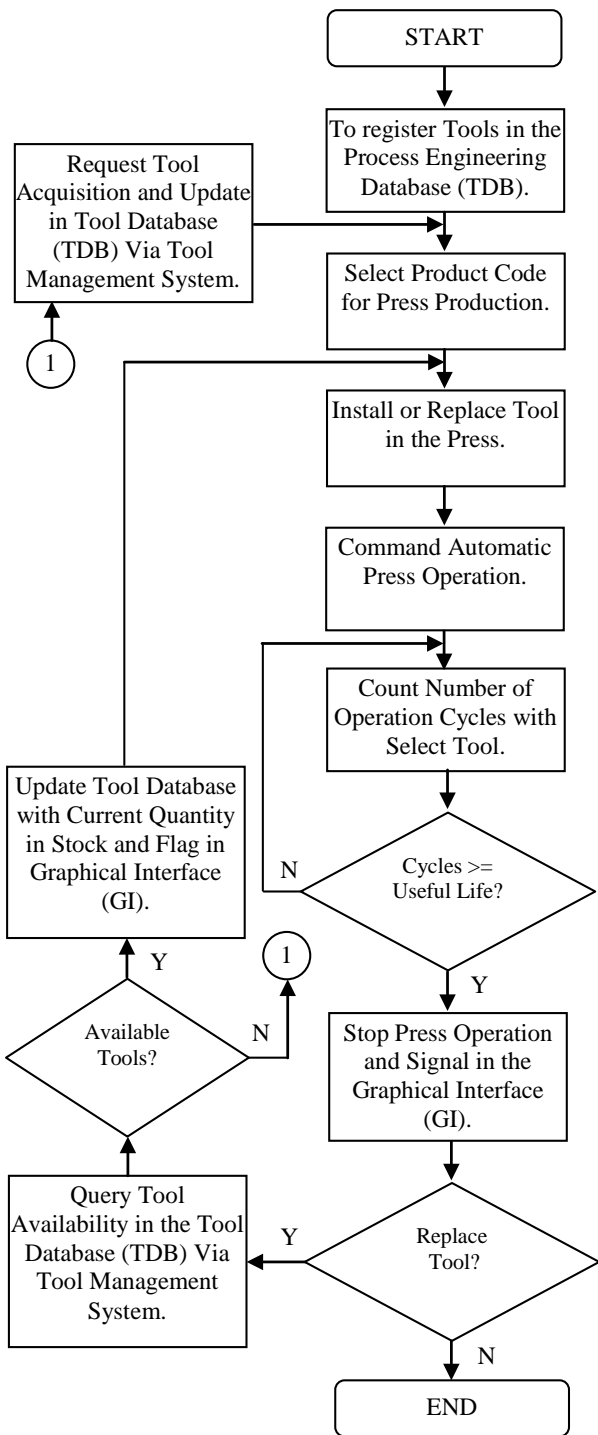


Figure 3: Flowchart of the management system.

b) Graphical interface

In the windows of the Graphical Interface (GI) that was elaborated for the prototype are present the resources that allow to the user of the system to make the registrations of the codes of the products, codes of the tools, times of useful life and also, to carry out the control, the monitoring and the interruption of the operation of the Representative Press Model (RPM).

In this sense, Figure 4 presents the virtual resources that were established for the Tool Registry

Window, which specifically serves the Process Engineering Sector.

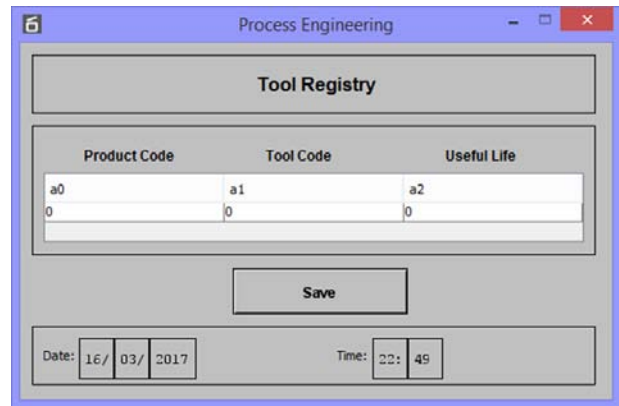


Figure 4: Process engineering tool registry window.

The virtual components contained in the Tool Registry Window, shown in Figure 4, allow the Process Engineering user to perform the following tasks:

- Enter each product code number in the field labeled "Product Code", where this field establishes the product that will be manufactured in a specific production process by the press.
- Register each tool code in the field called "Tool Code", this field contains the code defined by the company for each tool that is used by the press in its various manufacturing processes.
- Register each tool life in the field called "Useful Life", this field establishes the useful life for each tool that is used in the press in nominal use condition.
- Save in the Process Engineering Sector Database (TDB) the information that has been registered through the Tool Registry Window.
- View the current date (Date) and time (Time), which are provided by the Host Computer (HC).

The Figure 5 presents the virtual resources that were developed to attend the Press Window, which is dedicated to the production process. In this window there are regions with virtual components that allow the production user to perform the following tasks:

- Set the automatic mode (Automatic) or manual mode (Manual) of the press by means of the virtual key contained in the region that is called Press Condition. The manual mode is basically used to carry out tool replacement in the press or set the initial state of the actuator (Ready Press) to set the reference in the automatic operation.
- Execute the production cycle for the press using the button labeled "On/Off Cycle", which is present in the region called "Auto Mode Functions". When this button is activated the visual signaling that is called "On Cycle" will be activated (red color) and the one called "Cycle Off" will be inactive (black color), and the button in the not activated state will occur vice versa. It should be mentioned that in this region

- Display the information of the product code number in the field labeled "Product Code", the tool code in the field called "Tool Code", and the tool useful life in the field called "Useful Life", which are displayed in the columns in the main part of that window.
- Update the records of tools carried out by Process Engineering, through the button called "Open Database".
- Select a particular tool to process through the button labeled "Select". It should be mentioned that the user after choosing the tool and pressing the "Select" button, the information about this tool is loaded in the respective fields of the region named "Management Tool Life", which belongs to the Press Window.
- View the current date (Date) and time (Time) provided by the Host Computer (HC).

The Figure 7 presents the virtual resources that have been developed to meet the Tool Stock Window, which is dedicated to the Tool Management System.

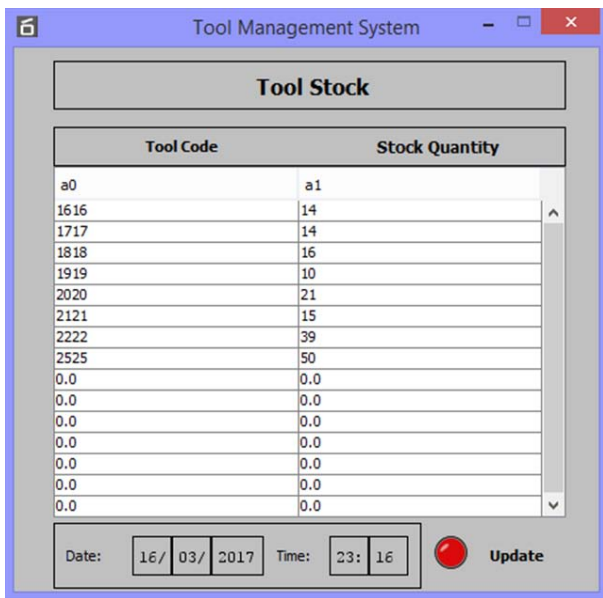


Figure 7: Tool stock window.

In the Tool Stock Window, shown in Figure 7, there are virtual components that allow the Tool Management System user to perform the following tasks:

- View information about the tool code in the field called "Tool Code", and the current number of tools available in the company stock in the field called "Stock Quantity", which are displayed in the main display columns of that window.
- View the state of the signaling that informs the update (Update) of information in the Tool Database (TDB).
- View the current date (Date) and time (Time) provided by the Host Computer (HC).

c) Practical tests

The following sequence of actions was performed to evaluate the effectiveness of the virtual components provided in the Graphical Interface (GI) windows that is proposed in this work:

- Perform the mechanical assemblies and electrical connections required to meet each of the components provided in the prototype shown in Figure 2.
- Program the Arduino UNO (AU) and Arduino MEGA 2560 (AM) units, in accordance with the respective proposals for use and the steps established in the analytical flowchart shown in Figure 3.
- Design the Graphical Interface (GI) with windows, layouts of virtual components, resources and structural hierarchy, as shown in Figure 4, Figure 5, Figure 6 and Figure 7.

The execution of the practical tests was divided in three stages, in the first one were evaluated specifically the resources related to the Tool Registry Window. This step evaluated the effectiveness of the resources provided in this window in allowing the user of the Process Engineering Sector to register the tools according to the standards adopted by the company.

In the second step it was observed whether the records registered in the Tool Registry Window were updated in the Tool Database (TDB) and available for selection in the Tool Selection Window of the Tool Preparation Room.

In the third stage the operation of the Representative Press Model (RPM) was activated, through the Press Window, in order to observe the capacity of the virtual components of this window in signaling the appropriate moment to carry out the replacement of the tool used in the current manufacturing process of the press. In this sense, Figure 8 shows the signaling activated when the current number of tool operation cycles (Tool Life = 4) is equal to the number of operations that was established by Process Engineering (Tool Limit = 4), stopping the cycle of the Representative Press Model (RPM).

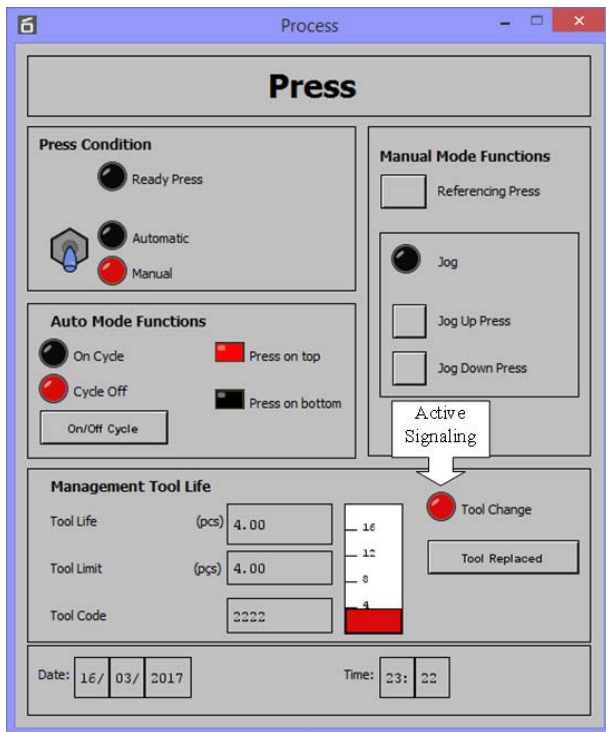


Figure 8: Signaling for tool replacement.

d) Results

The results observed in the practical tests were positive, because, with the virtual components established for the Graphical Interface (GI) windows, proposed in this work, it was possible to perform the tool registry, the selection of a specific tool to attend a given process, and the identification of the appropriate moment to perform the tool replacement, that is, the moment in which the useful life of the tool (Tool Life) is equal to the number of operations established for it (Tool Limit).

V. CONCLUSIONS

The positive results obtained in the practical tests suggest that the Graphical Interface (GI), when integrated into the real system that performs the management in the use of tools for an eccentric industrial press, may be able to aid a tool registration a selection of a specific tool to attend the process and, mainly, an identification of the appropriate moment to make the replacement of the tool in order to minimize the consequences from the premature or late tool replacement.

The virtual features contained in the Tool Selection Window and Tool Stock Window allow the user to view up-to-date information that are stored in the Tool Database (TDB), which minimize the possibility of errors in estimating available quantities of tools in the company stock.

The layout, expressiveness and details contained in the virtual components that were developed for

the Graphical Interface (GI) windows provided an intuitive and informative environment for the user of the Process Engineering Sector and the Tool Preparation Room to carry out their respective activities in the company, which collaborates to minimize the occurrence of operating errors in the use of the tool management system.

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Technological Methods Analysis in the Field of Exaflops Supercomputers Development Approaching

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Abstract- In this article authors describe new supercomputing developing roadmaps, illustrate how to solve Moore's Law problem. Authors show two different ways of creating new high-productive clusters: evaluative and revolutionary. There are new era so-called "Post Moore". It means specialists all over the World should together in collaboration create new electronic components, architecture principles, design criteria and etc.

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Technological Methods Analysis in the Field of Exaflops Supercomputers Development Approaching

Molyakov Andrey Sergeevich ^α & Eismont Leonid Konstantinovich ^ο

Abstract- In this article authors describe new supercomputing developing roadmaps, illustrate how to solve Moore's Law problem. Authors show two different ways of creating new high-productive clusters: evaluative and revolutionary. There are new era so-called "Post Moore". It means specialists all over the World should together in collaboration create new electronic components, architecture principles, design criteria and etc.

1. INNOVATIVE PROJECTS OF NEW SUPERCOMPUTERS DEVELOPING

Наиболее заметным явлением в начале работ по экзамасштабной и экзафлопсной тематике было проведение рабочих групп по инициативе DARPA. Далее, в 2010 году, была запущена новая программа DARPA UHPC развития экзамасштабных технологий. В этой программе участвуют четыре группы, каждая из которых, состоит из коммерческих компаний, национальных лабораторий и университетов: проект Runnemed (Intel), проект Echelon (NVIDIA/Cray), проект X-calibr (Лаборатория Sandia), проект Angstrom (MIT).

В соответствии с исторически сложившимися традициями, DARPA в своих работах заняла нишу инновационных проектов, а DOE (Министерство энергетики США) проводило большей частью эволюционную линию по созданию рекордно крупных суперкомпьютеров, прежде всего в двух ультракомпьютерных центрах в Окриджской и Аргонской лабораториях.

Тезис 1. DARPA (представляет военных и разведывательные службы США) не справилась в полной мере с задачами создания перспективных систем петафлопсного уровня реальной производительности (программа DARPA HPCS) и настолько неудачно начала работы по экзамасштабным технологиям (программы DARPA UHPC и OHPC), что можно говорить об их тупиковости и близком преждевременном закрытии. В связи с этим, ответственность за выполнение работ по экзамасштабной тематике теперь возлагается на DOE.

Тезис 2. Неудачи начала работ по экзамасштабным системам объясняются низким уровнем инновационности проектов DARPA UHPC и слабым вовлечением талантливых разработчиков в эти проекты.

Тезис 3. Инновационный стиль в виде работ инновационного характера, от алгоритмов и прикладного программного обеспечения до элементно-конструкторской базы, противопоставляется стратегии эволюционного развития за счет постепенного введения улучшений, "инкрементному" развитию, которое считается тупиковым в долгосрочной перспективе, но выгодно экономически основным промышленным вендорам (Intel, IBM, Cray, NVIDIA и др.).

Тезис 4. В качестве ключевой темы продвижения к экзамасштабным системам ставится вопрос создания новых моделей организации и выполнения параллельных программ. Выделяются два подхода, эволюционный и инновационный.

Тезис 5. Переход инициативы по работам экзамасштабной тематики к DOI и необходимость преобладания в этих работах инновационного подхода объясняется тем, что США могут потерять мировое лидерство в данной области, а это место будет немедленно занято другими странами, вероятнее всего из Азиатско-Тихоокеанского региона. Эти страны пойдут на любые риски в выборе наиболее оптимальных стратегий, поскольку находятся в роли догоняющих. В связи с этим, подключение самой мощной в США научно-технической инфраструктуры DOE в виде не только научных национальных лабораторий, но и национальных лабораторий ядерного оружейного комплекса к решению этой ставшей важнейшей проблемы современности, которые также и скоординируют работу ведущих университетов и промышленных вендоров, – принципиально необходимо.

Тезис 6. Стремление к мировому лидерству США в области экзамасштабных систем и их приложений должно быть совмещено с международным сотрудничеством по этой линии с целью эффективного использования мировых интеллектуальных ресурсов и достижений.

Тезис 7. Разработка экзамасштабных систем и технологий их применения для решения важнейших задач к концу 2020 года рассматривается как качественный переход в области основ и технологий, организации соответствующей инфраструктуры исследований и разработок для создания систем зеттафлопса и йотафлопса, в которых будут применены новейшие достижения нанотехнологий.

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Таким образом, для формирования мнения о проектах экзафлопсной тематики можно теперь рассматривать, в основном, работы DoE. Формирование крупного проекта DoE по созданию экзафлопсной машины на эволюционных или инновационных принципах задерживается. Сейчас выполняется множество небольших проектов по разным направлениям большим количеством групп.

В целом, в настоящее время в DoE имеются следующие направления работ по тематике экзамасштабных систем (это сейчас называют "exascale ecosystem", экзамасштабная экосистема, которые в 2013 году должны дополниться направлением OS/R или «Экзамасштабные операционные системы и системы поддержки выполнения программ» (Exascale Operating and Runtime Systems).

Далее приведем краткие сведения об этих программах. Эта информация специально структурирована так, чтобы в будущем вносить дальнейшие уточнения.

В части технологии создания процессора этот проект связан с работами по линии архитектуры Intel MIC (Many Integrated Core), которая в настоящее время стала называться Xeon Phi. Это микропроцессор со множеством облегченных 4-х тредовых ядер с системой команд X86 и векторными расширениями. В 2012 году вышел образец сопроцессорной платы Knight Corner с таким микропроцессором, изготовленным по технологии 22 нм, имеющим более 50 процессорных ядер. Эта плата содержит 8 Гбайт памяти GDDR5 и подключается через шину PCI Express. Производительность такого сопроцессора около 1 Тфлопс. Этот сопроцессор виден приложению как

вычислительный узел, работающий под управлением ОС Linux, так что его использование ожидается более простым, чем современных графических процессоров.

В части технологии памяти Intel в этом проекте будет работать с Micron Technologies над созданием гибридного куба памяти (HMC, Hybrid Memory Cube). Это вариант технологии 3D сборки кристаллов процессоров и памяти, что должно значительно повысить пропускную способность интерфейса процессора с памятью и снизить задержки обращений к памяти.

Например, экзамасштабные приложения разрабатываются уже в настоящее время в центрах разработки, как и специальное оборудование для этих приложений. Так что вероятен и вариант появления не одного (например, в Окриджской лаборатории), а нескольких образцов экзамасштабных систем, причем в этих центрах со-разработки, т.е. в Лос-Аламосской и Аргонской лаборатории, лаборатории Сандиа.

Кстати, Лос-Аламосская лаборатория и лаборатория Сандиа образовали недавно совместный центр ASEC, причем лаборатория Сандиа также сотрудничает с Окриджской лабораторией в рамках работ образованного в DoE Института перспективных архитектур и алгоритмов. Лаборатория Сандиа и Лос-Аламосская лаборатория имеют мощный производственный комплекс, выполняющий все виды работ, включая и работы по нанотехнологиям.

В таблицах 1 и 2 приведены "дорожные карты" реализации суперкомпьютеров эволюционного направления в Окриджской и Аргонской лабораториях. Рубеж в 30 PFlops взят в 2012 году.

Таблица 1: Оценки характеристик систем, создаваемых на этапах экзафлопсного проекта с применением тяжелых" процессорных ядер

Системная характеристика	Годы внедрения системы			
	2009	2011	2015	2018
Общая пиковая производительность	2 PF	20 PF	100-200 PF	1 EF
Общий объем оперативной памяти	0.3 PB	1 PB	5 PB	10 PB
Пиковая производительность узла	125 GF	200 GF	400 GF	1-10 TF
Пропускная способность памяти узла	25 GB/s	40 GB/s	100 GB/s	200-400 GB/s
Количество ядер в узле (параллелизм узла)	12	32	0(100)	0(1000)
Пропускная способность сетевого интерфейса узла	1.5 GB/s	10 GB/s	25 GB/s	50 GB/s
Количество узлов в системе	18,700	100,000	500,000	0(Million)
Количество ядер в системе (общий параллелизм)	225,000	3 Million	50 Million	0(Billion)
Общая мощность потребления	6 MW	~10 MW	~10 MW	~ 20 MW
Объем внешней памяти	15 PB	30 PB	150 PB	300 PB
Пропускная способность ввода-вывода	0.2 TB/s	2 TB/s	10 TB/s	20 TB/s
МТП, время между прерываниями по сбою или отказу системы	Days	Days	Days	0(1Day)

В Окриджской лаборатории потенциал модернизации суперкомпьютера XC30 до 100 Pflops, а для Аргонской лаборатории разрабатывается новый суперкомпьютер IBM BlueGene/R с производительностью до 100 Pflor/s, он будет введен в эксплуатацию в 2015 году. Кроме того, фирма IBM

готовит новый вариант суперкомпьютера на базе нового микропроцессора Power 8. По информации из экспертной среды, это 256 ядер, ядра трех типов – суперскалярные, легкие и легкие с векторными ускорителями, все ядра 6-тредовые.

Таблица 2: Оценки характеристик систем, создаваемых на этапах экзафлопсного проекта “легкого” направления

Системная характеристика	Годы внедрения системы				
	2004	2007	2012	2015	2019
Наименование системы/этапа	BG/L	BG/P	BG/Q (ONE)	TWO	THREE
Общая пиковая производительность	0.37 PF	1 PF	27 PF	309 PF	1127 PF (1.127 EF)
Общий объем оперативной памяти	0.034 PB	0.151 PB	2.147 PB	8.590 PB	25.770 PB
Пиковая производительность узла	5.6 GF	14 GF	205 GF	1.178 TF	4.301 TF
Пропускная способность памяти узла	5.6 GB/s	13.6 GB/s	42.6 GB/s	?	?
Объем памяти узла	0.5 GB	2-4 GB	16 GB	?	?
Количество ядер в узле (параллелизм узла)	2	4	16	32	96
Тактовая частота (GHz)	0.7	0.85	1.6	2.3	2.8
Количество запускаемых операций за такт в ядре	4	4	8	16	16
Пропускная способность сетевого интерфейса узла	2.1GB/s –3D torus 0.7GB/s – tree	5.1GB/s-3D torus 1.7GB/s-tree	40 GB/s-5D torus 4 GB/s extlinc	?	?
Количество узлов в системе	65536	73728	131072	262144	262144
Количество ядер в системе (общий параллелизм)	0.13 Million	0.3 Million	2 Million	8 Million	25 Million
Общая мощность потребления	2.5MW	4.8MW	8MW	30MW	40MW
Количество узлов в стойке	1024	1024	512	1024	1024
Количество стоек	64	72	256	256	256
Пиковая производительность стойки	5.7 TF	14 TF	105 TF	1.206 PF	4.404 PF

За 2011–2014 годы NUDT разработал новую суперкомпьютерную систему Tianhe-2 – шифр «Полет Дракона», с производительностью 30 Petaflops. Ожидается использование микропроцессоров Godson-3C или Godson-4A, мультитредовых микропроцессоров FT-1500, новой версии коммуникационной сети Arch. Эта разработка противопоставляется американскому суперкомпьютеру IBM Sequoia на 18-ядерных (4 треда в ядре) микропроцессорах PowerPC и 5-мерной сети типа тор. Ставится цель превзойти американский суперкомпьютер в 1.5 раза. Есть сведения, что этот суперкомпьютер уже практически готов, но это скрывается.

До 2016 года четырем ведущим исследовательским центрам Китая Министерства Обороны, Министерства энергетики и Министерства образования и промышленных технологий Китая (это National Air and Space Intelligence Center (NASIC), NUDT, ICT и National Applied Research Laboratories (NARL)) поставлена задача разработать и ввести в эксплуатацию вычислительный комплекс под кодовым названием «Тайваньский ястреб» производительностью

100 Petaflops. При этом важнейшую роль должна сыграть тайваньская фабрика TSMC; она на 60% принадлежит Китаю, на 20% принадлежит Японии, на 20% – иностранному капиталу США и Западной Европы (по данным конца 2011 года). По данным на сентябрь 2012 года доля Китая в TSMC составляет уже 75–80%.

Гетерогенный суперкомпьютер, включающий три типа массово-мультитредовых микропроцессоров, классические суперскалярные микропроцессоры, графические микропроцессоры и сетевые микропроцессоры. Базовый микропроцессор – ST-2, возможны его модификации. Конструктив 4D с жидкостной системой охлаждения. Здесь будут использоваться новые микропроцессоры линейки Godson-3 и Godson-4 с тяжелыми суперскалярными ядрами, а также линейка Godson-T с легкими ядрами (типа микропроцессора Tileria).

Начиная с 2007 года Япония не имела систем, входящих в первую десятку списка Top500, т.е. систем петафлопсного класса производительности. Новые

планы развития имели цель изменить эту ситуацию, что и произошло сначала осенью 2010 года (Tsunami 2.0), а потом летом 2011 года (K-компьютер).

Tsunami 2.0 был о запущен осенью 2010 года и попал на 4-е место ноябрьского списка Top500 с пиковой производительностью 2,39 PFLOPS и производительностью на тесте Linpack около 1.2 PFLOPS. С 2013 по 2015 планируется разработать обновленную версию Tsubame 3.0 производительностью 30 PFlops, потребляемая мощность оценивается на уровне 1 MW, а стоимость – около 65 млн. долларов. Суперкомпьютеры Tsubame можно отнести к суперкомпьютерам традиционного кластерного типа. Это направление не считается как основное, определяющее будущее области суперкомпьютерных вычислений Японии. Работы ведутся в Токийском технологическом институте.

K-компьютер – самый мощный и статусный суперкомпьютер Японии, разработанный по программе создания перспективных стратегических суперкомпьютеров. Головная организация этого проекта - Институт физических и химических исследований (RIKEN). Этот институт наиболее приближен к Министерству образования, культуры, спорта, науки и технологии (MEXT), отвечающего за суперкомпьютерную тематику, является его исследовательским центром. Этот проект можно считать японским ответом на американский проект DARPA HPCS.

В общей сложности, этот проект создания K-компьютера обошелся в 1 млрд. евро (около 1.5 млрд. долларов). Пиковая производительность K-компьютера ~ 10 PFLOPS, производительность на тесте Linpack – 8-9 PFLOPS, но самая важная характеристика – возможность достижения на реальных приложениях производительности около 1 PFLOPS. В качестве таких приложений разработчики ориентировались на 8 прикладных областей, при этом в качестве главных областей были выделены нанотехнологии и область живых систем.

Имеются также партнерские проекты государственного и частного сектора, ориентированные на разработку программного обеспечения систем экзауровня. Один из таких проектов – EADI (Exascale Application and Data Initiative), возглавляемый фирмой Fujitsu и представляющий собой заключительный этап разработки 10 PFLOPS-ой системы (RIKEN NGS, K-компьютер) для японского правительства. EADI – это комплексный проект, который включает все направления работ Fujitsu по линии экзафлопсных вычислений.

Особняком стоит проект создания военного суперкомпьютера экзамасштабного уровня «Стрела времени», который рассматривается далее в этом разделе. Ведутся работы по био- и квантовым компьютерам, но информации о создании суперкомпьютеров на этих технологиях пока нет.

До недавнего времени в Западной Европе большая часть работ в области суперкомпьютеров и суперкомпьютерных вычислений была связана с разработкой того или иного программного обеспечения, множества приложений, а также новыми направлениями в области элементно-конструкторских технологий. Значительная часть специалистов работала и работает в филиалах американских фирм, причем их привлечение к такой работе со стороны американцев – сознательная и широко проводимая политика использования зарубежных высококвалифицированных ресурсов в интересах США.

Оригинальных инновационных суперкомпьютерных проектов, тем более связанных с разработкой собственной элементной базы, не было, хотя возможности для таких работ явно были и есть, но проводилась политика полностью ориентироваться в этих вопросах на американские разработки. Исключением была только разработка коммуникационной сети EXTOLL.

В последние два года ситуация стала меняться, появилась политическая воля иметь в Европе большую самостоятельность в такой важнейшей отрасли, как суперкомпьютерные технологии и их приложения. В связи с этим, появились инновационные проекты создания аппаратных средств мультипетафлопсных и экзафлопсных систем, например DEEP (с применением Xeon Phi, сети EXTOLL, правда при значительном участии Intel), Mont Blanc (ядра ARM и графические ускорители Nvidia). Запущен крупнейший проект разработки программного обеспечения экзамасштабных систем CREST.

При этом ряд проектов был запущен с сильным участием российской компании T-платформы, которая кроме кластерных суперкомпьютеров при этом выходила и на инновационные возможности создания собственной элементно-компонентной базы. Есть мнение, что именно это и было главной причиной занесения компании T-платформы в марте 2013 года в США в черный список.

Знаковых успехов по линии создания мультипетафлопсных суперкомпьютеров и продвижения к экзафлопсным системам пока нет, хотя несколько крупных кластеров собственной разработки уже попали в верхнюю часть списка Top500 и организовано несколько вычислительных центров, ориентированных в будущем на вычисления экзафлопсного уровня.

Самый мощный суперкомпьютер петафлопсного уровня был недавно разработан в ФГУП РФЯЦ-ВНИИЭФ (г.Саров). Эта организация является реальным лидером в сегменте суперкомпьютеров высшего диапазона производительности. Очевидно, что именно на эту организацию сделана ставка Правительством в вопросе создания суперкомпьютеров уровня 10, 100 и 1000 Пфлос. Это предприятие ядерного оружейного комплекса и атомной

промышленности, что все объясняет. ГК «Росатом» подготовила в мае 2011 года Концепцию по экзафлопсным технологиям, в ее подготовке участвовали несколько организаций (Департамент развития научно-производственного блока ядерного оружейного комплекса, ФГУП «РФЯЦ-ВНИИЭФ», ФГУП РФЯЦ-ВНИИТФ, Департамент развития Минобрнауки России, ФГУП «НИИ «Квант», ИПМ им. М.В.Келдыша РАН, ИПС им. А.К. Айламазяна РАН, НИИСИ РАН, МСЦ РАН, НИИММ им. Н.Г. Чеботарева при КГУ, ННГУ им.Н.И.Лобачевского, МГУ им. М.В.Ломоносова, МАИ, МГТУ им. Н.Э. Баумана), а ФГУП РФЯЦ-ВНИИЭФ была головной.

Т-платформы и МГУ им. М.В.Ломоносова – это другая группа разработчиков, они недавно подписали Меморандум о намерениях по сотрудничеству в области создания суперкомпьютеров нового поколения экзафлопсного уровня. Это конкуренты группы, возглавляемой ФГУП «РФЯЦ-ВНИИЭФ».

Основу еще одной группы составляют ИПМ им. М.В.Келдыша РАН и ФГУП НИИ «Квант» – две организации с большой историей в несколько десятилетий совместных работ. Кроме сотрудничества с ФГУП «РФЯЦ-ВНИИЭФ» они ведут ряд проектов самостоятельно, поскольку работают главным образом в других прикладных областях. В последнее время к этой группе присоединились ЗАО «ВТ-Консалтинг», ООО «Е-троник», СПбГПУ, ФГУП ВО «Внештехника», Центр инженерных разработок физического факультета МГУ. Всегда было сотрудничество с ИПС им. А.К. Айламазяна РАН и ОАО «НИЦЭВТ», МСЦ.

По инновационной линии пока созданы три центра соразработки специальных экзафлопсных суперкомпьютеров для следующих областей: материаловедение (LANL – головной исполнитель), перспективные реакторы (ANL – головной исполнитель) и процессы горения (SNL – головной исполнитель). Это направление можно охарактеризовать как оптимизацию применения КМОП-технологий. Ведется множество небольших проектов фундаментальных исследований, причем одно из основных направлений – новые модели организации параллельных программ для экзафлопсных суперкомпьютеров, новые run-time системы и операционные системы, новые средства параллельного программирования.

Крупная инновационная программа разработки в DoE инновационного экзафлопсного суперкомпьютера ожидается после 2013 года. В настоящее время для поддержки основных промышленных вендоров запущена предапрительная программа FastForward по разработке процессоров и модулей памяти, а также системы хранения данных. По мнению авторов, наибольший интерес представляет проект фирм Cray/NVIDIA Echelon.

Китай традиционно виртуозно воспринимает, копирует и развивает чужие проекты, применяя при этом новейшие технологии, которые также часто имеют

иностранное происхождение, часто из Японии, Сингапура и Тайваня. Ведутся эволюционные и инновационные направления. Главный разработчик – Министерство обороны Китая в виде NUDT, университета оборонных технологий Китая.

По эволюционной линии авторы оценивают отставание Китая от США не более 2-3 лет. В настоящее время собран суперкомпьютер Tianhe-2 (проект «Полет дракона») с производительностью 30 Пфлопс. Эта разработка пока не афишируется, применяется американская и собственная элементная база, как микропроцессоры, так и сетевые СБИС. В 2016 году должен быть построен 100 Пфлопсный суперкомпьютер (проект «Тайваньский ястреб»).

По инновационной линии пока известно лишь о проекте СТ-2 создания военного суперкомпьютера (проект «Удар грома») экзафлопсного уровня производительности для решения информационных задач, на базе которого посредством изменения баланса в используемых микропроцессорах разного типа можно построить экзафлопсный суперкомпьютер и для научно-технических расчетов. По имеющимся сведениям, в основу этого проекта были положены российский проект СКСН Ангара и американский ParalleX.

Япония более самостоятельна и креативна в своих проектах, отличается сильной закрытостью. Имеются эволюционные и инновационные направления. По эволюционному направлению был разработан суперкомпьютер Tsubame и K-компьютер (10 петафлопс), причем если в Tsubame используется американская элементная база, то в K-компьютере – собственная. Намечено создание Tsubame-3 производительностью 30 петафлопс.

По инновационной линии известен пока лишь проект военного суперкомпьютера «Стрела времени», который разрабатывается Силами самообороны Японии, но за счет изменения состава микропроцессоров в вычислительных узлах может быть переориентирован на решение научно-технических задач.

По линии работ по элементной базе пост-Муровской эры достигнуты заметные результаты в области сверхпроводниковой электроники, которые можно расценивать как не хуже американских, по квантовым клеточным автоматам и квантовым компьютерам. Традиционно сильны позиции по коммуникационным сетям, где их можно считать мировым лидером.

Западная Европа ведет проекты экзафлопсных суперкомпьютеров эволюционного типа DEEP и Mont Blanc, это новое явление, раньше разработкой собственных аппаратных средств так активно не занимались, использовалась американская техника. Есть потенциал организации и самостоятельных инновационных проектов, но слишком много специалистов работают на США и Китай. Обсуждается вопрос создания европейского микроэлектронного гиганта типа фирмы Intel. Организовано несколько

центров выполнения экзафлопсных вычислений, много работ ведется по программному обеспечению для экзафлопсных систем.

В России пока только сформулирована Концепция экзафлопсных технологий, крупное целевое финансирование пока не открыто, но финансирование отдельных небольших исследовательских проектов уже началось.

Количество публикаций по этой тематике в России невелико. Судя по открытым данным, отставание России от США в классе задач с хорошей пространственно-временной локализацией обращений к памяти составляет около 10 раз (тест Linpack, рейтинг Top500), а в классе задач с плохой пространственно-временной локализацией – не менее 100 раз (тест BFS, рейтинг Graph500, тест RandomAccess, рейтинг HPCChallenge).

II. SUPERCOMPUTERS OF POST MOORE'S ERA

Считается, что предел развития кремниевых технологий – около 5 нм. Имеются оценки, что этот предел будет достигнут в 2020-2024 году, а проблемы начнутся уже после 2014 года, когда будет достигнут уровень 14-15 нм. Это пессимистические прогнозы.

Оптимистичные прогнозы по развитию КМОП-технологий обычно исходят от фирмы Intel. В настоящее время Intel и еще три фирмы в мире (TSMC, STMicroelectronics и Samsung) промышленно освоили технологию 22 нм, это произошло в 2012 году, в то время, как по прогнозам это должно было произойти в 2016 году.

Важны также планы по усовершенствованию модулей памяти, поскольку от них сильно зависит сейчас быстродействие и энергопотребление систем. Фирма Intel ведет работы совместно с фирмой Micron Technologies над созданием гибридного куба памяти (HMC, Hybrid Memory Cube).

Оптимистические прогнозы развития кремниевых технологий в настоящее время несколько успокаивают, но уже не позволяют снять напряженность в исследованиях и разработках по новым вариантам элементной базы для логических схем и памяти, вариантов соединения компонентов кристаллов и собственно кристаллов. Срок достижения предела развития кремниевых технологий (окончания действия закона Мура) близок и темпы приближения к нему опережают предсказания, что видно на примере технологий 22 нм. Работы по созданию новых технологий элементной базы ведутся уже несколько десятилетий, но в настоящее время ни одна из них не готова для практического использования вместо кремниевых технологий и пока речь может идти о выборе тех, которые можно было бы использовать хотя бы через 10 лет.

Вместе с тем, для отдельных вычислительных устройств и даже блоков готовые решения есть, есть и явно приоритетные направления исследований по

элементной базе будущих суперкомпьютеров с уровнем производительности зетта (10^{21}) и более уровня.

Тем не менее, круг занятых в этих работах российских специалистов стал обозначаться, началась работа с ними как с экспертами данной области. В дальнейшем это поможет дать более точные и объективные оценки, но уже первый опыт изучения этих направлений показал, что эту работу уже пора вести непрерывно, содействовать координации.

Достижение физических ограничений КМОП-технологий и проводимые работы по новым технологиям, которые напрямую связаны с наномиром и квантовыми эффектами, сделали вновь актуальными несколько забытые в разработчиках суперкомпьютеров в период успешного действия закона Мура вопросы по физическим ограничениям производительности вычислительных систем. При оценке и систематизации работ по разным вариантам перспективной элементной базы и оценке пределов повышения производительности суперкомпьютеров (это было одной из задач данного прогноза) оказалось удобным использовать базовые положения этих работ, а именно: оценку минимальных энергетических затрат (ограничение Лэндауэра); утверждение о зависимости вычислений и выделяемого тепла, т.е. связи между информацией и термодинамикой (принцип Неймана-Лэндауэра).

По представленному в разделе материалу можно акцентировать внимание на следующих аспектах:

1. В настоящее время промышленно освоена кремниевая (КМОП) технология 22 нм, но всего четырьмя фирмами в мире – Intel, TSMC, Samsung и STMicroelectronics. По прогнозу Intel, уровень 5 нм будет достигнут уже в 2020 году. Считается, что дальнейшая миниатюризация невозможна, а закон Мура перестанет работать не позже 2024 года.
2. Эффективное использование КМОП-технологий в ближайшее время и вплоть до окончания действия закона Мура связано с различными приемами оптимизации: архитектуры (специализированные процессоры и ускорители), микроархитектуры; соединений на кристалле и между кристаллами; конструктивов в виде 3D сборки модулей и 3D СБИС. Работы такого типа ведутся и рассматриваются как основные при создании экзафлопсных суперкомпьютеров, но они связаны с преодолением слишком многих проблем.
3. Новые решения в области элементно-конструкторской базы (технологий пост-Муровской эры) могут облегчить создание экзафлопсных суперкомпьютеров и стать основой для создания суперкомпьютеров следующих уровней производительности. Основные задачи при разработке новых вариантов элементной базы: повысить частоту работы, снизить

энергопотребление, добиться высокого уровня интеграции.

4. Современные микропроцессоры работают на частоте 3-6 GHz. Устройства на базе сверхпроводниковых технологий (RSFQ) могут работать на частоте нескольких сотен GHz, по публикациям известны образцы, работавшие на частотах 20GHz и 80 GHz. Устройства на базе квантовых клеточных автоматов (QCA) могут работать на частоте порядка THz.
5. Теоретический предел затрат на обработку одного бита информации в обычных компьютерах неререверсивного типа составляет $kT \ln 2$ (ограничение Лэндауэра, далее для простоты kT). Для устройств на современных КМОП-технологиях характерна оценка затрат на бит обрабатываемой информации около 1000000 kT . Из публикаций известно о получении на экспериментальном сумматоре на базе RQL-технологии (оптимизированного варианта RSFQ-технологий) затрат на один бит в 1000 kT . Было также опубликовано, что при использовании nSQUID технологии (еще один оптимизированный вариант RSFQ) было получено, что на устройстве типа сдвиговый регистр затраты на один бит составляют несколько kT . Это обнадеживающие результаты, но следует признать, что такие технологии пока пригодны для создания реконфигурируемых решающих полей арифметических устройств для реализации потоковых вычислений. Работ по продвижению этих технологий еще много, эти технологии будут наверняка применяться в сочетании с КМОП-технологиями.
6. Ограничение Лэндауэра и требование обеспечения работы без сбоев на протяжении длительных отрезков времени для обычных суперкомпьютеров неререверсивного типа (без специальных мер по сохранению энергии при обработке информации) обуславливают верхнюю границу физически достижимой производительности суперкомпьютеров в несколько десятков эксафлопс, это ограничение получило название «точка Стерлинга».
7. Дальнейший рост производительности возможен после принципиального пересмотра суперкомпьютеров и перехода к их реверсивной организации, а также применению хотя бы в виде ускорительных блоков квантовых компьютеров и аналоговых компьютеров, возможно также построенных на квантовых принципах.
8. Важнейшим первым в истории вычислительных систем примером компьютера последнего типа является 128-кубитовый квантовый компьютер канадской фирмы D-Wave. Процессор этого компьютера быстрее двух процессоров Xeon (2.6 GHz) на переборном алгоритме глобальной

минимизации на четыре порядка, но требует для работы температуры, близкой к абсолютному нулю.

9. Работы по вариантам перспективной элементной базы пост-Муровской эры отлично организованы на федеральном уровне в США, активно ведутся в Японии. Эти две страны – явные мировые лидеры в этом плане.

III. SUMMARY

1. Есть значительные проблемы при создании эксафлопсных суперкомпьютеров по производительности и энергопотреблению (сеть, память и процессор), отказоустойчивости и продуктивности программирования.
2. Элементно-конструкторская база позволяет использовать мультядерность (1000-кратно), повышенную пропускную способность кристаллов по вводу-выводу (3D-компоновка, TSV), оптические соединения между платами («Holley», WDM-технологии) и внутри кристаллов (нанотрубки), новая технология памяти (HMC, NVRAM).
3. Есть архитектурно-программные решения – массовая мультитредовость и модель разделения вычислений/доступа к данным (MT и DAE), потоковость (MD/DF), локализация данных и вычислений (RPC), гибридность/функциональная специализация (10x10), глобально-адресуемая память (PGAS/APGAS/HPGAS), интеллектуальная отказоустойчивость (Resilience).
4. Подходы к решению – эволюционный (DoE NNSA/ASCR), умеренно-инновационный (DoE ASCR), агрессивно инновационный (DARPA), инновационно-эволюционный&эмуляционный (DoE ASCR, NSF).
5. Сложность проблем и неочевидность решений за рубежом потребовала привлечения ресурсов не только на федеральном уровне, но и на региональном и мировом. Цели работ по эксамасштабной тематике (DARPA) и эксафлопсной (DoE) имеют отличия, но методы их достижения во многом совпадают. Зарубежный опыт показывает, что в организационном плане важным является централизованная формулировка целей работ и управления ими (формирование и поддержка «силового поля»). Работы по эксафлопсным суперкомпьютерам находятся на переходном этапе от применения только кремниевых технологий к переходу на применение пост-Муровских перспективных технологий.

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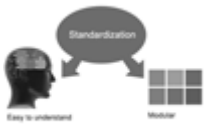




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The “FARSC” is a dignified title which is accorded to a person’s name viz. Dr. John E. Hall, Ph.D., FARSC or William Walldroff, M.S., FARSC.

The IFOARS institution is entitled to form a Board comprised of one Chairperson and three to five board members preferably from different streams. The Board will be recognized as “Institutional Board of Open Association of Research Society”-(IBOARS).

The Institute will be entitled to following benefits:



The IBOARS can initially review research papers of their institute and recommend them to publish with respective journal of Global Journals. It can also review the papers of other institutions after obtaining our consent. The second review will be done by peer reviewer of Global Journals Incorporation (USA) The Board is at liberty to appoint a peer reviewer with the approval of chairperson after consulting us.

The author fees of such paper may be waived off up to 40%.

The Global Journals Incorporation (USA) at its discretion can also refer double blind peer reviewed paper at their end to the board for the verification and to get recommendation for final stage of acceptance of publication.



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Journals Research
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The board members can also join us as Individual Fellow with 40% discount on total fees applicable to Individual Fellow. They will be entitled to avail all the benefits as declared. Please visit Individual Fellow-sub menu of GlobalJournals.org to have more relevant details.

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After nomination of your institution as “Institutional Fellow” and constantly functioning successfully for one year, we can consider giving recognition to your institute to function as Regional/Zonal office on our behalf.

The board can also take up the additional allied activities for betterment after our consultation.

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Open Association of Research Society, U.S.A (OARS) By-laws states that an individual Fellow may use the designations as applicable, or the corresponding initials. The Credentials of individual Fellow and Associate designations signify that the individual has gained knowledge of the fundamental concepts. One is magnanimous and proficient in an expertise course covering the professional code of conduct, and follows recognized standards of practice.



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Other:

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- The professional accredited with Fellow honor, is entitled to various benefits viz. name, fame, honor, regular flow of income, secured bright future, social status etc.



- In addition to above, if one is single author, then entitled to 40% discount on publishing research paper and can get 10% discount if one is co-author or main author among group of authors.
- The Fellow can organize symposium/seminar/conference on behalf of Global Journals Incorporation (USA) and he/she can also attend the same organized by other institutes on behalf of Global Journals.
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- Fellow can also join as paid peer reviewer and earn 15% remuneration of author charges and can also get an opportunity to join as member of the Editorial Board of Global Journals Incorporation (USA)
- • This individual has learned the basic methods of applying those concepts and techniques to common challenging situations. This individual has further demonstrated an in-depth understanding of the application of suitable techniques to a particular area of research practice.

Note :

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- In future, if the board feels the necessity to change any board member, the same can be done with the consent of the chairperson along with anyone board member without our approval.
- In case, the chairperson needs to be replaced then consent of 2/3rd board members are required and they are also required to jointly pass the resolution copy of which should be sent to us. In such case, it will be compulsory to obtain our approval before replacement.
- In case of “Difference of Opinion [if any]” among the Board members, our decision will be final and binding to everyone.

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PROCESS OF SUBMISSION OF RESEARCH PAPER

The Area or field of specialization may or may not be of any category as mentioned in 'Scope of Journal' menu of the GlobalJournals.org website. There are 37 Research Journal categorized with Six parental Journals GJCST, GJMR, GJRE, GJMBR, GJSFR, GJHSS. For Authors should prefer the mentioned categories. There are three widely used systems UDC, DDC and LCC. The details are available as 'Knowledge Abstract' at Home page. The major advantage of this coding is that, the research work will be exposed to and shared with all over the world as we are being abstracted and indexed worldwide.

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Author Guidelines:

1. General,
2. Ethical Guidelines,
3. Submission of Manuscripts,
4. Manuscript's Category,
5. Structure and Format of Manuscript,
6. After Acceptance.

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The recommended size of original research paper is less than seven thousand words, review papers fewer than seven thousands words also. Preparation of research paper or how to write research paper, are major hurdle, while writing manuscript. The research articles and research letters should be fewer than three thousand words, the structure original research paper; sometime review paper should be as follows:

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(a) Title should be relevant and commensurate with the theme of the paper.

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(e) Resources and techniques with sufficient complete experimental details (wherever possible by reference) to permit repetition; sources of information must be given and numerical methods must be specified by reference, unless non-standard.

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It is vital, that authors take care in submitting a manuscript that is written in simple language and adheres to published guidelines.

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Search engines for most searches, use Boolean searching, which is somewhat different from Internet searches. The Boolean search uses "operators," words (and, or, not, and near) that enable you to expand or narrow your affords. Tips for research paper while preparing research paper are very helpful guideline of research paper.

Choice of key words is first tool of tips to write research paper. Research paper writing is an art. A few tips for deciding as strategically as possible about keyword search:



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Keywords are the key that opens a door to research work sources. Keyword searching is an art in which researcher's skills are bound to improve with experience and time.

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24. Never copy others' work: Never copy others' work and give it your name because if evaluator has seen it anywhere you will be in trouble.

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27. Refresh your mind after intervals: Try to give rest to your mind by listening to soft music or by sleeping in intervals. This will also improve your memory.

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32. Never oversimplify everything: To add material in your research paper, never go for oversimplification. This will definitely irritate the evaluator. Be more or less specific. Also too, by no means, ever use rhythmic redundancies. Contractions aren't essential and shouldn't be there used. Comparisons are as terrible as clichés. Give up ampersands and abbreviations, and so on. Remove commas, that are, not necessary. Parenthetical words however should be together with this in commas. Understatement is all the time the complete best way to put onward earth-shaking thoughts. Give a detailed literary review.

33. Report concluded results: Use concluded results. From raw data, filter the results and then conclude your studies based on measurements and observations taken. Significant figures and appropriate number of decimal places should be used. Parenthetical remarks are prohibitive. Proofread carefully at final stage. In the end give outline to your arguments. Spot out perspectives of further study of this subject. Justify your conclusion by at the bottom of them with sufficient justifications and examples.

34. After conclusion: Once you have concluded your research, the next most important step is to present your findings. Presentation is extremely important as it is the definite medium through which your research is going to be in print to the rest of the crowd. Care should be taken to categorize your thoughts well and present them in a logical and neat manner. A good quality research paper format is essential because it serves to highlight your research paper and bring to light all necessary aspects in your research.

INFORMAL GUIDELINES OF RESEARCH PAPER WRITING

Key points to remember:

- Submit all work in its final form.
- Write your paper in the form, which is presented in the guidelines using the template.
- Please note the criterion for grading the final paper by peer-reviewers.

Final Points:

A purpose of organizing a research paper is to let people to interpret your effort selectively. The journal requires the following sections, submitted in the order listed, each section to start on a new page.

The introduction will be compiled from reference matter and will reflect the design processes or outline of basis that direct you to make study. As you will carry out the process of study, the method and process section will be constructed as like that. The result segment will show related statistics in nearly sequential order and will direct the reviewers next to the similar intellectual paths throughout the data that you took to carry out your study. The discussion section will provide understanding of the data and projections as to the implication of the results. The use of good quality references all through the paper will give the effort trustworthiness by representing an alertness of prior workings.



Writing a research paper is not an easy job no matter how trouble-free the actual research or concept. Practice, excellent preparation, and controlled record keeping are the only means to make straightforward the progression.

General style:

Specific editorial column necessities for compliance of a manuscript will always take over from directions in these general guidelines.

To make a paper clear

- Adhere to recommended page limits

Mistakes to evade

- Insertion a title at the foot of a page with the subsequent text on the next page
- Separating a table/chart or figure - impound each figure/table to a single page
- Submitting a manuscript with pages out of sequence

In every sections of your document

- Use standard writing style including articles ("a", "the," etc.)
- Keep on paying attention on the research topic of the paper
- Use paragraphs to split each significant point (excluding for the abstract)
- Align the primary line of each section
- Present your points in sound order
- Use present tense to report well accepted
- Use past tense to describe specific results
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- Shun use of extra pictures - include only those figures essential to presenting results

Title Page:

Choose a revealing title. It should be short. It should not have non-standard acronyms or abbreviations. It should not exceed two printed lines. It should include the name(s) and address (es) of all authors.



Abstract:

The summary should be two hundred words or less. It should briefly and clearly explain the key findings reported in the manuscript-- must have precise statistics. It should not have abnormal acronyms or abbreviations. It should be logical in itself. Shun citing references at this point.

An abstract is a brief distinct paragraph summary of finished work or work in development. In a minute or less a reviewer can be taught the foundation behind the study, common approach to the problem, relevant results, and significant conclusions or new questions.

Write your summary when your paper is completed because how can you write the summary of anything which is not yet written? Wealth of terminology is very essential in abstract. Yet, use comprehensive sentences and do not let go readability for briefness. You can maintain it succinct by phrasing sentences so that they provide more than lone rationale. The author can at this moment go straight to shortening the outcome. Sum up the study, with the subsequent elements in any summary. Try to maintain the initial two items to no more than one ruling each.

- Reason of the study - theory, overall issue, purpose
- Fundamental goal
- To the point depiction of the research
- Consequences, including definite statistics - if the consequences are quantitative in nature, account quantitative data; results of any numerical analysis should be reported
- Significant conclusions or questions that track from the research(es)

Approach:

- Single section, and succinct
- As a outline of job done, it is always written in past tense
- A conceptual should situate on its own, and not submit to any other part of the paper such as a form or table
- Center on shortening results - bound background information to a verdict or two, if completely necessary
- What you account in an conceptual must be regular with what you reported in the manuscript
- Exact spelling, clearness of sentences and phrases, and appropriate reporting of quantities (proper units, important statistics) are just as significant in an abstract as they are anywhere else

Introduction:

The **Introduction** should "introduce" the manuscript. The reviewer should be presented with sufficient background information to be capable to comprehend and calculate the purpose of your study without having to submit to other works. The basis for the study should be offered. Give most important references but shun difficult to make a comprehensive appraisal of the topic. In the introduction, describe the problem visibly. If the problem is not acknowledged in a logical, reasonable way, the reviewer will have no attention in your result. Speak in common terms about techniques used to explain the problem, if needed, but do not present any particulars about the protocols here. Following approach can create a valuable beginning:

- Explain the value (significance) of the study
- Shield the model - why did you employ this particular system or method? What is its compensation? You strength remark on its appropriateness from a abstract point of vision as well as point out sensible reasons for using it.
- Present a justification. Status your particular theory (es) or aim(s), and describe the logic that led you to choose them.
- Very for a short time explain the tentative propose and how it skilled the declared objectives.

Approach:

- Use past tense except for when referring to recognized facts. After all, the manuscript will be submitted after the entire job is done.
- Sort out your thoughts; manufacture one key point with every section. If you make the four points listed above, you will need a least of four paragraphs.



- Present surroundings information only as desirable in order hold up a situation. The reviewer does not desire to read the whole thing you know about a topic.
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Procedures (Methods and Materials):

This part is supposed to be the easiest to carve if you have good skills. A sound written Procedures segment allows a capable scientist to replacement your results. Present precise information about your supplies. The suppliers and clarity of reagents can be helpful bits of information. Present methods in sequential order but linked methodologies can be grouped as a segment. Be concise when relating the protocols. Attempt for the least amount of information that would permit another capable scientist to spare your outcome but be cautious that vital information is integrated. The use of subheadings is suggested and ought to be synchronized with the results section. When a technique is used that has been well described in another object, mention the specific item describing a way but draw the basic principle while stating the situation. The purpose is to text all particular resources and broad procedures, so that another person may use some or all of the methods in one more study or referee the scientific value of your work. It is not to be a step by step report of the whole thing you did, nor is a methods section a set of orders.

Materials:

- Explain materials individually only if the study is so complex that it saves liberty this way.
- Embrace particular materials, and any tools or provisions that are not frequently found in laboratories.
- Do not take in frequently found.
- If use of a definite type of tools.
- Materials may be reported in a part section or else they may be recognized along with your measures.

Methods:

- Report the method (not particulars of each process that engaged the same methodology)
- Describe the method entirely
- To be succinct, present methods under headings dedicated to specific dealings or groups of measures
- Simplify - details how procedures were completed not how they were exclusively performed on a particular day.
- If well known procedures were used, account the procedure by name, possibly with reference, and that's all.

Approach:

- It is embarrassed or not possible to use vigorous voice when documenting methods with no using first person, which would focus the reviewer's interest on the researcher rather than the job. As a result when script up the methods most authors use third person passive voice.
- Use standard style in this and in every other part of the paper - avoid familiar lists, and use full sentences.

What to keep away from

- Resources and methods are not a set of information.
- Skip all descriptive information and surroundings - save it for the argument.
- Leave out information that is immaterial to a third party.

Results:

The principle of a results segment is to present and demonstrate your conclusion. Create this part a entirely objective details of the outcome, and save all understanding for the discussion.

The page length of this segment is set by the sum and types of data to be reported. Carry on to be to the point, by means of statistics and tables, if suitable, to present consequences most efficiently. You must obviously differentiate material that would usually be incorporated in a study editorial from any unprocessed data or additional appendix matter that would not be available. In fact, such matter should not be submitted at all except requested by the instructor.



Content

- Sum up your conclusion in text and demonstrate them, if suitable, with figures and tables.
- In manuscript, explain each of your consequences, point the reader to remarks that are most appropriate.
- Present a background, such as by describing the question that was addressed by creation an exacting study.
- Explain results of control experiments and comprise remarks that are not accessible in a prescribed figure or table, if appropriate.
- Examine your data, then prepare the analyzed (transformed) data in the form of a figure (graph), table, or in manuscript form.

What to stay away from

- Do not discuss or infer your outcome, report surroundings information, or try to explain anything.
- Not at all, take in raw data or intermediate calculations in a research manuscript.
- Do not present the similar data more than once.
- Manuscript should complement any figures or tables, not duplicate the identical information.
- Never confuse figures with tables - there is a difference.

Approach

- As forever, use past tense when you submit to your results, and put the whole thing in a reasonable order.
- Put figures and tables, appropriately numbered, in order at the end of the report
- If you desire, you may place your figures and tables properly within the text of your results part.

Figures and tables

- If you put figures and tables at the end of the details, make certain that they are visibly distinguished from any attach appendix materials, such as raw facts
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- Make a decision if each premise is supported, discarded, or if you cannot make a conclusion with assurance. Do not just dismiss a study or part of a study as "uncertain."
- Research papers are not acknowledged if the work is imperfect. Draw what conclusions you can based upon the results that you have, and take care of the study as a finished work
- You may propose future guidelines, such as how the experiment might be personalized to accomplish a new idea.
- Give details all of your remarks as much as possible, focus on mechanisms.
- Make a decision if the tentative design sufficiently addressed the theory, and whether or not it was correctly restricted.
- Try to present substitute explanations if sensible alternatives be present.
- One research will not counter an overall question, so maintain the large picture in mind, where do you go next? The best studies unlock new avenues of study. What questions remain?
- Recommendations for detailed papers will offer supplementary suggestions.

Approach:

- When you refer to information, differentiate data generated by your own studies from available information
- Submit to work done by specific persons (including you) in past tense.
- Submit to generally acknowledged facts and main beliefs in present tense.



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<i>Introduction</i>	Containing all background details with clear goal and appropriate details, flow specification, no grammar and spelling mistake, well organized sentence and paragraph, reference cited	Unclear and confusing data, appropriate format, grammar and spelling errors with unorganized matter	Out of place depth and content, hazy format
<i>Methods and Procedures</i>	Clear and to the point with well arranged paragraph, precision and accuracy of facts and figures, well organized subheads	Difficult to comprehend with embarrassed text, too much explanation but completed	Incorrect and unorganized structure with hazy meaning
<i>Result</i>	Well organized, Clear and specific, Correct units with precision, correct data, well structuring of paragraph, no grammar and spelling mistake	Complete and embarrassed text, difficult to comprehend	Irregular format with wrong facts and figures
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<i>References</i>	Complete and correct format, well organized	Beside the point, Incomplete	Wrong format and structuring



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